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ANUBOSE INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi and Affiliated to JNTUH Hyderabad)

KSP Road, New Palvancha – 507115, Bhadradri-Kothagudem, Dist., Telangana State.

Website: www.abit.ac.in

Email Id: abitjntu@gmail.com

Cell: +91-9246907407

Date: 05-10-2018

CIRCULAR

This is to inform all the III B.Tech EEE students that the "*Program on Skill set Training on PLC and SCADA*" Program is commencing from 08-10-2018 to 12-10-2018 (5 Days).

All the interested students are instructed to register without fail.

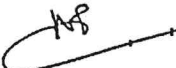
Timings: 9.00 am to 6.00 pm


Venue: Seminar Hall (Ground floor)

Circulated to: All Departments

Principal

EEE	ECE	CSE	MECH	MINING	CIVIL	MBA


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 ANUBOSE INSTITUTE OF TECHNOLOGY
 K.S.P Road, PALONCHA-507 115
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SKILL SET TRAINING PROGRAM ON PLC AND SCADA

(08-10-2018 to 12-10-2018)

ANUBOSE INSTITUTE OF TECHNOLOGY

Name :

Designation :

Department :

Affiliation /Organization :

Educational Qualification :

Postal Address :

Mobile Number

E-mail :

DD No & Date

Name of the Bank :

Place:

Date:

Signature of the applicant

(Signature of the organization head with seal)
[Photocopy of the Registration form may also be used for
additional Registration]

ORGANIZING COMMITTEE

Chief Patron

Mr. T. Bharat Krishna

Patrons

Dr. N. V. Subbarao

Principal, ANUBOSE INSTITUTE OF
TECHNOLOGY

Convener

Mr. V. Ashok

Head of the Department EEE.

Coordinator

DARAVATH SRINIVASA NAIK

Assistant Professor, EEE

For any enquiry contact:

Mobile No: 8008585001

HOD

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SKILL SET TRAINING PROGRAM
ON PLC AND SCADA
(08-10-2018 to 12-10-2018)



Organized by
Department of EEE

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About ABIT

Anubose Institute Of Technology (ABIT), located at Paloncha- an industrial hub in Bhadradi Kothagudem district, is sponsored by the National Educational Trust, Paloncha. The members of the trust are eminent personalities in various walks of life and dedicate themselves to the cause of education. In light of this, ABIT has been established in 2008 with special attention to the technical education and approved by AICTE, New Delhi and affiliated to the premier and world renowned Jawaharlal Nehru Technological University, Hyderabad.

The ABIT expands itself as "Always Best In Technical-education" by making use of the latest technologies and quality teaching. It lays emphasis on innovative research, investment in high-quality facilities and first-rate infrastructure

AnuBose Institute of Technology (ABIT) strives to make students confident and creative builders of their future. ABIT believes learning is maximized when it takes place in an environment enriched with support, encouragement and assistance. In ABIT we encourage the students to meet academic challenges with openness, enthusiasm, and a willingness to solve problems. We provide a rigorous, academic program through which effective educators lead students to take responsibility for learning. To raise itself as a centre of excellence through integrated educational, academic and industrial research and consultancy services. To actively participate in the development of technical manpower and physical infrastructure for

enhancing the country's credibility and stature. To undertake cooperative research in the industry for upgradation of current technologies and development of new ones, to improve quality and cost effectiveness, while participating in the ongoing process of globalization. To play a noble role for making significant contributions for social upliftment through technical education.

About Department

Electrical and Electronics Engineering deals with the innovation, design, development and industrial applications of electrical and electronics systems, components and devices. There is a wide variety of domains to which it applies. Among the most well-known fields, just to name a few are, Electrical Power Generation, Transmission and Distribution, Control Systems, Power Systems, Communications. The department of Electrical and Electronics Engineering (EEE) Program was started in the year of 2008 with an intake of 120 affiliated to JNTUH. The department has well equipped laboratories with standard quality equipment's. The department is catering to the needs of the industry by training students to make valuable contribution. The Department is endeavoring to produce highly trained and capable engineers, who can take up the challenges of the real world. The Department focuses on uncompromising academic standards in imparting both theoretical and practical instructions with a view to prepare professional engineers.

Resource Person

Dr. C. Hari Krishna

Professor and Principal, Mother Teresa Institute of Science and Technology

Program Objectives


1. The WIPO Academy is the centre of excellence for intellectual property (IP) education and training. The Academy helps to build human capacity in IP, which is essential to innovation.
2. It has been offering distance learning (DL) courses, covering all aspects of intellectual property (IP), to both professionals and students to increase the awareness among them.


Topics Covered

1. PLC basics and architectures, PLC-IEC 61131-3 programming languages & Visualization
2. Structured Programming
3. CPU and discrete input/output cards, Analog input/output cards and control loops
4. Introduction to SCADA

Program Outcome

1. Introduction of Skill Set Training Program on PLC and SCADA
2. Concepts and their applications in Industry


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Week/Day-wise Schedule

Name of the Program: *Skill set Training Program on PLC and SCADA*

S.NO	Day	Date	Topics to be covered
1.	Thursday	08-10-2018	PLC basics and architectures, PLC -IEC 61131-3 Programming Languages & Visualisation/HMI
2.	Friday	09-10-2018	Structured Programming (Functions & Functional blocks)
3.	Saturday	10-10-2018	CPU and Discrete input/output Cards, Analog input/output Cards and control loops
4.	Monday	11-10-2018	Introduction to SCADA, NI-Lab VIEW DSC (Data logging and Supervisory control) Software
5.	Tuesday	12-10-2018	SCADA- Network communication and communication with RTUs


D. Srinivas Naik

Program Coordinator


HOD-EEE



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PROGRAM REPORT

- Name of the Program : " One Week Program on “Skill Set Training Program on PLC and SCADA”
- Date/Duration : 08-10-2018 to 12-10-2018 5 days
- Resource Person : An Industrial Expert with vast experience
- Program Objective : The objective of the Skill Set Training Program on PLC (Programmable Logic Controller) and SCADA (Supervisory Control and Data Acquisition) is to provide participants with fundamental skills in these automation technologies. This program aims to impart practical knowledge and hands-on experience, enabling participants to operate, program, and troubleshoot PLCs and SCADA systems effectively. By the end of the training, participants will have a solid foundation to pursue careers in industrial automation and contribute to the efficient and safe operation of industrial processes.
- Topics covered : The following topics are covered
1. PLC basics and architectures, PLC -IEC 61131-3 Programming Languages & Visualisation/HMI
 2. Structured Programming (Functions& Functional blocks)
 3. CPU and Discrete input/output Cards, Analog input/output Cards and control loops
 4. Introduction to SCADA , NI-Lab VIEW DSC (Data logging and Supervisory control) Software
 5. SCADA- Network communication and communication with RTUs
- Program Outcome : By the end of the program, students would learn about
- Introduction of Skill Set Training Program on PLC and SCADA
 - Concepts and their applications in Industry.
- Name of the Coordinator: DARAVATH SRINIVASA NAIK, Assistant Professor, EEE.
- No of the Participants : 76

List of Registered Students

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Cell: +91-9246907407

Date: 06-03-2019

CIRCULAR

This is to inform all the IV B. Tech EEE students that the “*Program on Online Internship Training on Embedded system and IOT*” program is commencing from 11-03-2019 to 16-03-2019 (6 Days). All the interested students are instructed to register without fail.

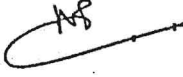
Timings: 09:00AM to 06:00PM


Venue: Seminar Hall (Ground floor)

Circulated to: All Departments

Principal

EEE	ECE	CSE	MECH	MINING	CIVIL	MBA


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ONLINE INTERNSHIP
TRAINING ON EMBEDDED
SYSTEM AND IOT

(11-03-2019 to 16-03-2019)

ANUBOSE INSTITUTE OF
TECHNOLOGY

Name :

Designation :

Department :

Affiliation /Organization :

Educational Qualification :

Postal Address :

Mobile Number

E-mail :

DD No & Date

Name of the Bank :

Place:

Date:

Signature of the applicant

(Signature of the organization head with seal)
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ORGANIZING COMMITTEE

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Mr. T. Bharat Krishna

Patrons

Dr. N. V. Subbarao
Principal, ANUBOSE INSTITUTE OF
TECHNOLOGY

Convener

Mr. V. Ashok
Head of the Department EEE.

Coordinator

Mr. Devavarapu Satyanarayana
Assistant Professor, EEE

For any enquiry contact:

Mobile No: 8328670245

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ONLINE INTERNSHIP TRAINING
ON EMBEDDED SYSTEM AND IOT
(11-03-2019 to 16-03-2019)



Organized by
Department of EEE

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Bhadradri Kothagudem Dt, Telangana.

Principal
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Resource Person

Dr. T. Charan Singh

Associate Professor & HOD-EEE

Swarna Bharathi Institute of Technology,
Khammam

Program Objectives

1. The WIPO Academy is the centre of excellence for intellectual property (IP) education and training. The Academy helps to build human capacity in IP, which is essential to innovation.
2. The WIPO Academy has been offering distance learning (DL) courses, covering all aspects of intellectual property (IP), to both professionals and students to increase the awareness among them.


Topics Covered

1. Embedded C programming Overview
2. Interfacing LED, Button, IR sensor with esp32 Board.
3. Interfacing the DHT11 sensor, Flame Sensor with esp32 Board.
4. ADC Concept Explanation and Initialization
5. Hands-on: Interfacing the Soil Moisture Sensor with esp32 Board.

Program Outcome

Enhance the knowledge of students on Embedded system and IOT


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Cell: +91-9246907407

Week/Day-wise Schedule

Name of the Program: Online Internship Training on Embedded system and IOT

S.NO	Day	Date	Topics to be covered
1.	Monday	11-03-2019	Embedded C programming Overview
2.	Tuesday	12-03-2019	Interfacing LED, Button, IR sensor with esp32 Board
3.	Wednesday	13-03-2019	Interfacing the DHT11 sensor, Flame Sensor with esp32 Board
4.	Thursday	14-03-2019	ADC Concept Explanation and Initialization
5.	Friday	15-03-2019	Hands-on: Interfacing the Soil Moisture Sensor with esp32 Board
6.	Saturday	16-03-2019	Introduction to DC motor and Servo motor, Interfacing the DC motor esp32 Board

Dr. Satyenuarayana
Program Coordinator

[Signature]
HOD-EEE

[Signature]
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Email Id: abijtntu@gmail.com

Cell: +91-9246907407

PROGRAM REPORT

- .Name of the Program : " One Week Program on Online Internship Training on Embedded system and IOT"
- Date/Duration : 11-03-2019 TO 16-03-2019, 6 days
- Resource Person : Dr. T. Charan Singh
Assoc Professor & HOD – EEE
Mother Terese Institute of Science & Technology, Sathupally.
- Program Objective : The objective of the Online Internship Training on Embedded Systems and IoT is to offer participants a practical learning experience in these cutting-edge technologies. This program aims to equip interns with essential skills in embedded systems design and IoT application development. Through hands-on projects and mentorship, participants will gain the knowledge and confidence to excel in the field of embedded systems and IoT, preparing them for future roles in the industry.
- Topics covered : The following topics are covered
1. Embedded C programming Overview
 2. Interfacing LED, Button, IR sensor with esp32 Board.
 3. : Interfacing the DHT11 sensor, Flame Sensor with esp32 Board.
 4. ADC Concept Explanation and Initialization
 5. Hands-on: Interfacing the Soil Moisture Sensor with esp32 Board.
 6. Serial Communication Protocol-SPI,UART, I2C
 7. Introduction to DC motor and Servo motor, Interfacing the DC motor esp32 Board
 8. Bluetooth Communication
- Program Outcome : By the end of the program, students would learn about
Enhance the knowledge of students in Embedded system
Learn more about IOT
- No of the Participants : 101


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Estd: 2008

JNTUH College Code: PP

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DATE: 15/08/2018

CIRCULAR

It is hereby informed to all the students of Mechanical Engineering, that " A One Week Certificate Program on DESIGNING OF SOLAR ENERGY PV SYSTEM" is going to be conducted from 20/08/2018 to 25/08/2018. All the interested students are requested to register their names in the department office on or before 19/08/2018. The details of the program will be available in the brochure.

All the interested students must attend the certificate program without fail on the abovementioned dates.

Timings: 09:00AM to 05:00PM

Venue: Seminar Hall (Ground floor)

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Circulated to: All Departments

EEE	ECE	CSE	MECH	MINING	S&H	MBA	CIVIL

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DESIGNING OF SOLAR ENERGY PV
SYSTEM

(20-08-2018 to 25-08-2018)

ANUBOSE INSTITUTE OF
TECHNOLOGY

Name :

Designation :

Department :

Affiliation /Organization :

Educational Qualification :

Postal Address :

Mobile Number

E-mail :

DD No & Date

Name of the Bank :

Place:

Date:

Signature of the applicant

(Signature of the organization head with seal)
[Photocopy of the Registration form may also be used for
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ORGANIZING COMMITTEE

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Dr.N. V. Subbarao

Principal,ANUBOSE INSTITUTE OF
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Convener

Mrs.K.NAGENDRA BABU

Head of the Dep. MECH

Coordinator

Mrs. P.SUDHER KUMAR

Assistant Professor, MECH

For any enquiry contact:

Mobile No: 8919640884

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DESIGNING OF SOLAR ENERGY PV
SYSTEM

(20-08-2018 to 25-08-2018)



Organized by
Department of MECH

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technical manpower and physical infrastructure for enhancing the country's credibility and stature. To undertake cooperative research in the industry for upgradation of current technologies and development of new ones, to improve quality and cost effectiveness, while participating in the ongoing process of globalization. To play a noble role for making significant contributions for social upliftment through technical education.

About Department

The department of mechanical Engineering was established in the year 2009 with initial intake of 60 students. Within a short span of time, seeing infrastructure available and capabilities of faculties of the RPS group, The undergraduate and post graduate programs of the department mechanical engineering impart solid technical background and research capabilities among the students which help them to excel in their respective fields. It has an array of courses for students to choose and study such as machine tools, production, thermal engineering, fem hardware and software, automation, robotics and control, fluid mechanics, automobile applications and designs.

We put all our efforts to make our students to compete internationally and to produce creative solutions to society's needs, universal moral values, adherent to professional ethical code, and to generate any knowledge and technologies essential to the local and global needs in the field of mechanical engineering.

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Bhadradi Kothagudem Dist.

Resource Person

B.VENU,
Senior lecturer, mechanical, govt. polytechnic, rudrampur

Cell no.-9985024682

Program Objectives

Identify and measuring V - I characteristics of a solar Photovoltaic Module, Series and Parallel connections of a Photovoltaic Module, Effect of Sun Tracking on Energy Generation

a) Tilt Angles and b) Azimuth angles
Topics Covered

Present Energy Scenario in India.

Introduction to Solar PV.

Components of Solar PV System.

Designing Standalone System.

Financial Analysis.

Practical Session.

Program Outcome

Differentiate between various renewable energy sources

Can Design and install a solar PV system for his home.

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KSP Road, New Palvancha – 507115, Bhadradri-Kothagudem, Dist., Telangana State.

Website: www.abit.ac.in

Email Id: abitjntu@gmail.com

Cell: +91-9246907407

Date: 20/08/2018

Week/Day-wise Schedule


Name of the Program: "A One week certificate Program on "DESIGNING OF SOLAR ENERGY PV SYSTEM


DURATION: 20/08/2018 to 25/08/2018 (1 week)

S.NO	Day	Date	Topics to be covered
1.	Monday	20/08/2018	Present Energy Scenario in India.
2.	Tuesday	21/08/2018	Introduction to Solar PV.
3.	Wednesday	22/08/2018	Components of Solar PV System.
4.	Thursday	23/08/2018	Designing Standalone System.
5.	Friday	24/08/2018	Financial Analysis.
6.	Saturday	25/08/2018	Practical Session.


Program Coordinator


Head of the Department
Department of Mechanical Engineering
Anubose Institute of Technology
Paloncha, Bhadradri Kothagudem Dist., T.S.


PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.


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K.S.P Road, Palvoncha,
Bhadradri Kothagudem Dt, Telangana.

Counselling Code: BOSE

Estd: 2008

JNTUH College Code: PP

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PROGRAM REPORT

Date: 20/08/2018

Name of the Program : "A One Week Certificate Program on **DESIGNING OF SOLAR ENERGY PV SYSTEM**"

Date/Duration : AUGUST 20th -25th 2018, 6days

Resource Person : B.VENU ,senior lecturer, mechanical, govt. polytechnic, rudrampur ,cell no.9985024682

Program Objective : The objective of this course is

- Identify and measuring V - I characteristics of a solar Photovoltaic Module
- Series and Parallel connections of a Photovoltaic Module
- Effect of Sun Tracking on Energy Generation

a) Tilt Angles and b) Azimuth angles

Topics covered : The following topics are covered

- present Energy Scenario in India.
- Introduction to Solar PV.
- Components of Solar PV System.
- Designing Standalone System.
- Financial Analysis.
- Practical Session.


Program Outcome : By the end of the program, students would

- Differentiate between various renewable energy sources
- Can Design and install a solar PV system for his home.


Name of the Coordinator : Mr. P.SUDHEER, Assistant Professor, MECH.

No of the Participants : 60


Program Coordinator


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Bhadradri Kothagudem Dist.


Head of Department
Department of Mechanical Engineering
Anubose Institute of Technology
Paloncha, Bhadradri Kothagudem Dist., T.S.


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Cell: +91-9246907407

DATE: 01-02-2019


CIRCULAR

It is hereby informed to all the students of Mechanical Engineering, that "A One -Week Certificate Program on **ADVANCED FINITE ELEMENT ANALYSIS**" is going to be conducted from 04/02/2019 to 09/02/2019. All the interested students are requested to register their names in the department office on or before Of 03-02-2019. The details of the program will be available in the brochure.









All the interested students must attend the certificate program without fail on the abovementioned dates.


Timings: 09:00AM to 05:00PM

Venue: Seminar Hall (Ground floor)


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Bhadradri Kothagudem Dist

Circulated to: All Departments

EEE	ECE	CSE	MECH	MINING	S&H	MBA	CIVIL
							


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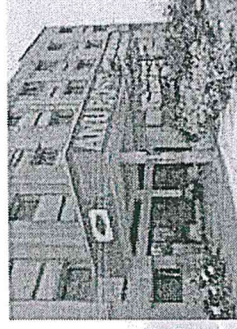
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dist., Telangana

ADVANCED FINITE ELEMENT ANALYSIS

(04-02-2019 to 09-02-2019)



Organized by
Department of MECH

MS

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K.S.P Road, Palvoncha,
Bhadradi Kothagudem Dt, Telangana.

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Patrons

Dr.N. V. Subbarao

Principal, ANUBOSE INSTITUTE OF TECHNOLOGY

Convener

Mrs.K.NAGENDRA BABU

Head of the Dep, MECH

Coordinator

Mrs. P.SUDHER KUMAR

Assistant Professor, MECH

For any enquiry contact:

Mobile No: 8919640884

HOD

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Bhadradi Kothagudem Dist

ADVANCED FINITE ELEMENT ANALYSIS

(04-02-2019 to 09-02-2019)

ANUBOSE INSTITUTE OF TECHNOLOGY

Name :

Designation :

Department :

Affiliation /Organization :

Educational Qualification :

Postal Address :

Mobile Number

E-mail :

DD No & Date

Name of the Bank :

Place:

Date:

Signature of the applicant

(Signature of the organization head with seal)
[Photocopy of the Registration form may also be used for
additional Registration]

About ABIT

AnuBose Institute Of Technology (ABIT), located at Paloncha - an industrial hub in Bhadradi Kothagudem district, is sponsored by the National Educational Trust, Paloncha. The members of the trust are eminent personalities in various walks of life and dedicate their selves to the cause of education. In light of this, ABIT has been established in 2008 with special attention to the technical education and approved by AICTE, New Delhi and affiliated to the premier and world renowned Jawaharlal Nehru Technological University, Hyderabad.

The ABIT expands itself as "Always Best In Technical-education" by making use of the latest technologies and quality teaching. It lays emphasis on innovative research, investment in high-quality facilities and first-rate infrastructure

AnuBose Institute of Technology (ABIT) strives to make students confident and creative builders of their future. ABIT believes learning is maximized when it takes place in an environment enriched with support, encouragement and assistance. In ABIT we encourage the students to meet academic challenges with openness, enthusiasm, and a willingness to solve problems. We provide a rigorous, academic program through which effective educators lead students to take responsibility for learning. To raise itself as a centre of excellence through integrated educational, academic and industrial research and consultancy services. To actively participate in the development of technical manpower and physical infrastructure for

enhancing the country's credibility and stature. To undertake cooperative research in the industry for upgradation of current technologies and development of new ones, to improve quality and cost effectiveness, while participating in the ongoing process of globalization. To play a noble role for making significant contributions for social upliftment through technical education.

About Department

The department of mechanical Engineering was established in the year 2009 with initial intake of 60 students. Within a short span of time, seeing infrastructure available and capabilities of faculties of the RPS group, The undergraduate and post graduate programs of the department mechanical engineering impart solid technical background and research capabilities among the students which help them to excel in their respective fields. It has an array of courses for students to choose and study such as machine tools, production, thermal engineering, fem hardware and software, automation, robotics and control, fluid mechanics, automobile applications and designs.

We put all our efforts to make our students to compete internationally and to produce creative solutions to society's needs, universal moral values, adherent to professional ethical code, and to generate any knowledge and technologies essential to the local and global needs in the field of mechanical engineering.


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AnuBose Institute of Technology
KSP Road PALVANCHA,
Bhadradi Kothagudem Dist.

Resource Person

P. Ramulu,
HOD, MECH, RUDRUMPUR,
GOVT. POLYTECHNIC, CELL NO-9014330947

Program Objectives

FEM applications include stress analysis, slope stability analysis, soil structure interactions.

Seepage of fluids in soils and rocks, analysis of dams, tunnels, bore holes, propagation of stress waves and dynamic soil structure interaction.

Topics Covered

Introduction to Advanced finite element analysis

Structural analysis and heat transfer

Fluid flow and mass transport

Electromagnetic potential

Program Outcome

Enhance the knowledge of students in FEM.
Learn more about Software packages.

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Website: www.abit.ac.in

Email Id: abitjntu@gmail.com

Cell: +91-9246907407

Date: 04/02/2019

Week/Day-wise Schedule

Name of the Program: A One Week Certificate Program on "ADVANCED FINITE ELEMENT ANALYSIS".

Duration: 04/02/2019 to 09/02/2019

S.NO	Day	Date	Topics to be covered
1.	Monday	04/02/2019	Introduction to Advanced finite element analysis
2.	Tuesday	05/02/2019	Structural analysis and heat transfer
3.	Wednesday	06/02/2019	Fluid flow and mass transport
4.	Thursday	07/02/2019	Electromagnetic potential
5.	Friday	08/02/2019	Survey of algorithms
6.	Saturday	09/02/2019	Implicit and Explicit Methods

Program Coordinator

HOD

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Head of the Department
Department of Mechanical Engineering
Anubose Institute of Technology
Paloncha, Bhadradri Kothagudem Dist., T.S.

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PROGRAM REPORT

Dates: 04/02/2019

Name of the Program : "A One Week Certificate program on ADVANCED FINITE ELEMENT ANALYSIS".

Date/Duration : FEB04th -09th, 2019, 6 days

Resource Person : P.Ramulu, HOD, MECH,
RUDRUMPUR, GOVT. POLYTECHNIC,
CELL NO-9014330947

Program Objective : The objective of this course is

- ❖ FEM applications include stress analysis, slope stability analysis, soil structure interactions.
- ❖ Seepage of fluids in soils and rocks, analysis of dams, tunnels, bore holes, propagation of stress waves and dynamic soil structure interaction.

Topics covered : The following topics are covered

- Introduction to Advanced finite element analysis
- Structural analysis and heat transfer
- Fluid flow and mass transport
- Electromagnetic potential

Program Outcome : By the end of the program, students would

- ❖ Enhance the knowledge of students in FEM
- ❖ Learn more about Software packages.

Name of the Co-ordinator: Mr. K.NAGENDRA BABU, Assistant Professor, MECH.

No of the Participants : 60

Program Coordinator

HOD

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Date: 2/10/2018

CIRCULAR

It is hereby informed to all the students of Computer science and Engineering, that








“A Week Work Shop on Cyber Security ” is going to be conducted from 08-10-2018 to 13-10-2018. All the interested students are requested to register their names in the department office on or before 08/10/2018. The details of the program will be available in the brochure.

All the interested students must attend the certificate program without fail on the abovementioned dates.


Timings: 03:00PM to 06:00PM

Venue: Seminar Hall (Ground floor)

Circulated to: All Departments

EEE	ECE	CSE	MECH	MINING	S&H	MBA
						


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Website: www.abit.ac.in

Email Id: abitjntu@gmail.com

Cell: +91-9246907407



Week/Day-wise Schedule

Name of the Program: "A Week Work Shop on Cyber Security".

S.NO	Day	Date	Topics to be covered
1.	Monday	08-10-2018	Packet sniffing
2.	Tuesday	09-10-2018	Key logging
3.	Wednesday	10-10-2018	Bug bounties
4.	Thursday	11-10-2018	RFID Blocking
5.	Friday	12-10-2018	Web Application firewall
6.	Saturday	13-10-2018	Web Scraper


Program Coordinator


HOD-CSE



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Email Id: abitjntu@gmail.com

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PROGRAM REPORT

Name of the Program : "A One Week Work Shop on A workshop on cyber security"

Date/Duration : October 8th -13th,6days

Resource Person : Dr,A.S.N.Chakravarthy,Professor JNTU Kakinada .

Program Objective : The objective of this course is

- ❖. The benefits of implementing and maintaining cyber security practices include:
- ❖. Business protection against cyber attacks and data breaches. Protection for data and networks. Prevention of unauthorized user access.

Topics covered : The following topics are covered

- Packet sniffing
- Key logging
- Bug bounties
- RFID Blocking
- Web Application firewall
- Web Scraper

Program Outcome : By the end of the program, students would

- ❖ Enhance the knowledge of students in - Introduction to Cyber Security
- ❖ Learn more about , MATLAB Software.


Name of the Coordinator : Mrs.K.Sowmya Assistant Professor, CSE.

No of the Participants : 49


Program Coordinator


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Email Id: abitjntu@gmail.com

Cell: +91-9246907407

Date: 21/3/2019

CIRCULAR


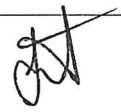





It is hereby informed to all the students of Computer Science and Engineering that "A One -Week Work Shop on programming skills for all" is going to be conducted from 25-03-2019 to. 30-03-2019. All the interested students are requested to register their names in the department office on or before 25-03-2019. The details of the program will be available in the brochure.


All the interested students must attend the certificate program without fail on the abovementioned dates.

Timings: 03:00PM to 06:00PM

Venue: Seminar Hall (Ground floor)

Circulated to: All Departments

EEE	ECE	CSE	MECH	MINING	S&H	MBA
						


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Email Id: abitjntu@gmail.com

Cell: +91-9246907407


Week/Day-wise Schedule


Name of the Program: " One -Week Work Shop on programming skills for all "

S.NO	Day	Date	Topics to be covered
1.	Monday	25-3-2019	Accountability.
2.	Tuesday	26-3-2019	Administrative Excellence.
3.	Wednesday	27-3-2019	Assertive Verbal Skills.
4.	Thursday	28-3-2019	Becoming a Great Leader.
5.	Friday	29-3-2019	Brand Management.
6.	Saturday	30-3-2019	Business Basics.


Program Coordinator


HOD-CSE


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Website: www.abit.ac.in

Email Id: abitjntu@gmail.com

Cell: +91-9246907407

PROGRAM REPORT

- Name of the Program : "A workshop on programming skills for all".
- Date/Duration : March 25th to 30th march 2019, 6 days
- Resource Person : Dr.R.Rajeshwar Rao Professor JNTU Vijawada
- Program Objective : The objective of this course is
- ❖ Programming skills are what we use to tell a computer, application, or software what to do.
 - ❖ These skills are applicable in various career paths — from software engineering to data analytics — and, arguably, even a wider variety of industries.
- Topics covered : The following topics are covered
- . Accountability.
 - Administrative Excellence
 - Assertive Verbal Skills.
 - Becoming a Great Leader
 - Brand Management
 - Business Basics.
- Program Outcome : By the end of the program, students would
- ❖ Enhance the knowledge of students in programming skills

Name of the Co-ordinator: Mr. I.Kranthi, Assistant Professor, CSE

No of the Participants : 120

Program Coordinator

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Email Id: abitjntu@gmail.com

Cell: +91-9246907407

CIRCULAR

It is hereby informed to all the students of Civil Engineering, that "A One Week Certificate Program on Digital Construction Technology " is going to be conducted from 10/09/2018 to 15/09/2018. All the interested students are requested to register their names in the department office on or before 07/09/2018. The details of the program will be available in the brochure.

All the interested students must attend the certificate program without fail on the abovementioned dates.

Timings: 09:00AM to 06:00PM

Venue: Seminar Hall (Ground floor)

Circulated to: All Departments

EEE	ECE	CSE	MECH	MINING	S&H	MBA	CIVIL

Program Coordinator

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Email Id: abitjntu@gmail.com

Cell: +91-9246907407

Week/Day-wise Schedule


Name of the Program: "A One-week certificate Program on Digital Construction Technology"


DURATION: 10/09/2018 TO 15/09/2018

S.no	Day	Date	Topics to be covered
1.	Monday	10/09/2018	Introduction to Digital Construction Technology and Introduction to 3D Printing
2.	Tuesday	11/09/2018	Techniques used and Software used in 3D Printing
3.	Wednesday	12/09/2018	History of 3D Printing
4.	Thursday	13/09/2018	Industrial applications of 3D Printing
5.	Friday	14/09/2018	Legal aspects and Problems faced in advancement of 3D Printing
6.	Saturday	15/09/2018	3D Printing services in India


Program Coordinator


HOD


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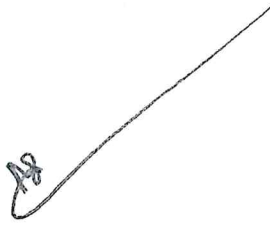
Email Id: abitjntu@gmail.com

Cell: +91-9246907407


PROGRAM REPORT

- Name of the Program: "A One-Week Certificate program on Digital Construction Technology"
- Date/Duration : 10/09/2018 to 15/09/2018
- Resource Person : Dr. R. SURESH
Professor in CIVIL
E- Mail ID: rangasuresh307@gmail.com, Cell: 9177538131.
- Program Objective : The objective of this course is
- ❖ The purpose of digital construction is to improve the overall working environment for different project stages.
 - ❖ Improving collaboration boosting efficiency create a safer environment and so on.
- Topics covered : The following topics are covered
- 3D Printing history
 - Techniques used: BIM
 - Industrial applications
 - Benefits and disadvantages
 - Services available in INDIA
- Program Outcome : By the end of the program, students would
- ❖ Enhance the knowledge of students in Digital construction Technology.
 - ❖ Learn more about 3D Printing.
 - ❖ Overcome the Problems faced in advancement of 3DPrinting
- Name of the Coordinator: MR.N. TRINADH Assistant Professor in CIVIL
- No of the Participants: 74


Program Coordinator


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Website: www.abit.ac.in

Email Id: abitintu@gmail.com

Cell: +91-9246907407

CIRCULAR

It is hereby informed to all the students of Civil Engineering that A One -Week Certificate Program on Green Buildings” is going to be conducted from 11/02/2019 TO 16/02/2019. All the interested students are requested to register their names in the department office on or before 10/02/2019. The details of the program will be available in the brochure.

All the interested students must attend the certificate program without fail on the above-mentioned dates.

Timings: 09:00AM to 06:00PM

Venue: Seminar Hall (Ground floor)

Circulated to: All Departments

EEE	ECE	CSE	MECH	MINING	S&H	MBA	CIVIL

Program Coordinator

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Website: www.abit.ac.in

Email Id: abitjntu@gmail.com

Cell: +91-9246907407

Week/Day-wise Schedule

Name of the Program: "A One-week certificate Program on Green Buildings"

DURATION: 11/02/2019 TO 16/02/2019

S. NO	Day	Date	Topics to be covered
1.	Monday	11/02/2019	About Green Buildings and why green construction is important to the environment
2.	Tuesday	12/02/2019	Green Building Technologies like exterior, interior, site solutions
3.	Wednesday	13/02/2019	Elements, Objectives, Importance of Green Buildings
4.	Thursday	14/02/2019	Factors considered in the design and Economical aspects of green buildings
5.	Friday	15/02/2019	Services for green buildings
6.	Saturday	16/02/2019	Regulation and operation and conclusion

Program Coordinator

HOD-CIVIL

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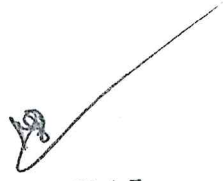
Email Id: abitjntu@gmail.com

Cell: +91-9246907407


PROGRAM REPORT

Name of the Program	:	"A One-Week Certificate program on Green Buildings
Date/Duration	:	11/02/2019 TO 16/02/2019
Resource Person:		SHIVARAMPRASAD Professor in CIVIL E- Mail ID: cv@gmail.com, Cell: 9948035362.
Program Objective	:	The objective of this course is <ul style="list-style-type: none"> ❖ Conversation energy resources ❖ Economic Benefits
Topics covered	:	The following topics are covered <ul style="list-style-type: none"> ● Introduction to green buildings. ● Green materials, Technologies ● Importance and need of green buildings. ● Infrastructure and design used in green buildings.
Program Outcome	:	By the end of the program, students would <ul style="list-style-type: none"> ❖ Understand the concept of green housing ❖ Economical sustainability ❖ Usage of locally available materials.
Name of the Coordinator	:	Mr. KISHORE Assistant Professor in CIVIL
No of the Participants	:	74


Program Coordinator


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DATE: 08/11/2018

CIRCULAR








This is to inform all the Master of Business Administration, that " A One Week Certificate Program on "LABOUR LAWS AND PRACTICES " is going to be conducted from 12/11/2018 to 17/11/2018 . All the interested students are instructed to register without fail.

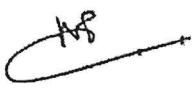
Timings: 09:00AM to 06:00PM


Venue: Seminar Hall (Ground floor)

Circulated to: All Departments

Principal

EEE	ECE	CSE	MECH	MINING	CIVIL	MBA
						


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About ABIT

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The ABIT expands itself as "Always Best In Technical-education" by making use of the latest technologies and quality teaching. It lays emphasis on innovative research, investment in high-quality facilities and first-rate infrastructure

AnuBose Institute of Technology (ABIT) strives to make students confident and creative builders of their future. ABIT believes learning is maximized when it takes place in an environment enriched with support, encouragement and assistance. In ABIT we encourage the students to meet academic challenges with openness, enthusiasm, and a willingness to solve problems. We provide a rigorous, academic program through which effective educators lead students to take responsibility for learning. To raise itself as a centre of excellence through integrated educational, academic and industrial research and consultancy services. To actively participate in the development of

technical manpower and physical infrastructure for enhancing the country's credibility and stature. To undertake cooperative research in the industry for upgradation of current technologies and development of new ones, to improve quality and cost effectiveness, while participating in the ongoing process of globalization. To play a noble role for making significant contributions for social upliftment through technical education.

About Department

The Department of Master of Business Administration was established in the year 2009. The Department offers a Post-Graduate Degree in the fields of Marketing, Finance & Human Resource Management. It consistently maintains high academic standards. This was mainly achieved by the collective and responsive effort of the Dedicated Management, faculty and the students. The department provides right atmosphere for the students to develop their personal and professional skills. The students are evaluated continuously on various dimensions. Learning is facilitated through a mix of classroom teaching, seminars, case studies, interaction with industry and society, business quizzes, group discussions, etc. The Department is equipped with well qualified and experienced faculty members to improve the student's managerial skills and professional attitude in order to make them to convert challenges into profitable opportunities in their professional career.

Resource Person

Dr.D.N.V. Krishna Reddy, Assistant Professor, Department of MBA

Sai Spoorthi Institute of Technology, Sathupally

Program Objectives

1. Introduction and understanding various labour laws.
2. Understanding minimum wages in a company.

Topics Covered

1. labour laws
2. including minimum wages
3. safety and health regulations

Program Outcome

understanding various labour laws including minimum wages

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Week / Day -wise schedule


Name of the program: One week certificate program on “Labour Laws and Practices”.

S. No	Date	Day	Topics covered
1	12-11-2018	Monday	Introduction and understanding various labour laws
2	13-11-2018	Tuesday	Understanding minimum wages in a company
3	14-11-2018	Wednesday	Understanding working hours in a company
4	15-11-2018	Thursday	Understanding employee benefits in a company
5	16-11-2018	Friday	Understanding safety and health regulations in a company
6	17-11-2018	Saturday	Understanding healthy employee and employer relationship


Program Coordinator


HOD-MBA

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Cell: +91-9246907407


PROGRAM REPORT


Name of the Program: " A One Week Certificate Program on "Labour Laws and ractices"

- Date/Duration : November 12th -17th November 2018, 1week
- Resource Person : Dr.D.N.V. Krishna Reddy, Assistant Professor, Department of MBA
Sai Spoorthi Institute of Technology, Sathupally.
E-mail:- drdnvkreddy@gmail.com
- Program Objective : The objective of this course is
❖ Introduction and understanding various labour laws
❖ Understanding minimum wages in a company
- Topics covered : The following topics are covered
- labour laws
 - including minimum wages,
 - working hours,
 - employee benefits,
 - safety and health regulations
- Program Outcome : By the end of the program, students would
❖ understanding various labour laws including minimum wages, working hours, employee benefits, safety and health regulations
- Name of the Coordinator : Mrs. M.Sumalatha (Assistant Professor), Department of MBA
- No of the Participants : 30


Program Coordinator


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Date: 23-09-2018

CIRCULAR

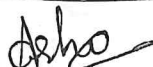






This is to inform all the III B.Tech ECE students that the "*A One-Week Certificate Program on FPGA Applications in Digital Circuits*" Program is commencing from 24-09-2018 to 29-09-2018 (6 Days). All the interested students are instructed to register without fail.


Timings: 09:00AM to 06:00PM

Venue: Seminar Hall (Ground floor)

Circulated to: All Departments


Principal

EEE	ECE	CSE	MECH	MINING	S&H	MI
						


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WEEK CERTIFICATE FPGA APPLICATIONS IN DIGITAL CIRCUITS

(24/09/2018 to 29/09/2018)

ANUBOSE INSTITUTE OF TECHNOLOGY

Name :

Designation :

Department :

Affiliation /Organization :

Educational Qualification :

Postal Address :

Mobile Number

E-mail :

DD No & Date

Name of the Bank :

Place:

Date:

Signature of the applicant

(Signature of the organization head with seal)
[Photocopy of the Registration form may also be used for
additional Registration]

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Mr. T. Bharat Krishna

Patrons

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Principal, ANUBOSE INSTITUTE OF
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Convener

Mrs. K. Vasavi
Head of the Dep, ECE

Coordinator

Mr. G. Venkateswarlu
Assistant Professor, ECE

For any enquiry contact:

Mobile No: 95125895625

HOD

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dist., Telangana

WEEK CERTIFICATE FPGA APPLICATIONS IN DIGITAL CIRCUITS

(24/09/2018 to 29/09/2018)



Organized by
Department of ECE

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About Department

The department of Electronics & Communication Engineering was established in the year 2008 with initial intake of 60 students. Within a short span of time, seeing infrastructure available and capabilities of faculties of the RPS group, intake has been doubled to 120. The undergraduate and post graduate programs of the department of Electronics and Communication impart solid technical background and research capabilities among the students which help them to excel in their respective fields. It has an array of courses for students to choose and study such as power electronics, information technology, electronic hardware and software, automation, robotics and control, communications, radio and television, computer engineering, solid-state devices, microelectronics, VLSI and other circuits & systems used in varying application ranging from simple circuits to sophisticated digital satellite systems, GSM & CDMA technology, speech processing & computer development.

We put all our efforts to make our students to compete internationally and to produce creative solutions to society's needs, universal moral values, adherent to professional ethical code, and to generate any knowledge and technologies essential to the local

and global needs in the field of Electronics & Communication.

Resource Person

J.V PRABHAKER RAO ASSOCIATE PROFESSOR

KLR College of Engineering and Technology

PALWANCHA

Program Objectives

1. A field-programmable gate array (FPGA) is a reconfigurable integrated circuit (IC) that lets you implement a wide range of custom digital circuits.

Topics Covered

1. Introduction to VLSI and FPGA
2. Introduction to Digital Circuits

Program Outcome

1. Enhance the knowledge of students in - Introduction to HDL Programming

MB

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Cell: +91-9246907407

Date: 22/09/2018

Week/Day-wise Schedule

Name of the Program: "A One week certificate Program on FPGA Applications in Digital circuits"

S.NO	Day	Date	Topics to be covered
1.	Monday	24-09-2018	Introduction to VLSI and FPGA
2.	Tuesday	25-09-2018	Introduction to Digital Circuits
3.	Wednesday	26-09-2018	Use of Digital Circuits Which are formed by Logic gates, Flip flops, and all other digital components
4.	Thursday	27-09-2018	Basic Knowledge on Chip Designing
5.	Friday	28-09-2018	HDL Programming: Introduction
6.	Saturday	29-09-2018	Programming and execution in Structural model and Behavioural model



Program Coordinator



HOD-ECE

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PROGRAM REPORT

Name of the Program : “FPGA Applications in Digital circuits”

Date/Duration : September 24th -29th of 2018, 1 week

Resource Person : J. V PRABHAKER RAO ASSOCIATE PROFESSOR

KLR College of Engineering and Technology

Program Objective : The objective of this course is

- A field-programmable gate array (FPGA) is a reconfigurable integrated circuit (IC) that lets you implement a wide range of custom digital circuits
- These custom digital circuits can be used to create optimized circuits for things like digital signal processing (DSP), machine learning, and crypto currency mining.
- They are found in many consumer electronics as well as on specialized equipment, such as satellites and communication equipment.

Topics covered : The following topics are covered

- Introduction to VLSI and FPGA
- Introduction to Digital Circuits
- Use of Digital Circuits Which are formed by Logic gates, Flip flops, and all other digital components
- Basic Knowledge on Chip Designing
- HDL Programming: Introduction, Programming and execution in Structural model and Behavioural model

Program Outcome : By the end of the program, students would

- Enhance the knowledge of students in - Introduction to HDL Programming
- HOW FPGA Useful in Digital circuit Design
- How to check and analyse the designed digital circuit by simulation

Name of the Coordinator: Mr. G.VENKATESHWARLU, Assistant Professor, ECE.

No of the Participants : 40

Program Coordinator

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Date:03-03-2019

CIRCULAR








This is to inform all the III B.Tech ECE students that the "*A One-Week Certificate Program on Analog Electronics Circuits*" Program is commencing from " 04-03-2019 TO 09-03-2019 (6 Days). All the interested students are instructed to register without fail.

Timings: 09:00AM to 06:00PM

Venue: Seminar Hall (Ground floor)

Circulated to: All Departments


Principal

EEE	ECE	CSE	MECH	MINING	S&H	MBA
						



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Program on Analog Electronics
Circuits(04 MARCH, 2019)

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Name :
Designation :
Department :
Affiliation /Organization :
Educational Qualification :
Postal Address :
Mobile Number
E-mail :
DD No & Date
Name of the Bank :
Place:
Date:

Signature of the applicant

(Signature of the organization head with seal)
[Photocopy of the Registration form may also be used for
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Convener

Mr .K. VASAVI

Head of the Department ECE.

Coordinator

Mr.M.MANGI LAL

Assistant Professor, ECE

For any enquiry contact:

Mobile No:9490005729

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Program on Analog Electronics
Circuits(04 MARCH, 2019)



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Department of ECE

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About Department

Electrical and Electronics Engineering deals with the innovation, design, development and industrial applications of electrical and electronics systems, components and devices. There is a wide variety of domains to which it applies. Among the most the well-known fields, just to name a few are, Electrical Power Generation, Transmission and Distribution, Control Systems, Power Systems, Communications. The department of Electrical and Electronics Engineering (EEE) Program was started in the year of 2008 with an intake of 120 affiliated to JNTUH. The department has well equipped laboratories with standard quality equipment. The department is catering to the needs of the industry by training students to make valuable contribution. The Department is endeavoring to produce highly trained and capable engineers, who can take up the challenges of the real world. The Department focuses on uncompromising academic standards in imparting both theoretical and practical instructions with a view to prepare professional engineers. .


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Resource Person

SK MUZAFER AHMED
ASSOCIATE PROFESSOR

Program Objectives

1. TO gain knowledge on Transistor Biasing, Single stage Amplifiers, Multi stage Amplifiers, Differential Amplifiers, Feedback Amplifiers, and Oscillators.
2. Design of Electronic circuits using LTSPICE.

Topics Covered

Analyse analog signals, and sources sources, and fundamental laws in analog

world

Basics of network laws to apply to analyse analog circuits and systems

Amplifiers and small-signal models.

Design and analysis of active filters and frequency response

Design and analysis of an Instrumentation amplifier

Design of electronic circuits using LTSPICE

Program Outcome

1. They covered topics like Transistor Biasing, Single stage Amplifiers, Multi stage Amplifiers, Differential Amplifiers, Feedback Amplifiers, and Oscillators

3. Students got the knowledge on designing Analog Circuits and simulate in LTSPICE



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Cell: +91-9246907407

Week/Day-wise Schedule

Date:02/03/2019

Name of the Program: A One-Week Certificate Program on “Analog Electronics Circuits”

S.NO	Day	Date	Topics to be covered
1.	Monday	04-03-2019	Analyze analog signals, and sources sources, and fundamental laws in analog world
2.	Tuesday	05-03-2019	Basics of network laws to apply to analyze analog circuits and systems
3.	Wednesday	06-03-2019	Amplifiers and small-signal models.
4.	Thursday	07-03-2019	Design and analysis of active filters and frequency response
5.	Friday	08-03-2019	Design and analysis of an Instrumentation amplifier
6.	Saturday	09-03-2019	Design of electronic circuits using LTSPICE


Program Coordinator


HOD-EEE


PRINCIPAL
ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadradri Kothagudem Dt, Telangana,

Counselling Code: BOSE

Estd: 2008

JNTUH College Code: PP

An ISO 9001:2015 Certified Institution



ANUBOSE INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi and Affiliated to JNTUH Hyderabad)

KSP Road, New Palvancha – 507115, Bhadradri-Kothagudem, Dist., Telangana State.

Website: www.abit.ac.in

Email Id: abitjntu@gmail.com

Cell: +91-9246907407

PROGRAM REPORT

Name of the Program : A One-week Certificate Program on “Analog Electronics Circuits”

Date/Duration : 04-03-2019 TO 09-03-2019

Resource Person : Sk. MUZAFER AHMED ASSOCIATE PROFESSOR

ECE DEPARTMENT

SRI MEDHA INSTITUTE OF TECHNOLOGY (WOMEN)

Program Objective : The objective of this course is

- TO gain knowledge on Transistor Biasing, Single stage Amplifiers, Multi stage Amplifiers, Differential Amplifiers, Feedback Amplifiers, and Oscillators.
- Design of Electronic circuits using LTSPICE

Topics covered : The following topics are covered

- Analyze analog signals, and sources sources, and fundamental laws in analog world
- Basics of network laws to apply to analyze analog circuits and systems
- Amplifiers and small-signal models.
- Design and analysis of active filters and frequency response
- Design and analysis of an Instrumentation amplifier
- Design of electronic circuits using LTSPICE

Program Outcome : By the end of the program,

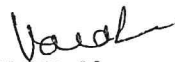
They covered topics like Transistor Biasing, Single stage Amplifiers, Multi stage Amplifiers, Differential Amplifiers, Feedback Amplifiers, and Oscillators

Students got the knowledge on designing Analog Circuits and simulate in LTSPICE

Name of the Coordinator: Mr. M.MANGILAL, Assistant Professor, ECE.

No of the Participants: 40


Program Coordinator


HOD Sign


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Bhadradi Kothagudem Dt, Telangana. Bhadradi Kothagudem Dt, Telangana.