

Counselling Code: BOSE

Estd: 2008

JNTUH College Code: PP

An ISO 9001:2015 Certified Institution



ANUBOSE INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi and Affiliated to JNTUH Hyderabad)

KSP Road, New Paloncha – 507115, BhadradiKothagudem Dist., Telangana State.

Website: www.abit.ac.in

Email Id: abitjntu@gmail.com

Cell: +91-9246907407

DATE: 31/03/2023

PROCEEDINGS

As per request received from all faculty members & approved by head of departments. It is resolved to sanction the research incentives to faculty members towards attending conference, workshops and professional membership fee for the academic year 2022-23. The list of faculty approved incentives is enclosed


PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.

Copy to:

All HoDs

Account Section



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 KSP Road, New Paloncha – 507115, Bhadrachalkothagudem Dist., Telangana State.
 Website: www.abit.ac.in Email Id: abifintu@gmail.com Cell: +91-9246907407

S.No	Academic Year	Date	Name of teacher	Name of conference/ workshop attended for which financial support provided	Name of the professional body for which membership fee is provided	Amount of support received (in INR)
1	2022 - 2023	2022-07-20	S SANOHYA	MACHINE LEARNING	DEEN DAYAL UPADHYAYA COLLEGE	1000/-
2	2022 - 2023	2022-08-01	D SHOK KUMAR	TECHNOLOGIES FOR SOCIAL NEEDS	KARUNYA INSTITUTE OF TECHNOLOGY & SCIENCE	1000/-
3	2022 - 2023	2022-09-22	D YAMUNA	MACHINE LEARNING	ASSAM SCIENCE & TECHNOLOGY UNIVERSITY	1000/-
4	2022 - 2023	2022-08-03	D YAMUNA	RESEARCH TRENDS IN TRIBOLOGY	ASSAM SCIENCE & TECHNOLOGY UNIVERSITY	1000/-
5	2022 - 2023	2022-08-03	K CHANDULAL	RESEARCH TRENDS IN TRIBOLOGY	NORTH EASTERN REGIONAL INSTITUTE OF SCIENCE & TECHNOLOGY	1000/-
6	2022 - 2023	2022-07-20	G DEVILAL	MACHINE LEARNING	DEEN DAYAL UPADHYAYA COLLEGE	1000/-
7	2022 - 2023	2022-07-20	D SWETCHA	MACHINE LEARNING	DEEN DAYAL UPADHYAYA COLLEGE	1000/-
8	2022 - 2023	2022-07-20	K VASANI	MACHINE LEARNING	ASSAM SCIENCE & TECHNOLOGY UNIVERSITY	1000/-
9	2022 - 2023	2022-08-01	K CHANDULAL	MACHINE LEARNING	MHRD-INSTITUTION	1000/-
10	2022 - 2023	2022-07-20	V RAVINDRA NAIK	MACHINE LEARNING	NORTH EASTERN REGIONAL INSTITUTE OF SCIENCE & TECHNOLOGY	1000/-
11	2022 - 2023	2022-08-01	T VENKATA KARNAKAR	TECHNOLOGIES FOR SOCIAL NEEDS	DEEN DAYAL UPADHYAYA COLLEGE	1000/-
12	2022 - 2023	2022-07-20	M D TALUJIN	MACHINE LEARNING	ASSAM SCIENCE & TECHNOLOGY UNIVERSITY	1000/-
13	2022 - 2023	2022-09-22	P SUDHER KUMAR	ONE WEEK FDP ON MACHINE LEARNING	ASSAM SCIENCE & TECHNOLOGY UNIVERSITY	1000/-
14	2022 - 2023	2022-08-03	H SHEKAR BABU	RESEARCH TRENDS IN TRIBOLOGY	JHULELAL INSTITUTE OF TECHNOLOGY	1000/-
15	2022 - 2023	2022-08-03	M D GOUSIYA BEGUM	RESEARCH TRENDS IN TRIBOLOGY	DEEN DAYAL UPADHYAYA COLLEGE	1000/-
16	2022 - 2023	2022-07-20	M VARALAKMI	MACHINE LEARNING	DEEN DAYAL UPADHYAYA COLLEGE	1000/-
17	2022 - 2023	2022-08-03	P SUDHER KUMAR	RESEARCH TRENDS IN TRIBOLOGY	DEEN DAYAL UPADHYAYA COLLEGE	1000/-
18	2022 - 2023	2022-08-02	G DEEPTHI	COMPARATIVE ANALYSIS OF CROSSLINK	GITAM UNIVERSITY	1000/-
19	2022 - 2023	2022-03-24	NV SUBBA RAO	PAPER PUBLICATION	THE INTERNATIONAL JOURNAL OF ANALYTICAL AND EXPERIMENTAL MODEL ANALYSIS	1000/-
20	2022 - 2023	2022-03-24	G DEEPTHI	QUALITY OF SIGNAL IMPROVEMENT	GRIJANANDA CHOWDARY INSTITUTE OF MANAGEMENT AND TECHNOLOGY	1000/-
21	2022 - 2023	2022-08-02	V ANUSHA	RECENT RESEARCH TRENDS IN TRIBOLOGY	JOURNAL OF ENGINEERING SCIENCE	1000/-
22	2022 - 2023	2022-08-07	A AVANI	PAPER PUBLICATION	JOURNAL OF ENGINEERING SCIENCE	1000/-
23	2022 - 2023	2022-06-03	A AVANI	PAPER PUBLICATION	JOURNAL OF APPLIED SCIENCE AND COMPUTATIONS	1000/-
24	2022 - 2023	2022-04-02	NV SUBBA RAO	PAPER PUBLICATION	JOURNAL OF ENGINEERING SCIENCE	1000/-
25	2022 - 2023	2022-07-07	A AVANI	PAPER PUBLICATION	JOURNAL OF ENGINEERING SCIENCE	1000/-
26	2022 - 2023	2022-06-07	A AVANI	PAPER PUBLICATION	JOURNAL OF INTER DISCIPLINARY CYCLE RESEARCH	1000/-
27	2022 - 2023	2022-01-01	NV SUBBA RAO	PAPER PUBLICATION	JOURNAL OF ENGINEERING SCIENCE	1000/-
28	2022 - 2023	2022-09-07	A AVANI	PAPER PUBLICATION	JOURNAL OF ENGINEERING SCIENCE	1000/-
29	2022 - 2023	2022-07-07	A AVANI	PAPER PUBLICATION	JOURNAL OF INTER DISCIPLINARY CYCLE RESEARCH	1000/-
30	2022 - 2023	2022-02-02	NV SUBBA RAO	PAPER PUBLICATION	THE INTERNATIONAL JOURNAL OF ANALYTICAL AND EXPERIMENTAL MODEL ANALYSIS	1000/-
31	2022 - 2023	2022-01-01	NV SUBBA RAO	PAPER PUBLICATION	JOURNAL OF ENGINEERING SCIENCE	1000/-
32	2022 - 2023	2022-08-06	A AVANI	PAPER PUBLICATION	IAEST JOURNAL FOR ADVANCED RESEARCH IN APPLIED SCIENCE	1000/-
33	2022 - 2023	2022-04-03	NV SUBBA RAO	PAPER PUBLICATION	JOURNAL OF INTER DISCIPLINARY CYCLE RESEARCH	1000/-
34	2022 - 2023	2022-02-01	NV SUBBA RAO	PAPER PUBLICATION	THE INTERNATIONAL JOURNAL OF ANALYTICAL AND EXPERIMENTAL MODEL ANALYSIS	1000/-
35	2022 - 2023	2022-02-01	NV SUBBA RAO	PAPER PUBLICATION	GITAM UNIVERSITY	1000/-
36	2022 - 2023	2022-01-09	G DEEPTHI	CROSSLINK ANALYSIS	JOURNAL OF INTER DISCIPLINARY CYCLE RESEARCH	1000/-
37	2022 - 2023	2022-01-01	NV SUBBA RAO	PAPER PUBLICATION	NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING	1000/-
38	2022 - 2023	2023-07-25	B CHARAN	DESIGN OF STRUCTURE USING LATEST VERSION OF STAAD	NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING	1000/-
39	2022 - 2023	2023-07-25	B RAMESH	DESIGN OF STRUCTURE USING LATEST VERSION OF STAAD	Dr. D.V PATIL INSTITUTE OF TECHNOLOGY	1000/-
40	2022 - 2023	2023-05-07	V SHYOK	SUSTAINABLE INNOVATION IN INDIA	JHULELAL INSTITUTE OF TECHNOLOGY	1000/-
41	2022 - 2023	2023-05-11	V SHYOK	ONE WEEK FDP ON ELECTRIC VEHICLE	JHULELAL INSTITUTE OF TECHNOLOGY	1000/-
42	2022 - 2023	2023-05-07	T RAMBABU	SUSTAINABLE INNOVATION IN INDIA	Dr. D.V PATIL INSTITUTE OF TECHNOLOGY	1000/-
43	2022 - 2023	2023-05-11	SK ANBELI	ONE WEEK FDP ON ELECTRIC VEHICLE	NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING	1000/-
44	2022 - 2023	2023-07-25	G MADHULATHA	DESIGN OF STRUCTURE USING LATEST VERSION OF STAAD	ASSAM SCIENCE & TECHNOLOGY UNIVERSITY	1000/-
45	2022 - 2023	2023-05-11	M D GOUSIYA BEGUM	ONE WEEK FDP ON ELECTRIC VEHICLE	JHULELAL INSTITUTE OF TECHNOLOGY	1000/-
46	2022 - 2023	2023-05-11	R SHEKAR BABU	ONE WEEK FDP ON ELECTRIC VEHICLE	NORTH EASTERN REGIONAL INSTITUTE OF SCIENCE & TECHNOLOGY	1000/-
47	2022 - 2023	2023-07-25	VENKATA NARASIMHA PRABATH	DESIGN OF STRUCTURE USING LATEST VERSION OF STAAD	NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING	1000/-
48	2022 - 2023	2023-07-25	B LAKMAN NAIK	DESIGN OF STRUCTURE USING LATEST VERSION OF STAAD	Dr. D.V PATIL INSTITUTE OF TECHNOLOGY	1000/-
49	2022 - 2023	2023-05-07	L PRASHANTH KUMAR	SUSTAINABLE INNOVATION IN INDIA	Dr. D.V PATIL INSTITUTE OF TECHNOLOGY	1000/-
50	2022 - 2023	2023-05-07	P RAMA SRINIVASA RAO	SUSTAINABLE INNOVATION IN INDIA	GITAM UNIVERSITY	1000/-
51	2022 - 2023	2023-01-01	G DEEPTHI	CROSSLINK ANALYSIS	Dr. D.V PATIL INSTITUTE OF TECHNOLOGY	1000/-
52	2022 - 2023	2023-05-07	N SRINANTH	SUSTAINABLE INNOVATION IN INDIA	Dr. D.V PATIL INSTITUTE OF TECHNOLOGY	1000/-
53	2022 - 2023	2023-05-07	P LOKESWAR RAO	SUSTAINABLE INNOVATION IN INDIA	Dr. D.V PATIL INSTITUTE OF TECHNOLOGY	1000/-

PROCTORIAL
 Anubose Institute of Technology
 KSP Road, PALVANCHA,
 Kothagudem Dist



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : P. Lokeshwara Rao
- Designation : Assistant professor
- Department : M.T.M.G.
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: Accelerating... Clean... Green... and... Sustainable... Innovation.....
..... In india
- Duration of the Program : 7th May - 14th June, 2023
- Associating professional body : Dr. D. V. S. Patil Institute of Technology
- Financial support particulars (Rs.) :
 - Registration Charges : 10.00/-
 - Travelling Allowance : -
 - Membership Fee : -
 - Others : -

Date: _____ Signature of the faculty member

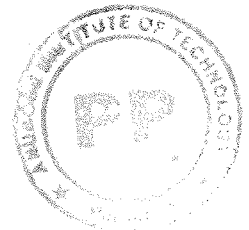
- Recommendations of the HoD: _____
- Recommendations of the IQAC: _____
- Recommendations of the Principal: _____

Sanctioned/Not Sanctioned

Account Department

Accountant: _____
Date: 4/05/2022

Principal
Anubose Institute of Technology
KSP Road, Paloncha, Bhadradi, Kothagudem Dist.,
Telangana.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradri Kothagudem Dist.

V.No.....

Date: 30/04/2023

CASH VOUCHER

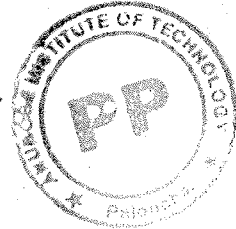
DEBIT.....	PARTICULARS	AMOUNT	
		Rs.	ps.
	Accelerating clean, green and sustainable innovation in India	1000/-	
	P. Lokeshwara Rao		

Cashier

Principal

Received Rs. 1000 (Rupees One Thousand) Rupees only being.....

PRINCIPAL
ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadradri Kothagudem Dt, Telangana.



[Signature]
Signature

[Signature]
PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.

DPU

Dr. D. Y. Patil Institute of Technology, Pimpri, Pune
&

ASSOCHAM GEM Green Building Council, New Delhi

Faculty Development Program on

“Accelerating Clean, Green and Sustainable Innovation in India”

Certificate of Participation

P. hokeshwara Rao

This certificate is awarded to

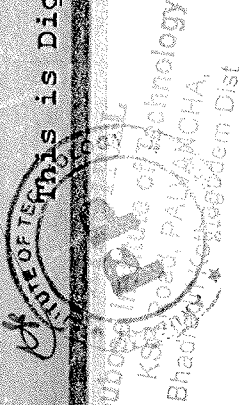
of

Anubose Institute of Technology

in recognition to attending the FDP on “*Accelerating Clean, Green and Sustainable Innovation In India*” organized by Department of Electrical Engineering and Department of Instrumentation Engineering of Dr. D. Y. Patil Institute of Technology, Pimpri, Pune in collaboration with ASSOCHAM GEM Green Building Council, New Delhi, during 7th

May 2023 to 14th June 2023

FDP0505180610



This is Digital Certificate and does not require signature

PRINCIPAL
ANUBOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvanche
Bhamburda, Kolhapur, Maharashtra, India



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Cell: +91-9246907407



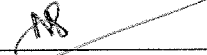
CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : S. Sandhya
- Designation : Assistant professor
- Department : ECE
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details: Online Summer School cum faculty Development program
 On Advance in signal processing and Machine Learning
- Duration of the Program : 20th - 26th July, 2022
- Associating professional body : Deen Dayal Upadhyaya College
- Financial support particulars (Rs.) :
 i. Registration Charges : 1000/-
- ii. Travelling Allowance : -
- iii. Membership Fee : -
- iv. Others : -



Signature of the faculty member

Date:


- Recommendations of the HoD: 
- Recommendations of the IQAC: 
- Recommendations of the Principal: 


Sanctioned/Not Sanctioned

Account Department

Accountant: 

Date: 19/07/2022


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 Bhadradri Kothagudem Dist.

ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P. Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

V.No.....

Date: 13/07/2022

CASH VOUCHER

DEBIT.....

PARTICULARS	AMOUNT	
	Rs.	ps.
Online summer school - cum - faculty development program on advances in signal processing & machine learning S. Sandhya	1000/-	

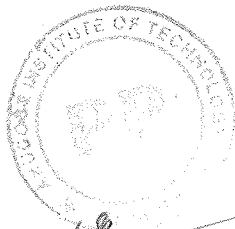
Cashier


Principal

Received Rs. 1000 (Rupees One Thousand
rupees only)
being.....


PRINCIPAL

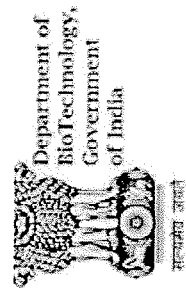
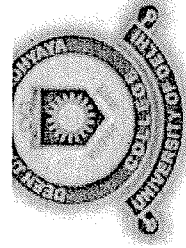
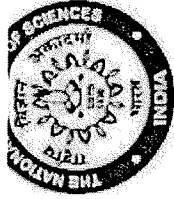
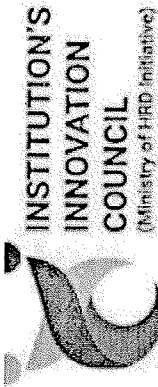
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Bhadradi Kothagudem Dt, Telangana.




PRINCIPAL

Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.


Signature



Online Summer School-cum-Faculty Development Program

ON

Advances in Signal Processing and Machine Learning

Jointly organized by

MHRD-Institution Innovation Council, DDUC Chapter, Deen Dayal Upadhyaya College, University of Delhi
(Under the aegis of DBT Star College Program)

Department of Electronic Science, University of Delhi & National Academy of Sciences India (NASI) – Delhi Chapter
Supported By

IEEE Electron Device Society (EDS), Delhi Chapter

July 20, 2022 – July 26, 2022

Certificate of Completion

This is to certify that

S. Sandhya

ASSISTANT PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, ANUBOSE INSTITUTE OF TECHNOLOGY

Has Completed Online Summer School-cum-Faculty Development Program on Advances in Signal Processing and Machine Learning

Dr. Hem Chand Jain
Officiating Principal
Deen Dayal Upadhyaya College
University of Delhi

Professor Miridula Gupta, SMIEEE, FIETE
Chairperson-IEEE EDS Delhi Chapter
Head, Department of Electronic Science
University of Delhi South Campus, New Delhi 110021, India

Dr. Manoj Saxena, MNASC
Convener-Summer School
Coordinator-DBT Star College Program (DDU)
Deen Dayal Upadhyaya College, University of Delhi

Registered Email ID of Attendee : swpatra@gmail.com
Certificate No. 2022/MHRD/ICDDUC/Online Summer School/July/76



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ANUBOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha, Anubose Institute of Technology
Bhadradi Kothagudem Dt, Telangana, PIN-507 301, ALVANCHA,
Bhadradi Kothagudem Dist.



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Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : D. Ashak Kumar
- Designation : Assistant professor
- Department : MBA
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details: Technologies for Societal Needs Real time appli-
 cation of AI, ML and Research Initiatives
- Duration of the Program : 01/08/2022 - 07/08/2022
- Associating professional body : Karunya Institute of Technology & Science
- Financial support particulars (Rs.) :
 i. Registration Charges : 1000/-
 ii. Travelling Allowance : -
 iii. Membership Fee : -
 iv. Others : -
 AR

Date: _____ Signature of the faculty member

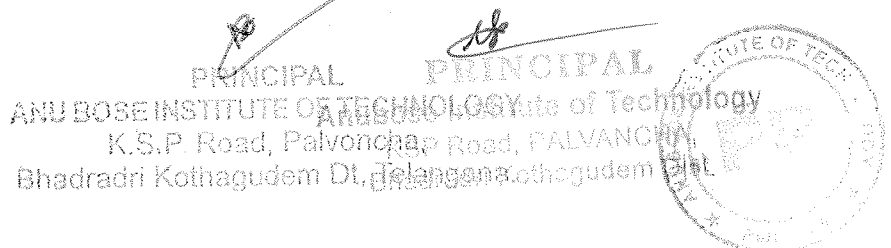
- Recommendations of the HoD: Ref
- Recommendations of the IQAC: Ref
- Recommendations of the Principal: AR

Sanctioned/Not Sanctioned

Account Department

Accountant: MP

Date: 01/08/2022



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P. Road, NEW PALONCHA-507 115,
Bhadradri Kothagudem Dist.

V.No.....

Date: 25/07/2022

CASH VOUCHER

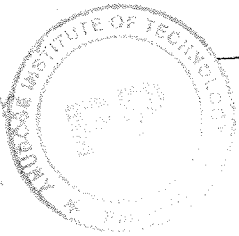
DEBIT.....	PARTICULARS	AMOUNT	
		Rs.	ps.
	Technologies for societal needs real time application of AI, ML and research Initiatives	1000/-	
	D. Ashok Kumar		

Cashier

[Signature]
Principal

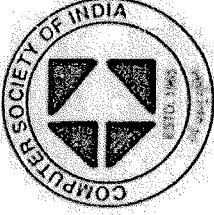
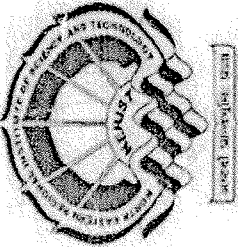
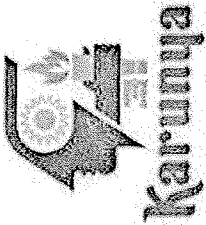
Received Rs. 1000 (Rupees One Thousand
rupees only
being.....

[Signature]
PRINCIPAL
ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadradri Kothagudem Dt, Telangana.



[Signature]
Signature

[Signature]
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Bhadradri Kothagudem Dist



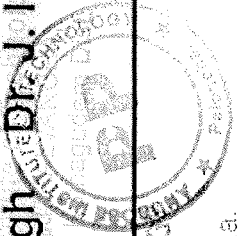
CERTIFICATE OF PARTICIPATION

This is to certify that D. Ashok kumar from Anubose Institute of Technology participated in the

One Week Online International Faculty Development Program on "Technologies for Societal Needs - Real time applications of AI, ML and Research initiatives" jointly organized by Department of Computer Science and Engineering, Karunya Institute of Technology and Sciences, Tamil Nadu, India and Department of Computer Science and Engineering, North Eastern Regional Institute of Science and Technology, Arunachal Pradesh, India from 01-08-2022 to 07-08-2022.

P. Lalitha
Dr. P. Getzi Jeba Organizing Secretary
K. Ramalingam Co-Convenor

Dr. J. Immanuel
Dr. J. Immanuel Johnraja HoD (CSE), KITS
Dr. Moirangthem Marjit Singh Convener & HoD (CSE), NERIST



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Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : D. Yamuna
- Designation : Assistant professor
- Department : Mechanic
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details: ... Machine learning and doing a phenomenal job on the exploratory data analysis of flight price dataset during the program
- Duration of the Program : ... 22 to 28 March 2021
- Associating professional body : ... Assam Science and Technology University
- Financial support particulars (Rs.) :
 - Registration Charges : 1000/-
 - Travelling Allowance : —
 - Membership Fee : —
 - Others : —

Date: _____ Signature of the faculty member *Yamuna*

- Recommendations of the HoD: *[Signature]*
- Recommendations of the IQAC: *[Signature]*
- Recommendations of the Principal: *[Signature]*

Sanctioned/Not Sanction

Account Department

Accountant: *[Signature]*
Date: 21/03/2022

PRINCIPAL
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 Bhadradi Kothagudem Dist.

ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradri Kothagudem Dist.

V.No.....

Date: 15/03/2022

CASH VOUCHER

DEBIT.....

PARTICULARS	AMOUNT	
	Rs.	ps.
Machine learning & doing a phenomenal job on exploratory data analysis of flight price data set during the program D. Yamuna	1000/-	

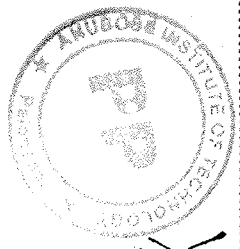
Cashier

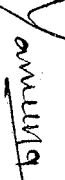

Principal

Received Rs. 1000 (Rupees One Thousand)

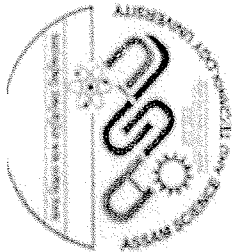
being Rupees only

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K.S.P. Road, Palvoncha,
Bhadradri Kothagudem Dt, Telangana.

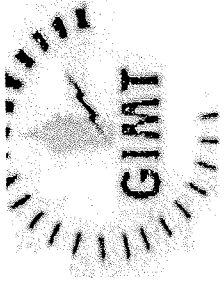



Signature


PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist



GIRIJANANDA CHOWDHURY INSTITUTE OF MANAGEMENT AND TECHNOLOGY



Certificate of Participation

This is to certify that

D. Yamuna

of

ANUBOSE INSTITUTE OF TECHNOLOGY

has attended and actively participated the Webinar Series on "Recent Research Trends in Tribology" organized by Department of Mechanical Engineering, GIMT Guwahati, sponsored by the collaborative research Scheme of Assam Science and Technology University (ASTU), under TEQIP-III from 3rd to 9th August, 2022.

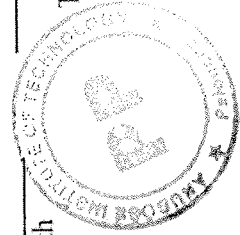
Sangeeta Das
(Dr. Sangeeta Das)
Coordinator
GIMT-Guwahati

Rajib Bhowmik
(Mr. Rajib Bhowmik)
Coordinator
GIMT-Guwahati

Debarshi Mallick
Dr. Debarshi Mallick
HoD, ME Department
GIMT-Guwahati

Thuleswar Nath
Prof. Thuleswar Nath
Principal,
GIMT-Guwahati

Phukan
Dr. B.R. Phukan
TEQIP Coordinator,
ASTU-Guwahati



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : P. Sudeesh Kumar
- Designation : Assistant professor
- Department : Mechanical
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: The webinar Series On Recent Research Trends
..... In Tribology
- Duration of the Program : 3rd to 5th August, 2022
- Associating professional body : Deen Dayal Upadhyaya College
- Financial support particulars (Rs.) :
i. Registration Charges : 1000/-
- ii. Travelling Allowance : -
- iii. Membership Fee : -
- iv. Others : -

[Signature]

Signature of the faculty member

Date:

- Recommendations of the HoD: *[Signature]*
- Recommendations of the IQAC: *[Signature]*
- Recommendations of the Principal: *[Signature]*

Sanctioned/Not Sanction

Account Department

Accountant: *[Signature]*

Date: 07/08/2022

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KSP Road, Paloncha,
Bhadradi Kothagudem Dt, Telangana.

[Signature]
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Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P. Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

V.No.....

Date: 08/02/2022

CASH VOUCHER

DEBIT.....

PARTICULARS	Rs.	AMOUNT	ps.
The webinar Series On Recen -t Research Trends in Tribology P. Sudheer Kumar	1000/-		

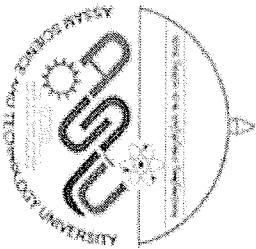
Cashier

Signature

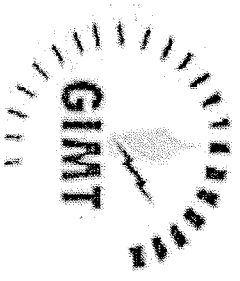
Received Rs. 1000/- Only
One thousand Rupees (Rupees)

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K.S.P. Road, PALVANCHI,
Bhadradi Kothagudem Dist.

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Anubose Institute of Technology
K.S.P. Road, PALVANCHI,
Bhadradi Kothagudem Dist.



GIRIJANANDA CHOWDHURY INSTITUTE OF MANAGEMENT AND TECHNOLOGY



Certificate of Participation

This is to certify that

P. Sudheer Kumar

of

ANUBOSE INSTITUTE OF TECHNOLOGY

has attended and actively participated the Webinar Series on "Recent Research Trends in Tribology" organized by Department of Mechanical Engineering, GIMT Guwahati, sponsored by the collaborative research Scheme of Assam Science and Technology University (ASTU), under TEQIP-III from 3rd to 9th August, 2022.

Sangeeta Saia

(Dr. Sangeeta Das)
Coordinator
GIMT-Guwahati

(Mr. Rajib Bhowmik)
Coordinator
GIMT-Guwahati

Dr. Debasisi Mallick
HOD, ME Department
GIMT-Guwahati
Bhadrauli Kohagudem Dist.

Prof. Thuleswar Nath
Principal,
GIMT-Guwahati

Dr. B.R. Phukan
TEQIP Coordinator,
ASTU-Guwahati



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : D. Yamuna
- Designation : Assistant professor
- Department : Mechanical
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details: The webinar Series On Recent Research Trend
 in Tribology
- Duration of the Program : 3rd to 9th August, 2022
- Associating professional body : Assam Science and Technology University
- Financial support particulars (Rs.) :
 i. Registration Charges : 1000/-
- ii. Travelling Allowance : —
- iii. Membership Fee : —
- iv. Others : —

Yam

Signature of the faculty member

Date:

- Recommendations of the HoD: *[Signature]*
- Recommendations of the IQAC: *[Signature]*
- Recommendations of the Principal: *[Signature]*

Sanctioned/Not Sanctioned

Account Department

Accountant: *[Signature]*

Date: 02/08/2022

[Signature]
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 Bhadradi, Kothagudem Dist.,
 Telangana.



ANU BOSE INSTITUTE OF TECHNOLOGY

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Bhadradi Kothagudem Dist.

V.No.....

Date: 27/07/2022

CASH VOUCHER

DEBIT

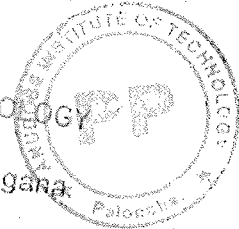
PARTICULARS	AMOUNT	
	Rs.	ps.
The webinar series on recent research trends in Tribology	1000/-	
D.Yamuna		

Cashier

Principal

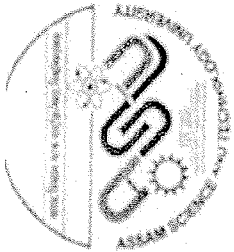
Received Rs. 1000 (Rupees One Thousand Rupees only) being.....

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Bhadradi Kothagudem Dt, Telangana

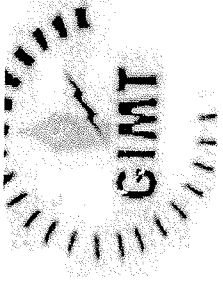


Yamuna
Signature

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Bhadradi Kothagudem Dist.



GIRIJANANDA CHOWDHURY INSTITUTE OF MANAGEMENT AND TECHNOLOGY



Certificate of Participation

This is to certify that

D. Yamuns

of

ANUBOSE INSTITUTE OF TECHNOLOGY

has attended and actively participated the Webinar Series on "Recent Research Trends in Tribology" organized by Department of Mechanical Engineering, GIMT Guwahati, sponsored by the collaborative research Scheme of Assam Science and Technology University (ASTU), under TEQIP-III from 3rd to 9th August, 2022.

Sangeeta Das

(Dr. Sangeeta Das)
Coordinator
GIMT-Guwahati

Rajib Bhowmik

(Mr. Rajib Bhowmik)
Coordinator
GIMT-Guwahati

Debarshi Mallick

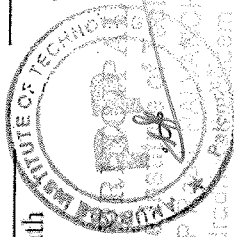
Dr. Debarshi Mallick
HoD, ME Department
GIMT-Guwahati

Thuleswar Nath


Prof. Thuleswar Nath
Principal,
GIMT-Guwahati

B.R. Phukan

Dr. B.R. Phukan
TEQIP Coordinator,
ASTU-Guwahati



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	College PP	Counselling BOSE
CODE		

Financial Support Request Letter

1. Name of the faculty member :K... Chandulal.....
2. Designation :Assistant professor.....
3. Department :Mechanical.....
4. Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details:.....The Webinar Series On Recent Research Trends
in Tribology.....
5. Duration of the Program :3rd to 9th August, 2022.....
6. Associating professional body :North Eastern Region Institute of Science and Technology.....
7. Financial support particulars (Rs.) :

 - i. Registration Charges :1000/-.....
 - ii. Travelling Allowance :-.....
 - iii. Membership Fee :-.....
 - iv. Others :-.....

Chen


Date: _____ **Signature of the faculty member**


1. Recommendations of the HoD: *Deef*
2. Recommendations of the IQAC: *Deef*
3. Recommendations of the Principal: *NS*

✓
Sanctioned/Not Sanction

Account Department

Accountant: *mp*
Date: 02/02/2022


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Bhadradi Kothagudem Dist.

V.No.....

Date: 27/07/2022

CASH VOUCHER


DEBIT.....

PARTICULARS	AMOUNT	
	Rs.	ps.
The webinar series on recent research trends in Tribology	1000/-	
K.Chandhulal		

Cashier



Principal

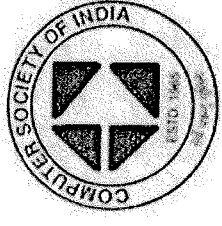
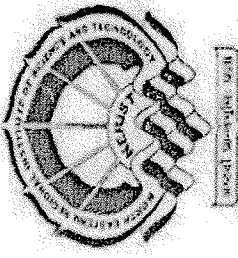
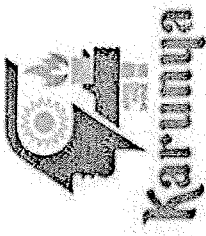
Received Rs. 1000 (Rupees One Thousand)
rupees only
being.....


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Signature


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KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.



CERTIFICATE OF PARTICIPATION

This is to certify that K. Chandulal from Anubose Institute of Technology participated in the

One Week Online International Faculty Development Program on "Technologies for Societal Needs - Real time applications of AI, ML and Research initiatives" jointly organized by Department of Computer Science and Engineering, Karunya Institute of Technology and Sciences, Tamil Nadu, India and Department of Computer Science and Engineering, North Eastern Regional Institute of Science and Technology, Arunachal Pradesh, India from 01-08-2022 to 07-08-2022.

P. Getzi Jeba

K. Ramalakshmi

[Signature]

[Signature]

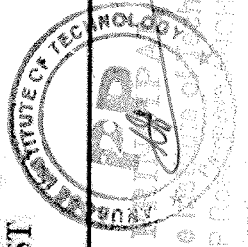
Dr. P. Getzi Jeba
Organizing Secretary

Dr. K. Ramalakshmi
Co-Convenor

Dr. Moirangthem Marjit Singh
Convenor & HoD (CSE), NERIST

Dr. J. Immanuel Johnraja
HoD (CSE), KIITS

PRINCIPAL



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Bhadrachali Kothagudem Dt, Telangana
Anubose Institute of Technology
KSP Road, Palvoncha,
Bhadrachali Kothagudem Dt, Telangana



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : G. Devital
- Designation : Assistant professor
- Department : ECE
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details: Online Summer School - Cum - Faculty Development pro
gram On Advances in Signal processing & machine
learning
- Duration of the Program : 20th - 26th July, 2022
- Associating professional body : Deen Dayal Upadhyay's College
- Financial support particulars (Rs.) :
 i. Registration Charges : 1000/-
- ii. Travelling Allowance : -
- iii. Membership Fee : -
- iv. Others : -

Date: _____ Signature of the faculty member Devi

1. Recommendations of the HoD: Suf

2. Recommendations of the IQAC: mbt

3. Recommendations of the Principal: [Signature]

Sanctioned/Not Sanctioned

Account Department

Accountant: [Signature]

Date: 20/07/2022

Principal [Signature]

ANUBOSE INSTITUTE OF TECHNOLOGY
 K.S.P. Road, Paloncha, Bhadradri Kothagudem Dist.,
 Telangana State.

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 Telangana State.

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Bhadradri Kothagudem Dist.

V.No.....

Date: 13/07/2022

CASH VOUCHER

DEBIT.....

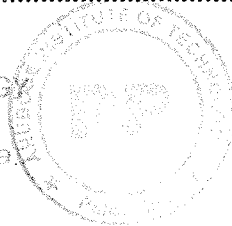
PARTICULARS	AMOUNT	
	Rs.	ps.
Online summer school-cum-faculty development program on advances in signal processing & machine learning. Sr. Devital	1000/-	

Cashier

Principal

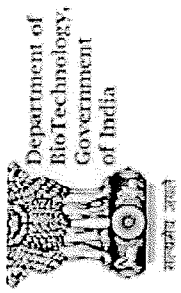
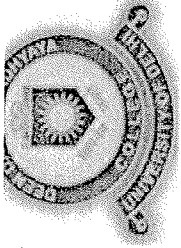
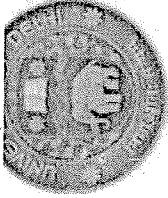
Received Rs. 1000 (Rupees One Thousand rupees only) being.....

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K.S.P. Road, Palvoncha,
Bhadradri Kothagudem Dt, Telangana



Devital
Signature

PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist



Online Summer School-cum-Faculty Development Program



ON

Advances in Signal Processing and Machine Learning

Jointly organized by

MHRD-Institution Innovation Council, DDUC Chapter, Deen Dayal Upadhyaya College, University of Delhi
(Under the aegis of DBT Star College Program)

Department of Electronic Science, University of Delhi & National Academy of Sciences India (NASI) – Delhi Chapter

Supported By

IEEE Electron Device Society (EDS), Delhi Chapter

July 20, 2022 – July 26, 2022

Certificate of Completion

This is to certify that

G. Devital

ASSISTANT PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, ANUBOSE INSTITUTE OF TECHNOLOGY

Has Completed Online Summer School-cum-Faculty Development Program on Advances in Signal Processing and Machine Learning

Dr. Hem Chand Jain
Officiating Principal
Deen Dayal Upadhyaya College
University of Delhi

Registered Email ID of Attendee : swpatra@gmail.com
Certificate No. 2022/MHRD/ICDDUC/Online Summer School/July/76

Professor Mridula Gupta, SMIEEE, FIETE
Chairperson-IEEE EDS Delhi Chapter
Head, Department of Electronic Science
University of Delhi South Campus, New Delhi 110021, India

Dr. Manoj Saxena, MNASC
Convener-Summer School
Coordinator-DBT Star College Program (DDU)
Deen Dayal Upadhyaya College, University of Delhi

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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : D. Swecha
- Designation : Assistant professor
- Department : ECE
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: ... Online Summer School - cum - faculty development program ...
... on Advances in signal processing & machine learning ...
.....
- Duration of the Program : 20th - 26th July, 2021
- Associating professional body : Deen Dayal Upadhyay College
- Financial support particulars (Rs.) :
i. Registration Charges : 1000/-
ii. Travelling Allowance :
iii. Membership Fee :
iv. Others :

Date: _____ Signature of the faculty member

1. Recommendations of the HoD:

2. Recommendations of the IQAC:

3. Recommendations of the Principal:

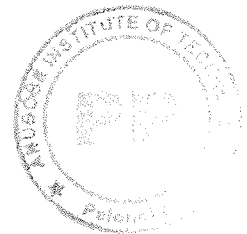
Sanctioned/Not Sanctioned

Account Department

Accountant:

Date: 19/07/2022

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KSP Road, Paloncha,
Bhadradri Kothagudem Dist.,
Telangana State.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P. Road, NEW PALONCHA-507 115,
Bhadradri Kothagudem Dist.

Date: 13/07/2022

V.No.....

CASH VOUCHER

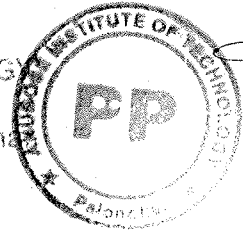
DEBIT	PARTICULARS	AMOUNT	
		Rs.	ps.
	Online summer school-cum-faculty development program on advances in signal processing & machine learning D. Swecha	1000/-	

Cashier

Principal

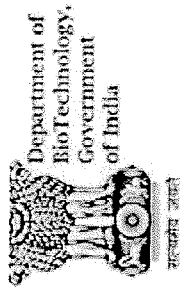
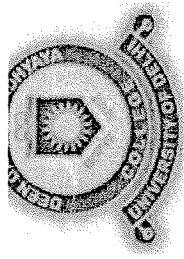
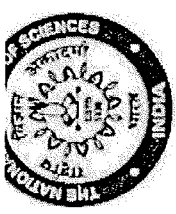
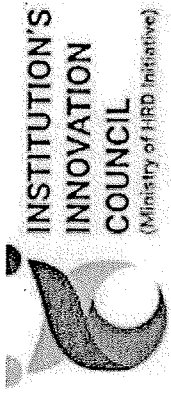
Received Rs. 1000 (Rupees One Thousand Rupees only) being.....

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Bhadradri Kothagudem Dt, Telangana



Signature

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Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.



Online Summer School-cum-Faculty Development Program

on

Advances in Signal Processing and Machine Learning

Jointly organized by

MHRD-Institution Innovation Council, DDUC Chapter, Deen Dayal Upadhyaya College, University of Delhi
(Under the aegis of DBT Star College Program)

Department of Electronic Science, University of Delhi & National Academy of Sciences India (NASI) – Delhi Chapter

Supported By

IEEE Electron Device Society (EDS), Delhi Chapter

July 20, 2022 – July 26, 2022

Certificate of Completion

This is to certify that

D. Swecha

ASSISTANT PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, ANUBOSE INSTITUTE OF TECHNOLOGY

Has Completed Online Summer School-cum-Faculty Development Program on Advances in Signal Processing and Machine Learning

[Signature]

Dr. Hem Chand Jain
Officiating Principal
Deen Dayal Upadhyaya College
University of Delhi

[Signature]

Professor Miridula Gupta, SMIEEE, FIETE
Chairperson-IEEE EDS Delhi Chapter
Head, Department of Electronic Science
University of Delhi South Campus, New Delhi 110021, India

[Signature]

Dr. Manoj Saxena, MNASc
Convener-Summer School
Coordinator-DBT Star College Program (DDU)
Deen Dayal Upadhyaya College, University of Delhi

Registered Email ID of Attendee : swpatra@gmail.com
Certificate No. 2022/11111111111111111111/Online Summer School/July/76



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ANUBOSE INSTITUTE OF TECHNOLOGY

Plot No. K.S.P. Road, Palvoncha,
Dist. Bhubaneswar, Odisha-751005



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : *K. Vasavi*
- Designation : *Assistant professor*
- Department : *ECE*
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: *Online Summer School - cum - faculty Development program*
..... *On Advances in Signal processing & Machine learning*
- Duration of the Program : *20th - 26th July, 2022*
- Associating professional body : *Deen Dayal Upadhyaya College*
- Financial support particulars (Rs.) :
i. Registration Charges : *1000/-*
ii. Travelling Allowance : *-*
iii. Membership Fee : *-*
iv. Others : *-*

Vasavi

Signature of the faculty member

Date:

1. Recommendations of the HoD: *[Signature]*

2. Recommendations of the IQAC: *[Signature]*

3. Recommendations of the Principal: *[Signature]*


Sanctioned/Not Sanction

Account Department

Accountant: *[Signature]*

Date: *19/07/2022*

PRINCIPAL
ANUBOSE INSTITUTE OF TECHNOLOGY
K.S.P. ROAD, NEW PALONCHA,
BHADRADRI KOTHAGUDEM DIST.,
TELANGANA STATE



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

V.No.....

Date: 13/07/2022

CASH VOUCHER

DEBIT.....

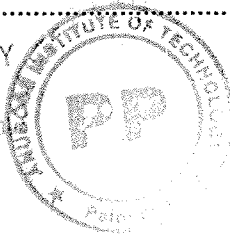
PARTICULARS	AMOUNT	
	Rs.	ps.
Online summer school-cum - faculty development program on advances in signal processing & machine learning K. Vasavi	1000/-	

Cashier


Principal

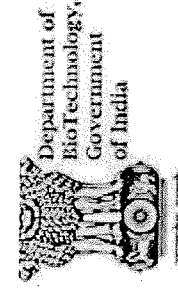
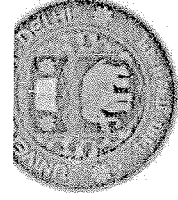
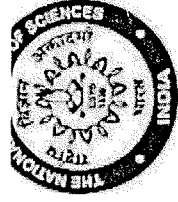
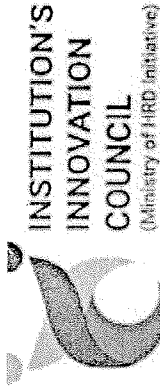
Received Rs. 1000 (Rupees One Thousand)
rupees only
being.....

PRINCIPAL
ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadradi Kothagudem Dt, Telangana




Signature

PRINCIPAL
Anubose Institute of Technol...
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist



Online Summer School-cum-Faculty Development Program



on

Advances in Signal Processing and Machine Learning

Jointly organized by

MHRD-Institution Innovation Council, DDUC Chapter, Deen Dayal Upadhyaya College, University of Delhi
(Under the aegis of DBT Star College Program)

Department of Electronic Science, University of Delhi & National Academy of Sciences India (NASI) – Delhi Chapter
Supported By

IEEE Electron Device Society (EDS), Delhi Chapter

July 20, 2022 – July 26, 2022

Certificate of Completion

This is to certify that

K. VASAVI

ASSISTANT PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, ANUBOSE INSTITUTE OF TECHNOLOGY

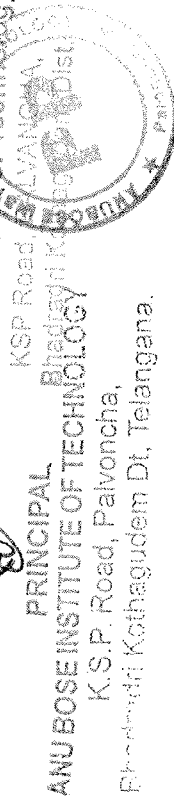
Has Completed Online Summer School-cum-Faculty Development Program on Advances in Signal Processing and Machine Learning

Dr. Hem Chand Jain
Officiating Principal
Deen Dayal Upadhyaya College
University of Delhi

Professor Miridula Gupta, SMIEEE, FIETE
Chairperson-IEEE EDS/Delhi Chapter
Head, Department of Electronic Science
University of Delhi South Campus, New Delhi 110021, India

Dr. Manoj Saxena, MNASc
Convener-Summer School
Coordinator-DBT Star College Program (DDU)
Deen Dayal Upadhyaya College, University of Delhi

Registered Email ID of Attendee : swpatra@gmail.com
Certificate No. 2022/MHRD/ICDDUC/Online Summer School/July/76



PRINCIPAL
ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadrachari Kothagudem Dt, Telangana.



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,
Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : ... *K. Chandulal*
- Designation : *Assistant Professor*
- Department : ... *Mechanical*
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: *Technologies for societies Needs Real time applications*
of A.I.M. and Research Initiatives
- Duration of the Program : *1st to 7th August 2022*
- Associating professional body : *Karunya Institute of Technology & Sciences*
- Financial support particulars (Rs.) :

 - Registration Charges : ... *1000/-*
 - Travelling Allowance : -
 - Membership Fee : -
 - Others : -

Date: _____ Signature of the faculty member *Chandulal*

- Recommendations of the HoD: *Not* _____
- Recommendations of the IQAC: *Not* _____
- Recommendations of the Principal: *MS* _____

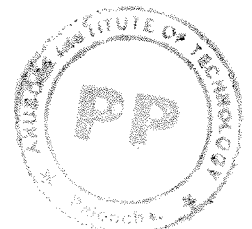
Sanctioned/Not Sanctioned

Account Department

Accountant: *MP*

Date: *01/10/2022*

PRINCIPAL
ANUBOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Paloncha,
KSP Road, Bhadradi, Kothagudem Dt, Telangana.
Bhadradi Kothagudem Dist.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P. Road, NEW PALONCHA-507 115,
Bhadradri Kothagudem Dist.

Date: 15/03/2022

V.No.....

CASH VOUCHER

DEBIT.....	PARTICULARS	AMOUNT	
		Rs.	ps.
	Technologies for societal needs Real time applications of AI, ML and Research initiatives K. Chandulal	1000/-	

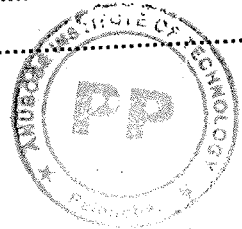
Cashier

Received Rs. 1000 (Rupees) one thousand

rupees only

being

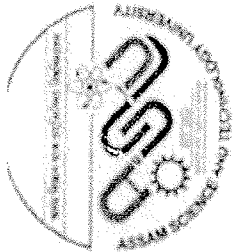
ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadradri Kothagudem Dt, Telangana.



K. Chandulal
Signature

K. Chandulal
Principal

PRINCIPAL
Anubose Institute of Technology
KSP Road, PALONCHA,
Bhadradri Kothagudem Dist.



GIRIJANANDA CHOWDHURY INSTITUTE OF MANAGEMENT AND TECHNOLOGY



Certificate of Participation

This is to certify that

K. Chandulal

of

ANUBOSE INSTITUTE OF TECHNOLOGY

has attended and actively participated the Webinar Series on "Recent Research Trends in Tribology" organized by Department of Mechanical Engineering, GIMT Guwahati, sponsored by the collaborative research Scheme of Assam Science and Technology University (ASTU), under TEQIP-III from 3rd to 9th August, 2022.

Sangeeta Das

(Dr. Sangeeta Das)
Coordinator
GIMT-Guwahati

Bhowmik

(Mr. Rajib Bhowmik)
Coordinator
GIMT-Guwahati

M Mallick

Dr. Debarshi Mallick
HoD, ME Department
GIMT-Guwahati
ANUBOSE INSTITUTE OF TECHNOLOGY

T. Nath

Prof. Thaleswar Nath
Principal,
GIMT-Guwahati
ANUBOSE INSTITUTE OF TECHNOLOGY

B. Phukan

Dr. B.R. Phukan
TEQIP Coordinator,
ASTU-Guwahati



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Bhadradi Kothagudem Dt, Telangana.



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : V. Ravindra Naik
- Designation : Assistant professor
- Department : ECE
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: Online Summer School - Com - faculty Development program on
..... Advances in signal processing & machine learning
- Duration of the Program : July 20th - July 26th, 2022
- Associating professional body : MHRD Institution
- Financial support particulars (Rs.) :
i. Registration Charges : 1000/-
ii. Travelling Allowance : -
iii. Membership Fee : -
iv. Others : -

Reuh

Signature of the faculty member

Date:

- Recommendations of the HoD: *[Signature]*
- Recommendations of the IQAC: *[Signature]*
- Recommendations of the Principal: *[Signature]*

Sanctioned/Not Sanctioned

Account Department

Accountant: *[Signature]*

Date: 19/07/2022

PRINCIPAL

ANUBOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadradi Kothagudem Dt, Telangana

PRINCIPAL

Anubose Institute of Technology
KSP Road, PALVONCHA,
Bhadradi Kothagudem Dist.

ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P. Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

V.No.....

Date: 13/07/2022

CASH VOUCHER

DEBIT

PARTICULARS	AMOUNT	
	Rs.	ps.
Online summer school - cum - faculty development program on advances in signal processing & machine learning V. Ravindra Naik	1000/-	

Cashier

Principal

Received Rs. 1000 (Rupees One Thousand) only
being

Principal

ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadradi Kothagudem Dt, Telangana.

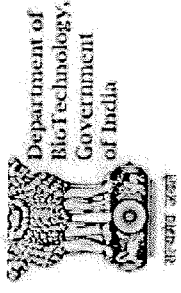
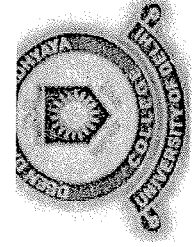
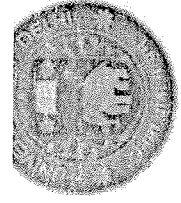
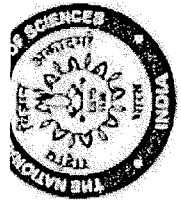


Ravindra Naik

Signature

Principal

Anu Bose Institute of Technology,
K.S.P. Road, PALVANCHA,
Bhadradi Kothagudem Dist.



Online Summer School-cum-Faculty Development Program
 on
Advances in Signal Processing and Machine Learning

Jointly organized by

MHRD-Institution Innovation Council, DDUC Chapter, Deen Dayal Upadhyaya College, University of Delhi
 (Under the aegis of DBT Star College Program)

Department of Electronic Science, University of Delhi & National Academy of Sciences India (NASI) – Delhi Chapter

Supported By

IEEE Electron Device Society (EDS), Delhi Chapter

July 20, 2022 – July 26, 2022

Certificate of Completion

This is to certify that

V. Ravindra Naik

ASSISTANT PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, ANUBOSE INSTITUTE OF TECHNOLOGY

Has Completed Online Summer School-cum-Faculty Development Program on Advances in Signal Processing and Machine Learning

[Signature]

[Signature]

[Signature]

Dr. Hem Chand Jain
 Officiating Principal
 Deen Dayal Upadhyaya College
 University of Delhi

Professor Mridula Gupta, SMIEEE, FIETE
 Chairperson-IEEE EDS Delhi Chapter
 Head, Department of Electronic Science
 University of Delhi South Campus, New Delhi 110021, India

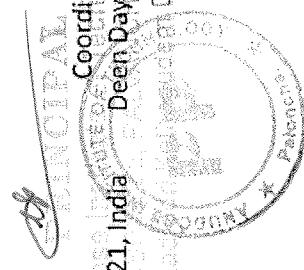
Dr. Manoj Saxena, MNASc
 Convener-Summer School


Coordinator
 DBT Star College Program (DDU)
 Deen Dayal Upadhyaya College, University of Delhi

Registered Email ID of Attendee : swpatra@gmail.com
 Certificate No. 2022/MHRD/ICDDUC/Online Summer School/July/76

PRINCIPAL
ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P. Road, Palvoncha,
 Bhadradi Kothagudem Dt, Telangana.



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	College PP	Counselling BOSE
CODE		

Financial Support Request Letter

1. Name of the faculty member : ...T. Venkata Karunakar.....
2. Designation :Assistant professor.....
3. Department :MBA.....
4. Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details:.....Technologies for societal needs Real time applications of AI, ML and Research Initiative.....
5. Duration of the Program : 01-08-2022 to 07-08-2022.....
6. Associating professional body : North Eastern Regional Institute.....
7. Financial support particulars (Rs.) :
 i. Registration Charges :1000/-.....
 ii. Travelling Allowance :
 iii. Membership Fee :
 iv. Others :


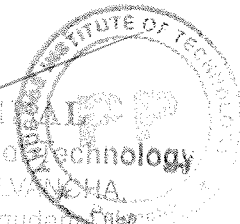
Date: _____ **Signature of the faculty member** Utkar

1. Recommendations of the HoD: [Signature]
2. Recommendations of the IQAC: [Signature]
3. Recommendations of the Principal: [Signature]

✓
Sanctioned/Not Sanction

Account Department

Accountant: [Signature]
Date: 01/08/2022



 ANUBOSE INSTITUTE OF TECHNOLOGY
 K.S.P. Road, Palvoncha, Bhadradri Kothagudem Dt, Telangana.

ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

V.No.....

Date: 25/07/2022

CASH VOUCHER

DEBIT.....

PARTICULARS	AMOUNT	
	Rs.	ps.
Technologies for societal needs real time application of AI, ML & research Initiatives T.Venkata Karunakar	1000/-	

Cashier

[Signature]
Principal

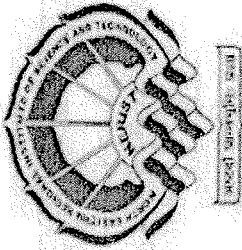
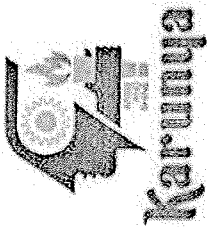
[Signature]
PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVONCHA,
Bhadradi Kothagudem Dist.

Received Rs. 1000 (Rupees One Thousand
rupees only)
being.....

PRINCIPAL
ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadradi Kothagudem Dt, Telangana.



Venkata Karunakar
Signature



CERTIFICATE OF PARTICIPATION

This is to certify that T. Venkata karunakar from

Anubose Institute of Technology participated in the
One Week Online International Faculty Development Program on "Technologies for Societal Needs
- Real time applications of AI, ML and Research initiatives" jointly organized by Department of
Computer Science and Engineering, Karunya Institute of Technology and Sciences, Tamil Nadu, India and
Department of Computer Science and Engineering, North Eastern Regional Institute of Science and
Technology, Arunachal Pradesh, India from 01-08-2022 to 07-08-2022.

P. Lalitha

Dr. P. Getzi Jeba
Organizing Secretary

K. Ramakrishna

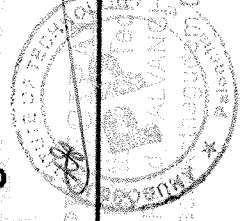
Dr. K. Ramalakshmi
Co-Convenor

Dr. J. Immanuel Johnraja


Dr. Moirangthem Marjit Singh
Convenor & HoD (CSE), NERIST

Dr. J. Immanuel Johnraja

Dr. J. Immanuel Johnraja
HoD (CSE), KITS



PRINCIPAL Anubose
ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palyoncha, Sharda,
Kothagudem Dt, Telangana

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	College PP	Counselling BOSE
CODE		

Financial Support Request Letter

1. Name of the faculty member : MD. Tajudin.....
2. Designation : Assistant professor.....
3. Department : ECE.....
4. Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details: ..Online.. Summer.. School-cum-faculty.. Development.. program on
 ..Advances.. in.. signal.. processing.. &.. Machine.. learning.....
5. Duration of the Program : ..20th - 26th July, 2022.....
6. Associating professional body : ..Deen Dayal Upadhyaya College.....
7. Financial support particulars (Rs.) :

 - i. Registration Charges : 1000/-.....
 - ii. Travelling Allowance : -.....
 - iii. Membership Fee : -.....
 - iv. Others : -.....

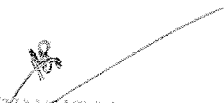
Date: _____ **Signature of the faculty member** _____

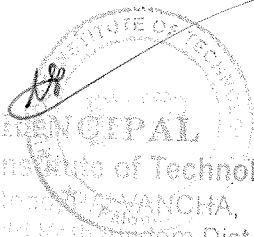
1. Recommendations of the HoD: _____
2. Recommendations of the IQAC: _____
3. Recommendations of the Principal: _____

Sanctioned/Not Sanction

Account Department

Accountant: _____
Date: 19/07/2023


 PRINCIPAL
 ANUBOSE INSTITUTE OF TECHNOLOGY
 K.S.P. Road, Paloncha,
 Bhadradri Kothagudem Dt, Telangana


 PRINCIPAL
 Anubose Institute of Technology
 KSP Road, PALONCHA,
 Bhadradri Kothagudem Dist.

ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P. Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

V.No.....

Date: 13/07/2022

CASH VOUCHER

DEBIT

PARTICULARS	AMOUNT	
	Rs.	ps.
Online summer school-cum-faculty development program on advances in signal processing & machine learning MD. Tajudin	1000/-	

Cashier

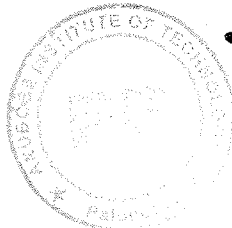
18

Principal

Received Rs. 1,000 (Rupees One Thousand) Rupees only being

PRINCIPAL

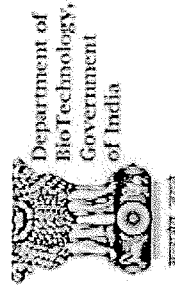
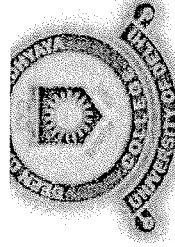
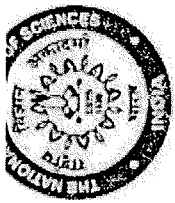
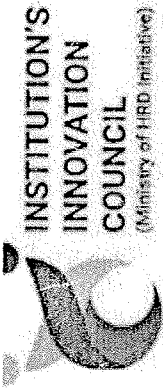
ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadradi Kothagudem Dt, Telangana.



Tajudin
Signature

PRINCIPAL

Anu Bose Institute of Technology
K.S.P. Road, PALVONCHA,
Bhadradi Kothagudem Dist



Online Summer School-cum-Faculty Development Program



on

Advances in Signal Processing and Machine Learning

Jointly organized by

MHRD-Institution Innovation Council, DDUC Chapter, Deen Dayal Upadhyaya College, University of Delhi

(Under the aegis of DBT Star College Program)

Department of Electronic Science, University of Delhi & National Academy of Sciences India (NASI) – Delhi Chapter

Supported By

IEEE Electron Device Society (EDS), Delhi Chapter

July 20, 2022 – July 26, 2022

Certificate of Completion

This is to certify that

MD. Tajudin

ASSISTANT PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, ANUBOSE INSTITUTE OF TECHNOLOGY

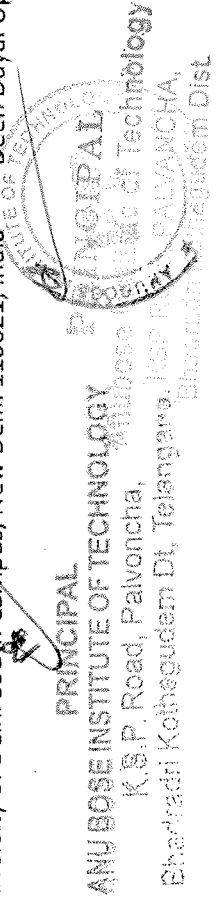
Has Completed Online Summer School-cum-Faculty Development Program on Advances in Signal Processing and Machine Learning

Dr. Hem Chand Jain
Officiating Principal
Deen Dayal Upadhyaya College
University of Delhi

Professor Miridula Gupta, SMIEEE, FIETE
Chairperson-IEEE EDS Delhi Chapter
Head, Department of Electronic Science
University of Delhi South Campus, New Delhi 110021, India

Dr. Manoj Saxena, MNASC
Convener-Summer School
Coordinator-DBT Star College Program (DDU)
Deen Dayal Upadhyaya College, University of Delhi

Registered Email ID of Attendee : swpatra@gmail.com
Certificate No. 2022/MHRDI/CDDUC/Online Summer School/July/76





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(An ISO 9001:2015 Certified Institution) Estd: 2008)

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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : P. Sudheer Kumar
- Designation : Assistant professor
- Department : Mechanical
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: The 7-days Faculty development program on
..... Machine learning
- Duration of the Program : 22th to 23th March - 2022
- Associating professional body : Assam Science & Technology University
- Financial support particulars (Rs.) :
i. Registration Charges : 1000/-
- ii. Travelling Allowance : -
- iii. Membership Fee : -
- iv. Others : -

Date: _____ Signature of the faculty member

1. Recommendations of the HoD: _____

2. Recommendations of the IQAC: _____

3. Recommendations of the Principal: _____

Sanctioned/Not Sanction

Account Department

Accountant: _____

Date: 27/03/2022

PRINCIPAL
ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadradri Kothagudem Dt, Telangana. PALVANCHA,
Bhadradri Kothagudem Dist.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

Date: 15/03/2022

V.No:

CASH VOUCHER

DEBIT.....

PARTICULARS	AMOUNT	
	Rs.	ps.
The 7-days faculty development program on Machine learning P. Sudheer Kumar	1000/-	

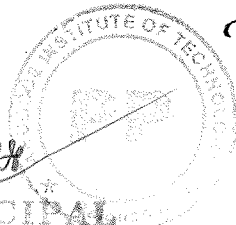
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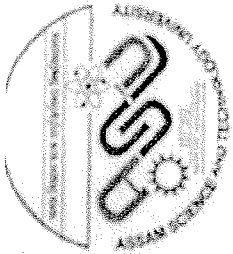
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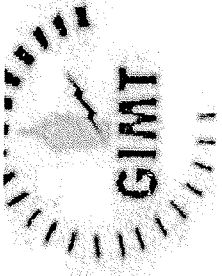
P. Sudheer Kumar
Signature

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Certificate of Participation

This is to certify that

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of

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(Dr. Sangeeta Das)
Coordinator
GIMT-Guwahati

Rajib Bhowmik

(Mr. Rajib Bhowmik)
Coordinator
GIMT-Guwahati

Dr. Debajit Matlick

Dr. Debajit Matlick
HOD, ME Department
GIMT-Guwahati
K.S.P. Road, Palvonicha,
Bhadradi Kothagudem Dt, Telangana.

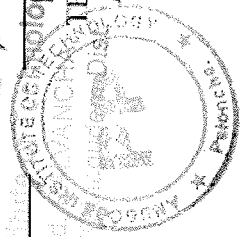
Thuleswar Nath

Prof. Thuleswar Nath
Principal,
GIMT-Guwahati

B.R. Phukan

Dr. B.R. Phukan
TEQIP Coordinator,
ASTU-Guwahati

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Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

1. Name of the faculty member : R. Sekhara Babu

2. Designation : Assistant professor

3. Department : EEE

4. Conference/Publication/ Membership Fee/ Workshop /FDP Certificate

Details: The webinar series on Recent Research Trends in Tribology

5. Duration of the Program : 3rd to 9th August, 2022

6. Associating professional body : Assam Science and Technology University

7. Financial support particulars (Rs.)

i. Registration Charges : 1000/-

ii. Travelling Allowance :

iii. Membership Fee :

iv. Others :

Date: _____ Signature of the faculty member Sekhar

1. Recommendations of the HoD: [Signature]

2. Recommendations of the IQAC: [Signature]

3. Recommendations of the Principal: [Signature]

Sanctioned/Not Sanctioned

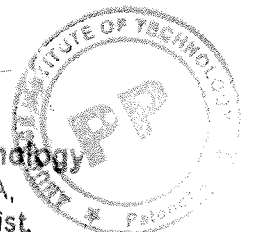
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Accountant: [Signature]

Date: 02/08/2022

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Date: 27/07/2022

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Principal

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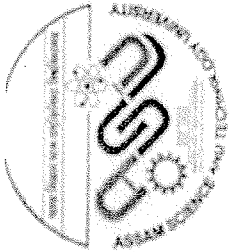
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Sekhar
Signature



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(Dr. Sangeeta Das)
Coordinator
GIMT-Guwahati

Rhowmik

(Mr. Rajib Bhowmik)
Coordinator
GIMT-Guwahati

Dr. D. Gurshi Mallick

Dr. D. Gurshi Mallick
HOD, M.E. Department
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Principal,
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Dr. B.R. Phukan
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CODE	College	Counselling
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Financial Support Request Letter

- Name of the faculty member : ... M.D. Gausiyas Begum ...
- Designation : ... Assistant professor ...
- Department : ... EEE ...
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: ... the webinar series on Recent Research Trends
... in Tribology ...
- Duration of the Program : ... 3rd to 9th August 2022 ...
- Associating professional body : ... Jhulelal Institute of Technology ...
- Financial support particulars (Rs.) :
i. Registration Charges : ... 1000/- ...
ii. Travelling Allowance : ...
iii. Membership Fee : ...
iv. Others : ...

Date:

Gausiyas
Signature of the faculty member

- Recommendations of the HoD: *Auf*
- Recommendations of the IQAC: *Neeraj*
- Recommendations of the Principal: *H*

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Date: 26/07/2022

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PARTICULARS	AMOUNT	
	Rs.	ps.
The webinar series on recent research trends in Tribology	1000/-	
MD. Gousiya Begum		

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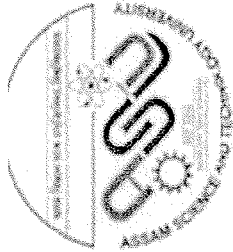
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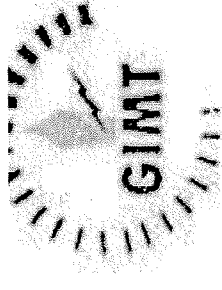


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Certificate of Participation

This is to certify that

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(Dr. Sangeeta Das)
Coordinator
GIMT-Guwahati

Rajib Bhowmik

(Mr. Rajib Bhowmik)
Coordinator
GIMT-Guwahati

Dr. Debarshi Mallick

Dr. Debarshi Mallick
HoD, ME Department
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QIP Coordinator,
ASTU-Guwahati





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Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : ... M. Karalaxmi
- Designation : ... Assistant professor
- Department : ... ECE
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: ... Online Summer School - cum - Faculty Development
... program on Advances in Signal processing & Machine
... learning
- Duration of the Program : ... 20th - 26th July, 2022
- Associating professional body : ... Deen Dayal Upadhyay's College
- Financial support particulars (Rs.) :

 - Registration Charges : ... 1000/-
 - Travelling Allowance : ... -
 - Membership Fee : ... -
 - Others : ... -

Ueure

Date: _____ Signature of the faculty member

- Recommendations of the HoD: *[Signature]*
- Recommendations of the IQAC: *[Signature]*
- Recommendations of the Principal: *[Signature]*

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Account Department

Accountant: *[Signature]*

Date: 12/07/2022

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V.No.....

Date: 13/07/2022

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PARTICULARS	AMOUNT	
	Rs.	ps.
Online summer school-cum-faculty development program on advances in signal processing & machine learning M. Varalaxmi	1000/-	

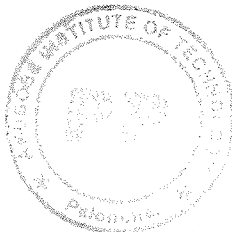
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Principal

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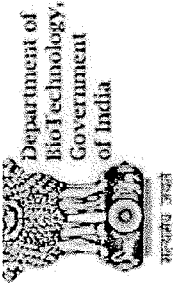
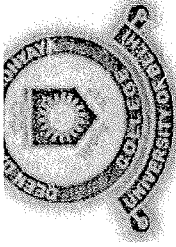
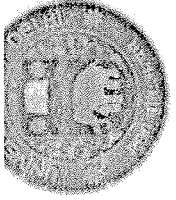
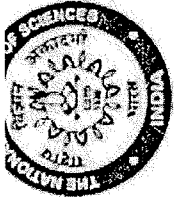
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Varalaxmi
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Online Summer School-cum-Faculty Development Program



on

Advances in Signal Processing and Machine Learning

Jointly organized by

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(Under the aegis of DBT Star College Program)

Department of Electronic Science, University of Delhi & National Academy of Sciences India (NASI) – Delhi Chapter

Supported By

IEEE Electron Device Society (EDS), Delhi Chapter

July 20, 2022 – July 26, 2022

Certificate of Completion

This is to certify that

M. Vaxalaxmi

ASSISTANT PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, ANUBOSE INSTITUTE OF TECHNOLOGY

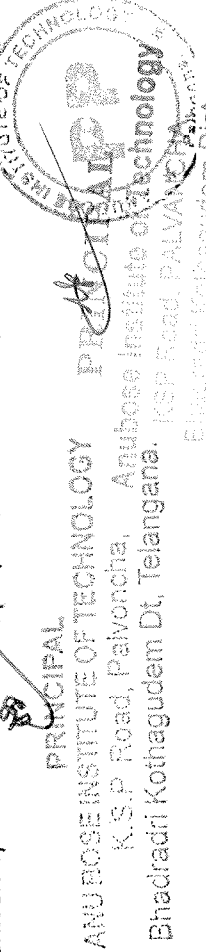
Has Completed Online Summer School-cum-Faculty Development Program on Advances in Signal Processing and Machine Learning

Dr. Hem Chand Jain
Officiating Principal
Deen Dayal Upadhyaya College
University of Delhi

Professor Mridula Gupta, SMIEEE, FIETE
Chairperson-IEEE EDS Delhi Chapter
Head, Department of Electronic Science
University of Delhi South Campus, New Delhi 110021, India

Dr. Manoj Saxena, MNASc
Convener-Summer School
Coordinator-DBT Star College Program (DDU)
Deen Dayal Upadhyaya College, University of Delhi

Registered Email ID of Attendee : swpatra@gmail.com
Certificate No. 2022/MHRD/ICDDUC/Online Summer School/July/76



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CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : *K. Nagendra Babu*
- Designation : *Assistant professor*
- Department : *Mechanics*
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: *Online Summer School - cum - faculty Development
program on Advances in Signal processing and Machine
learning*
- Duration of the Program : *July 20, 2022 - July 26, 2022*
- Associating professional body : *Deen Dayal Upadhyaya College*
- Financial support particulars (Rs.)
 - Registration Charges : *1000/-*
 - Travelling Allowance :
 - Membership Fee :
 - Others :

Date:

NCB

Signature of the faculty member

- Recommendations of the HoD: *Ney*
- Recommendations of the IQAC: *Ney*
- Recommendations of the Principal: *[Signature]*

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Account Department

Accountant: *[Signature]*

Date: *14/03/2022*

[Signature]
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Date: 15/03/2022

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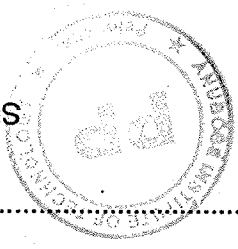
PARTICULARS	AMOUNT	
	Rs.	ps.
Online summer school - cum - faculty Development program on Advances in signal processing and machine learning K. Nageswari Babu	1000/-	

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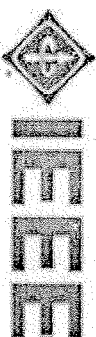
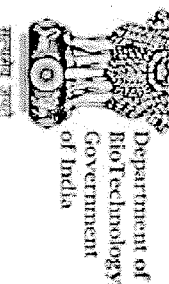
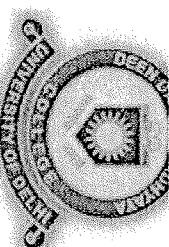


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on

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Supported By

IEEE Electron Device Society (EDS), Delhi Chapter

July 20, 2022 – July 26, 2022

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ASSISTANT PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, ANUBOSE INSTITUTE OF TECHNOLOGY

Has Completed Online Summer School-cum-Faculty Development Program on Advances in Signal Processing and Machine Learning

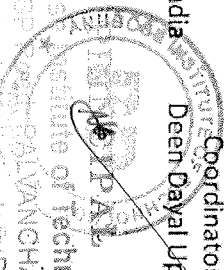
Dr. Hem Chand Jain
Officiating Principal
Deen Dayal Upadhyaya College
University of Delhi

Professor Mridula Gupta, SMIEEE, FIETE
Chairperson-IEEE EDS Delhi Chapter
Head, Department of Electronic Science
University of Delhi South Campus, New Delhi 110021, India

Dr. Manoj Saxena, M/NASC
Convener-Summer School
Coordinator-DBT Star College Program (DDU)
Deen Dayal Upadhyaya College, University of Delhi

Registered Email ID of Attendee : swprata@gmail.com
Certificate No. 2022/МНЯРДСВДУС/Online Summer School/July/75

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CODE	College	Counselling
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Financial Support Request Letter

1. Name of the faculty member : G. Deepthi

2. Designation : Asst. Professor

3. Department : ECE

4. Conference/Publication/ Membership Fee/ Workshop /FDP Certificate

Details: Comparative analysis of cross talk effects in Dielectric inserted horizontal and vertical multi-layer GNR

5. Duration of the Program :

6. Associating professional body :

7. Financial support particulars (Rs.) :

i. Registration Charges : 10,000/-

ii. Travelling Allowance :

iii. Membership Fee :

iv. Others :

Deepthi

Signature of the faculty member

Date: 2/Aug/2022

1. Recommendations of the HoD: [Signature]

2. Recommendations of the IQAC: [Signature]

3. Recommendations of the Principal: [Signature]

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Account Department

Accountant: [Signature]

Date: 2/08/2022

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Comparative Analysis of Crosstalk Effects in Dielectric Inserted Horizontal and Vertical Multi-layer GNR Interconnects for Ternary Logic System

Gurijala Deepthi² and Madhavi Tatineni

Department of Electronics and Communication Engineering, GITAM Deemed to be university, Visakhapatnam, 530045, India

In this work, the performance of copper (Cu), dielectric inserted horizontal graphene nanoribbon (Di-HGNR) interconnect and dielectric inserted vertical graphene nanoribbon (Di-VGNR) interconnects are investigated using active shielding and passive shielding techniques. However, the analysis is carried out by adapting driver-interconnect-load system. This analysis considers the interconnect length from 500 to 2000 μm for 10 nm technology node. Further, the crosstalk induced effects on various interconnect structures are examined. It is envisaged that Di-VGNR exhibits lowest propagation delay compared to Cu and Di-HGNR. Further, the in-phase and out-phase crosstalk delay among the coupled interconnect lines is determined. It is investigated that active shielded Di-VGNR has least crosstalk induced delay compared to other interconnect structures considered in this study. Therefore, Di-VGNR interconnects outperforms Cu and Di-HGNR and are best suited for future VLSI interconnects.
 © 2022 The Electrochemical Society ("ECS"). Published on behalf of ECS by IOP Publishing Limited. [DOI: 10.1149/2162-8777/ac8373]

Manuscript submitted June 25, 2022; revised manuscript received July 15, 2022. Published August 2, 2022.

As the technology scales down from micro to nano scale, interconnect delays are more prevailing than the gate delays. At high frequencies inductive and capacitive natures of interconnect conductors dominate the resistive nature.¹⁻³ These impedance parameters are susceptible to the choice of interconnect material and also have a significant impact on crosstalk effects and speed of the circuitry. The best choice for building the interconnects are metals and their alloys due to their high electrical conductivity. Right from the start of semiconductor industry, aluminum and copper were the most common metals. But in today's higher data rate applications, Cu suffers from higher resistivity due to significant effects of joule heating, grain and surface scattering.^{4,5} Also due to the increasing demand for integration, performance and reliability, a handful of various interconnect materials like carbon nanotubes (CNTs) and graphene nanoribbons (GNRs) are used.⁶⁻¹² CNTs and GNRs play a pivotal role during the relentless drive insight of miniaturization scale, owing to their remarkable structural properties. CNT appears in the form of cylindrical carbon molecules exhibiting greater mechanical strength, possess unique electrical properties and are efficient conductors of heat.¹²⁻¹⁴ The intrinsic properties of CNT becomes useful in solving the problems that are plaguing the metallic interconnects and proves to be an appropriate interconnect material for nanoelectronic devices. CNT comes in a couple of varieties as SWCNTs and MWCNTs. SWCNT is just a single cylindrical wall possessing both metallic and semiconductor properties whereas MWCNTs which are always metallic in nature are composed of multiple concentric cylinders.¹⁴

GNRs are planar, quasi 1-D graphene structures which can be patterned using high resolution lithography. However, GNRs are believed to be more controllable from fabrication point of view due to its planar nature and are much preferred than CNTs.⁷ GNRs can be categorized based on their chirality as either metallic or semiconductive in nature. The most preferred one for interconnect application is the metallic type. GNRs are also associated with single layer GNR (SLGNRs) and multilayer GNRs (MLGNRs). Due to high resistance in SLGNRs, they are not considered as feasible materials for interconnect applications. Coming to the GNR metal contacts, it can be realized either by top contact MLGNRs (TC-MLGNRs) or side contact MLGNRs (SC-MLGNRs). Further, the presence of multiple parallel conduction paths in SC-MLGNR reduces the equivalent resistance making it well suited for interconnect applications compared to TC-MLGNR interconnects. However, the concept of interlayer electron hopping confines the

number of stacked GNR layers in SC-MLGNR. This arises from the point of modulation in carbon-carbon bond lengths by the elastic strain produced in the stacked layers.¹⁵ Hence, results in the reduction of mean free path (MFP) causing increase in the equivalent resistance of SC-MLGNR. The adverse effects of interlayer electron hopping can be eased off by the introduction of high- k material among the subsequent layers of GNR which leads to improved MFP and reduced equivalent resistance.^{16,17} Though researchers have provided a bountiful of work on these novel materials but still the performance of CNTs and GNRs in various applications is a big challenge. Therefore, the efficaciousness of dielectric inserted GNR (Di-GNR) can be enriched by placing it either in vertical direction or in horizontal direction. Horizontal GNRs (HGNRs) perpetuate high level of performance, but the critical issues emerging in HGNRs are related to thermal challenges and the problems which arouse related to electrical contact. Due to anisotropic property, they may hinder heat dissipation in vertical direction. Unlike HGNRs, VGNRs have high electrical conductivity as each and every layer take part in electron transport.¹⁸ Also, VGNR can offer a promising solution towards the thermal challenges tackled by the HGNR.

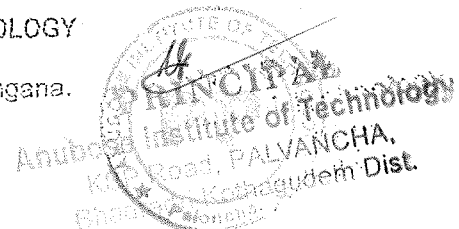
Traditionally switching theory was used for the digital design which is limited to two valued logics. Thereby the complexity of interconnects is increased due to the transmission of limited amount of information.¹⁹ Consequently, for the breakthrough of bit density limit, multi valued logic (MVL) has been brought to play. Owing to this, connections among the chip can be reduced and subsequently more amount of data can be transacted. In ternary logic system the radix is three and the value is nearer to $e = 2.718$.²⁰ In ternary logic system, the logic levels are characterized by three voltage levels as 0, $0.5V_{dd}$ and V_{dd} which are equivalent to logic 0, logic 1 and logic 2 levels, respectively. Optimization of radix can avoid the hardware implementation issues. Conversely, the ternary logic system is more vulnerable to noise and crosstalk effects due to its lower noise margin.

As a consequence of this, there has been a recurring interest in the topics of interconnect widening, buffer insertion and shielding for the analysis of crosstalk effects.²¹⁻²⁹ Crosstalk refers to the unintentional effects which causes functional failure in the chip. Due to capacitive coupling, the signal switching in one net causes effect on the neighboring one. This in turn causes noise and delay across the interconnect lines. Hence, it can be compensated by introducing a shield wire between the aggressor line and the victim line. It can be of two ways active and passive. In active shielding a dedicated shield line is used and its switching pattern depends on the switching of adjacent lines. Crosstalk noise and delay can be reduced effectively in active shielding but due to high switching activity, more amount

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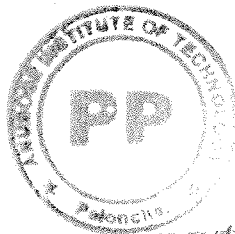
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Financial Support Request Letter

1. Name of the faculty member : ... Dr. N. V. Subb. Reddy

2. Designation : ... Assistant Professor

3. Department : ... EEE

4. Conference/Publication/ Membership Fee/ Workshop /FDP Certificate

Details: ... An implementation of S.S.C - Based power flow enhancement in transmission lines

5. Duration of the Program :

6. Associating professional body :

7. Financial support particulars (Rs.) : ... \$


i. Registration Charges : ... 1000/-

ii. Travelling Allowance :

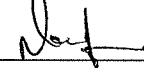
iii. Membership Fee :


iv. Others :

Date: 2/ Feb/ 2022


Signature of the faculty member


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2. Recommendations of the IQAC: 


3. Recommendations of the Principal: 

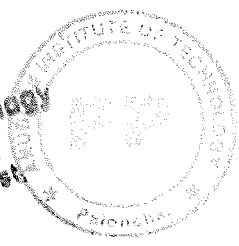
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An Implementation of SSSC-Based Power Flow Enhancement in Transmission Lines

Dr.N.V.Subba Rao

Anu Bose Institute of Technology, Paloncha, India

nvsubbarao@rediffmail.com

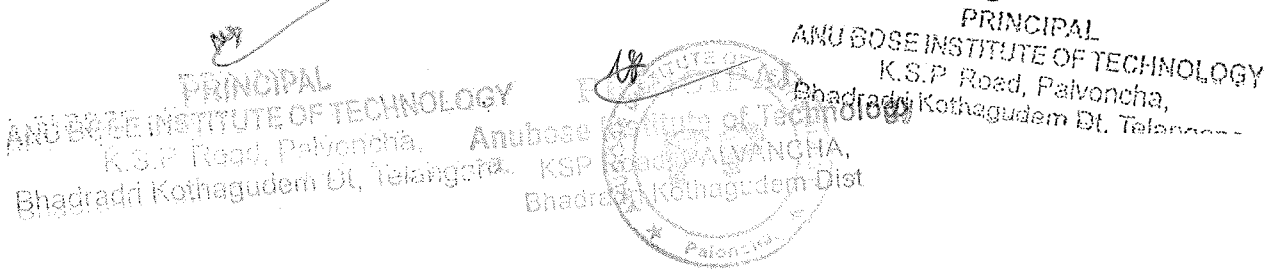
ABSTRACT

The SSSC is used to operate the transmission line at stable condition. Various schemes of SSSC is already proposed. And a still lot of scope is left in this development to make it more efficient in terms of voltage stress, switching losses and harmonic suppression. The scheme proposed here is used to produce a H - bridge based cascaded inverter. It can produce the pulses with different width and magnitude. It almost resembles a sine wave at the output of transformer. The output results are compared with conventional type SSSC with cascaded type. This paper solved only the simulation part of this work. In the proposed work a scale down model of the SSSC can be proposed to build. This scale down model can be used to analysis the different situations like voltage compensation part. The sample of voltage and current will be taken from the transmission line and is given to the processor to decide the angle of firing for the thyristor device. A cascaded type H - bridge based multilevel inverter is used as SSSC. Three DC separate DC sources are used here as energy source for the SSSC. These three DC source can be interfaced with SSSC in a sequential manner as explained in different modes. This sequential switching is helpful in reducing switching losses and switching stresses.

1.INTRODUCTION

Static Synchronous Series Compensator (SSSC) is a FACTS device which has its wide range of applications in transmission lines and grids. In power system, several FACTS devices such as STATCOM, static VAR compensator, UPFC, TCSC etc are employed in maintaining power stability and

operate the transmission system as reliable one. Based on the performance of these FACTS devices flickering and SSR can also be reduced in transmission grids. SSSC based hybrid series compensation scheme combine the capacitor action with TCSC and SSSC are employed in damping of SSR(sub-synchronous resonance)[1]. In this hybrid



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An implimentation of sssc-Based power flow enhancement in transmission lines. (Dr. N.V. Subba Rao)	1000/-	

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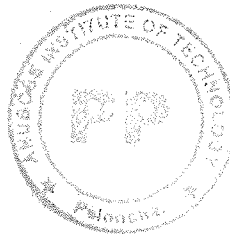
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Financial Support Request Letter

1. Name of the faculty member : G. Deepthi
2. Designation : Assistant professor
3. Department : G.C.E.
4. Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details: Quality of signal improvement in prominent
 CNTFET Based Ternary logic system
5. Duration of the Program :
6. Associating professional body :
7. Financial support particulars (Rs.) :

 - i. Registration Charges : 1000/-
 - ii. Travelling Allowance :
 - iii. Membership Fee :
 - iv. Others :

Date: 24/march/2022

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Quality of Signal Improvement in Prominent CNTFET Based Ternary Logic System for Futuristic Dielectric Inserted MLGNRs for Integrated Circuit Designs

Gurijala Deepthi^z and Madhavi Tatineni

Department of Electronics and Communication Engineering, GITAM Deemed to be university, Visakhapatnam 530045, India

Booming VLSI technology has graciously facilitated down-scaling dimensions of on-chip devices and interconnects in integrated circuits (ICs) to nano-miniaturized scale. However, at nano-dimensions where added benefits of scaling are constrained by associated highly-dense on-chip nano-interconnect structures, their electro-migration effects and several limiting signal-integrity issues. These cumulatively affect the quality of signal (QoS) at output. Improving output QoS is essential for attaining faithful system performance. The present paper judiciously attempts to address as well as limit this graving issue and is successfully proven by obtained results. Firstly, different structures and their performance of futuristic graphene based multi-layer graphene nano ribbon (MLGNR) interconnect is investigated. These include basic MLGNR and dielectric inserted side contact MLGNR (DS-MLGNR). Secondly, to improve data rates and performance, efficient and novel carbon nanotube field effect transistors (CNTFETs) based ternary logic system is incorporated for the prominent nano-MLGNR interconnects. Thirdly, QoS enhancement of highly potential DS-MLGNR interconnect is proposed using active shielding technique. Finally, is chase to further enrich QoS, adaptive least mean square (LMS) equalization technique is used at the receiver. The proposed work comprising of futuristic novel graphene interconnects with efficient ternary logic system together with adaption of several QoS improvement techniques are magnificent and panacea solution to limiting nano-interconnects in advanced ICs. Several interesting and seminal analyses such as delay, power, power-delay product, crosstalk, eye-diagram are performed that supports the novelty and effectiveness of the proposed work. The vivid performance analyses have been implemented at nano-size 10 nm technology node.
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On-chip interconnects are paramount connecting structures in integrated circuits (ICs) alike street and highways linking each and every parts within and outside the country. Over time, continuous increase in vehicles on physical roads has adversely affected the traffic, reach out time, pollution and several other issues. The on-chip interconnects could not remain aloof of the similar effects (as experienced in physical roads) wherein scaling of technology has resulted in smaller and dense interconnect structures that leads to congestion in signal movement, increased delay, elevated power dissipation, coupling, electro-migration and several signal integrity issues in ICs.^{1,2} The focus of the present paper is to energize and improve the performance of this nano on-chip interconnects that are also many times referred to as life-line of ICs.

The on-chip interconnects have been widely realized using copper (Cu) material. The Cu on-chip interconnects offer economical and good conductivity those are essentially needed for large scale production and to attain good performance in ICs. However, with miniaturization of technology at nano-dimensions, Cu on-chip interconnects are constraint by several non-ideal effects such as surface scattering, grain boundaries, hillock formation, Joule heating, electro-migration issues which in-turn lowers on-chip conductivity significantly.^{3,4} Hunt for high performance connectivity has triggered revolutionary researches globally.⁵⁻⁸ One of the prospective outcomes of these researches have been proposed to replace Cu with fascinating graphene material.^{9,10} Graphene can be patterned in different structures as its circular form referred to as carbon nanotube (CNT) and planar form as graphene nano-ribbon (GNR). CNT and GNR possess huge potential and are prospective on-chip interconnects due to their inimitable electrical, physical, thermal and mechanical properties which include larger mean free path (MFP), higher thermal conductivity and better current carrying capability.⁹ GNRs are easier to fabricate due to their planar structure over cylindrically patterned CNTs.¹⁰ Henceforth, GNRs are considered in the present paper.

The GNR interconnects can be classified as single layer GNR (SLGNR) and multilayer GNR (MLGNR) depending on the number of layers present. SLGNRs possess single layer while MLGNRs

constituted of multiple layers of nano-graphene ribbons. Owing to presence of only single layer in SLGNR, its resistance is too high to be considered as for on-chip interconnect applications. Henceforth, SLGNRs are not preferred. On the other hand, MLGNR attributes lower resistance due to parallel configured graphene ribbons which effectively reduce the equivalent resistance. Hence, MLGNR are favorably good for the present interest of research work. The connection to MLGNR can be made through either side ways that is referred as side-contacted MLGNR (SC-MLGNR) or from top termed as top-contacted MLGNR (TC-MLGNR).^{11,12} SC-MLGNR is mostly preferred due to lesser resistance owing to presence of multiple parallel conduction paths. However, the concept of inter-layer electron hopping confines the number of stacked GNR layers in SC-MLGNR. This arises due to the modulation in carbon-carbon bond lengths by the elastic strain produced in the stacked layers.¹³ This effectively causes reduction of MFP that result in increased equivalent resistance of SC-MLGNR. The diminishing effects of interlayer electron hopping can be significantly mitigated by introduction of high-k dielectric material between the subsequent layers of graphene nano ribbons.^{14,15} This structure is referred to as dielectric inserted SC-MLGNR (DS-MLGNR). Introduction of dielectric between graphene layers prevents its conversion into graphite, thereby improving its further MFP and lowering scattering rate which results in increase in conductivity in each graphene layer. Henceforth, prominent DS-MLGNR interconnects is considered in the current paper.

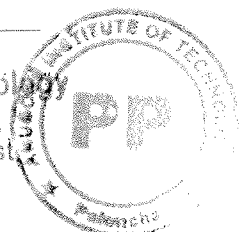
The on-chip interconnects are often driven and loaded with silicon based CMOS inverters that operate in binary logic (2-level) system. However, to improve the system performance in terms of higher bandwidth, multi-value logic (MVL) such as ternary logic system can be incorporated.¹⁶⁻²⁴ Ternary logic system is an eminent approach for sending more number of bits/information per transmission which enables attaining higher throughput.¹⁶ In Ref. 17, a resistive based MVL system using CNTFET for realization of standard ternary inverter (STI) is presented. However, feasibility to integrate large resistors along with carbon nanotube field effect transistor (CNTFET) technology is challenging and more space consuming. This leads to increased area overheads and more power consumption. Sheng Lin et al. have proposed a novel structure of CNTFET based STI that contains no lumped resistive element.¹⁸

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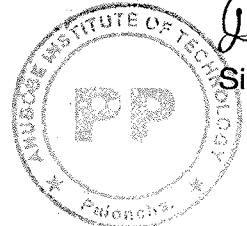
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Financial Support Request Letter

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3. Department : Mechanical
4. Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details: Webinar series on recent Research trends in
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5. Duration of the Program : 3rd to 9th August, 2022
6. Associating professional body : Assam science and technology university
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 - ii. Travelling Allowance : -
 - iii. Membership Fee : -
 - iv. Others :

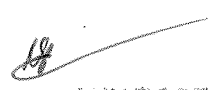
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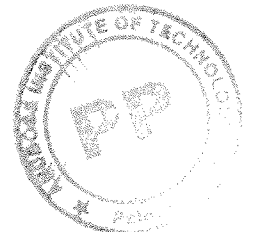
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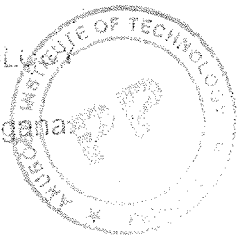
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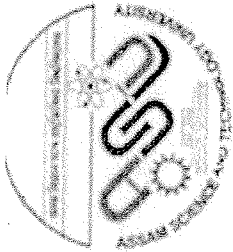
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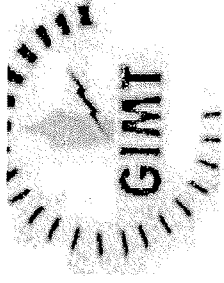


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(Dr. Sangeeta Das)
Coordinator
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Rajib Bhowmik

(Mr. Rajib Bhowmik)
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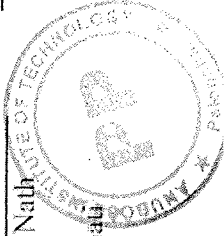
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- Department : CSE

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Details: Smart Grid are able to provide optimal energy trading with demand responses via the use of cloud computing enabled virtual power plants

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- Associating professional body :
- Financial support particulars (Rs.) :

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 - Travelling Allowance :
 - Membership Fee :
 - Others :

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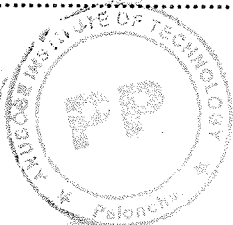
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Smart grids are able to provide optimal energy trading with demand responses via the use of cloud computing enabled virtual power plants.

Dr. A. Avani

Assistant Professor, Department of CSE, Anu Bose Institute of Technology, Paloncha, India

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ABSTRACT

The growing penetration of energy from renewable sources and electric vehicles (EVs) presents a substantial challenge for something like the operator of the power grid in the form of an increase in peak demand as well as a loss in power quality. In addition, there is an increasing need for services that provide fast charging in smart grids. It might be difficult to keep up with the ever-increasing demand for quick charging services. In order to overcome this obstacle, we present in this article a new data mining architecture for managing virtual power plants (VPPs) in smart grids that is founded on cloud computing and combines commodities trading with services provision. This architecture is designed to manage virtual nuclear reactors. By buying electricity via an online energy trading platform hosted in the cloud, the planned system would enable electric vehicles (EVs) to be fueled at increased power rates without having an impact on the functioning of the power grid.

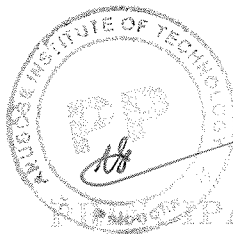
In addition, customers that own storage devices have the ability to sell any excess energy they produce to the market. The energy trading platform may, on the one hand, be seen of as a domestic market that is part of the VPP and is designed to increase that organization's income. On the other hand, it is in the owners of electric vehicles (EVs) best interest to keep the cost of charging as low as possible. Because of this, we represent the interactions that take place seen between EV owners and the VPP as a competitive game rather than a cooperative one.

In order to find the Nash equilibrium (NE) of the game, we first need to create an algorithm and then investigate the amount of computation and communication overhead it entails. In order to test the effectiveness of the proposed algorithm, we make use of actual data provided by the California Independent System Operator (CAISO). According to the findings of our research, users who own merely storage devices have the potential to receive an average income increase of approximately 200% by participating in the suggested single market. In addition, customers who use just electric vehicles may cut their charging expenses by roughly half, on average. Consumers that own electric vehicles as well as storage devices are able to cut their charging expenses even lower, by about 120%; in this scenario, users make a profit by making advantage of the single market.

I. INTRODUCTION

The rising contribution of renewable energy resources in the distribution grid is being driven in large part by the environmental advantages and economic incentives provided by these resources.

New challenges to the reliable and stable operations of the power grid, particularly during peak hours, have been introduced as a result of the uncertainties associated with the production of green sources, the substantial increase in the capacity of





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CODE	College	Counselling
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Financial Support Request Letter

- Name of the faculty member : A. Avani
- Designation : Assistant professor
- Department : CSE
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: Operating on I-GMM-based data preparation approach on a cloud platform
- Duration of the Program :
- Associating professional body :
- Financial support particulars (Rs.) :

 - Registration Charges : 1000/-
 - Travelling Allowance :
 - Membership Fee :
 - Others :

Date: June /2022

Avani
Signature of the faculty member

1. Recommendations of the HoD: [Signature]

2. Recommendations of the IQAC: [Signature]

3. Recommendations of the Principal: [Signature]

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Account Department

Accountant: [Signature]

Date: 03/06/2022

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Operating an I-GMM - based data preparation approach on a cloud platform. (A. Avani)	1000/-	

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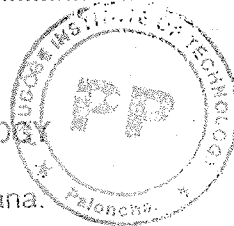
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Operating an I-GMM-based data preparation approach on a cloud platform

Dr. A. Avani

Assistant Professor, Department of CSE, Anu Bose Institute of Technology, Paloncha, India

avanialla@gmail.com

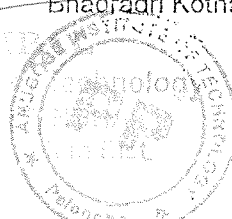
ABSTRACT

The amount of information that is kept in the sustainable energy cloud platform may be significantly improved via the use of pre-processing, which in turn makes the process of data analysis more effective. In this research, an updated GMM model is used to build a technique for the preparation of data pertaining to power grids. The solution is based upon the distributed architecture of cloud computing. This method starts with building a smart grid cloud platform on top of the cloud storage architecture. After that, you should execute feature extraction on the grid data that has been saved on the cloud platform. In order to finish the preparation of the data, the upgraded GMM model is employed to both categorise and preserve raw grid data. This brings the whole process full circle. Experiments have been conducted to prove, in the end, that this strategy is effective.

I. INTRODUCTION

The energy demand in a number of different places is expanding at a quick rate as a direct result of the rapid expansion of economic development. During the same time period, the growth and maturation of the Internet of Things technology has also led to its widespread use in the electrical grid. Because of all of these factors, the data included in the smart grid indicates a meteoric rise [1][2]. However, these data types are diverse [3,] the data sources are complex [4,] and there are also issues such as missing data, abnormal data, and inconsistent formats [5,] which cause significant disruption to power grid companies' data gathering and analysis. [As an example:] [As an example:] [As an example:] [As an example:] [Therefore, preparing the data in the smart grid, and then categorising and storing it, may significantly increase the data's overall quality while also reducing the amount of computation time and storage space that is necessary for data analysis. These have the potential to lower the costs associated with running the grid.

According to various processing aims, data preparation may be split into data cleaning, data integration, and data categorization, et cetera. The primary goal of data cleaning is to get rid of any errors, such as missing data, noise pollution, inconsistent formatting, and so on. For instance, Lin et al. [6] find a solution to the issue of anomalous converter status data by using a data cleaning method that is based on cluster analysis analysis and neural networks; and Lv et al. [7] propose a smart grid incomplete data verifying algorithm that is based on machine learning. Both of these methods are able to solve the problem. The primary goal of data integration is to transform information obtained from a wide variety of data sources into a form that can be stored consistently. For instance, Li et al. [8] presented a wide-area distributed power quality data integration architecture for power grids. This design would make use of distributed data integration technology as well as wide-area distributed backup and recovery technology. [Citation needed] The process of distinguishing data based on its origin, format, and application





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Financial Support Request Letter

1. Name of the faculty member : Dr. N.V. Subhasa

2. Designation : Assistant professor

3. Department : EE

4. Conference/Publication/ Membership Fee/ Workshop /FDP Certificate

Details: Using dynamic voltage restorer to enhance / improve
ment of power quality

5. Duration of the Program :

6. Associating professional body :

7. Financial support particulars (Rs.) :

i. Registration Charges : 1000/-

ii. Travelling Allowance :

iii. Membership Fee :

iv. Others :

Date: 04 April 2022

N.V. Sub
Signature of the faculty member

1. Recommendations of the HoD: Ashraf

2. Recommendations of the IQAC: Neel

3. Recommendations of the Principal: HR

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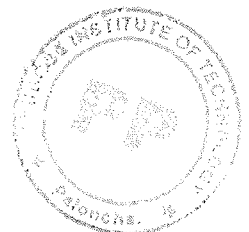
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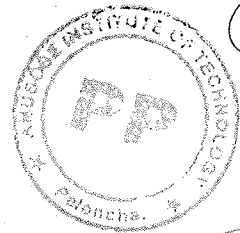
PARTICULARS	AMOUNT	
	Rs.	ps.
Using dynamic voltage restorer to enhance /improvement of power quality (Dr.N.v.Subba Rao)	1000/-	

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Using Dynamic Voltage Restorer To Enhance/ Improvement of Power Quality

Dr.N.V.Subba Rao

Professor, Department of EEE, Anu Bose Institute of Technology, Paloncha, India
nvsubarao@rediffmail.com

ABSTRACT:

Power Quality is a fundamental worry in the advanced power framework that can influence customers also, utility. The combination of environmentally friendly power sources, savvy lattice frameworks and broad utilization of force hardware gear led to heap issues in the cutting edge electric power framework. Current and voltage sounds, voltage list, and swell can harm the delicate hardware. These gadgets are vulnerable to input voltage varieties made by obstruction with different pieces of the framework. Subsequently, in the advanced age, with an expansion in delicate and costly electronic gear, power quality is fundamental for the power framework's dependable and safe activity. Dynamic Voltage Restorer (DVR) is a potential Distribution Flexible AC Transmission System (D-FACTS) gadget generally took on to conquer the issues of non-standard voltage, current, or recurrence in the appropriation matrix. It infuses voltages in the appropriation line to keep up with the voltage profile and guarantees consistent burden voltage. The reproductions were directed in MATLAB/Simulink to show the DVR-based proposed methodology's viability to smooth the twisted voltage because of music. A power framework model with a programmable power source is utilized to incorporate third also, fifth sounds. The frameworks' reaction for load voltage is assessed for with and without DVR situations. It has been noticed that the proposed DVR based system has actually dealt with the voltage mutilation, and a smooth remunerated load voltage was accomplished. The heap voltage THD rate was around 18% and 23% with addition third and fifth sounds in the stockpile voltage, separately. The incorporation of the proposed DVR has decreased THD around under 4% in the two cases.

INTRODUCTION:

Electrical Energy is imperceptible, a general ware that is promptly accessible in the vast majority of the world, and it has now been perceived as regular customer need [1]. Sustainable power Systems (RESs) is utilized to help the essential energy interest in sun oriented, Solar warm, wind energy, and so on. The discontinuous idea of RESs, sounds, what's more, receptive power issues end the power framework's execution by beginning steadiness worries in the power framework [2], [3]. The Flexible AC Transmission Systems (Realities) gadgets are broadly adjusted for responsive power remuneration, voltage steadiness, and power quality in dissemination matrices all over the planet [4], [5]. Notwithstanding, Reality

gadgets additionally change various boundaries on the transmission and appropriation framework [6]. This work presents an investigation of the power quality and focuses on recognizing the reasons for unfortunate power quality and give the answers for these power quality issues. Some gear like PCs, PCs, transfers, strong state gadgets, flexible speed drives, and optical gadgets are known as delicate gear. These gadgets are powerless to enter voltage varieties made by obstruction with different pieces of the framework.

The power framework is partitioned into the accompanying parts as age, transmission, conveyance, and by utilizing other transmission line power frameworks is taken care of to various burdens on the circulation side. Power



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Financial Support Request Letter

- Name of the faculty member : A. Avani
- Designation : Assistant professor
- Department : CSE

4. Conference/Publication/ Membership Fee/ Workshop /FDP Certificate

Details: Authentication upon that block chain that is completely anonymous together with addition to improvements with distributed generation of leading network

- Duration of the Program :
- Associating professional body :
- Financial support particulars (Rs.) :

 - Registration Charges : 1000/-
 - Travelling Allowance : -
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Date: July/2022

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Signature of the faculty member

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Authentication upon that Blockchain that Is Completely Anonymous Together with Addition To improvements with Distributed Generation Computation Offloading Network

Dr. A. Avani

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avanialla@gmail.com

ABSTRACT

Conventional virtualized smart grid systems have a number of significant obstacles, two of the most significant of which are obtaining low-latency and offering real-time services. As a result, there has been a growing tendency toward shifting toward edge computing. Existing cryptographic methods often do not enable conditional anonymous or flexible identity management, despite the fact that there have been a number of cryptographic protocols developed specifically to make it easier to maintain encrypted systems in smart grid networks.

In light of this, the purpose of this article is to provide a mutual authentication and key negotiation methodology for edge computing-based smart grid systems that is based on blockchain technology. Specifically, by using blockchain technology, the protocol is able to offer efficient conditional anonymity as well as access control. This is accomplished without the use of any extra complicated encryption algorithms. The security analysis demonstrates that the protocol delivers a fair degree of safety assurance. Furthermore, the comparison summary for both safety and efficacy hints that the suggested technique may have use in the context of a power system implementation.

I. INTRODUCTION

One of the many categories of the Industrial Internet of Things (IIoT), smart grid systems are among those that have the potential to increase the dependability, flexibility, and quality of energy distribution [1]. However, when the system grows in size (for example, as the number of customers grows), it may become more difficult to achieve certain goals, such as reducing latency and enhancing quality of service (QoS) [2]. As a result, there have been attempts made to use edge computing to mitigate these challenges, such as utilising electric vehicle charging stations to act as edge computing devices and facilitate real-time decision making, and as a result, improving provisioned quality of service

and eco-friendliness in latency-sensitive applications [3, 4]. In addition, there have been attempts made to use edge computing to improve provisioned QoS and eco-friendliness in latency-sensitive applications [5, 6].

It is a well-known cliché that there is no such thing as a completely safe system, and this adage holds true for smart grid security as well.

For instance, the characteristics that are inherent to the architecture of edge computing, such as heterogeneity, mobility, geo-distribution, and location-awareness, can be exploited by attackers to carry out their malicious activities. These characteristics include heterogeneity, mobility, geo-distribution, and location-awareness. In light of this, the

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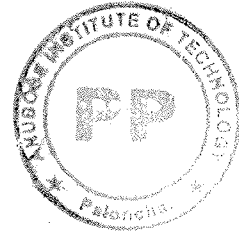
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Financial Support Request Letter

1. Name of the faculty member : A. Avani

2. Designation : Assistant professor

3. Department : csk

4. Conference/Publication/ Membership Fee/ Workshop /FDP Certificate

Details: optimizing routing & storage node deployment together for consistent data storage in multicultural KSN

5. Duration of the Program :

6. Associating professional body :

7. Financial support particulars (Rs.) :

i. Registration Charges : 1000/-

ii. Travelling Allowance :

iii. Membership Fee :

iv. Others :

Date: June/2022

A. Avani
Signature of the faculty member

1. Recommendations of the HoD: [Signature]

2. Recommendations of the IQAC: [Signature]

3. Recommendations of the Principal: [Signature]

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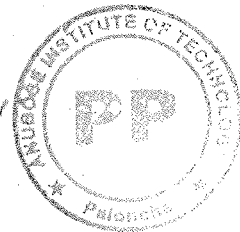
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Optimizing Routing and Storage Node Deployment Together for Consistent Data Storage in Multicultural WSN

Dr. A. Avani

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avanialla@gmail.com

ABSTRACT:

Sensor networks have been widely used in the big data age, yielding a lot of data for several purposes. However, handling the enormous amount of data is a highly difficult problem since sensor networks are often installed in hostile locations. In this research, the topic of data storage reliability in heterogeneous wireless sensor networks, where robust storage nodes are placed in sensor networks and data redundancy is used via coding approaches, is the major emphasis. We create an algorithm to jointly optimize data routing and storage node placement in order to reduce the expenses associated with data delivery and data storage. Since the issue is NP-hard and may be expressed as a binary nonlinear combinatorial optimization problem, it is very difficult to construct approximation techniques. We carefully propose an effective algorithm powered by a continuous-time Markov chain to plan the deployment of the storage node and associated routing strategy by using the Markov approximation framework. We also do in-depth simulations to confirm the effectiveness of our method.

INTRODUCTION:

Wireless Sensor Networks (WSNs) have rapidly advanced in a variety of applications during the last several decades [1]. A variety of sensors have been widely installed and networked for monitoring and surveying purposes in the big data era[2-5]. Data storage has still grown to be a highly challenging task as a consequence of the

development of WSNs, which generates enormous sensory data [6-11]. Difficult problem. Due to their limited resources (such as computation, data processing, and storage capabilities), sensor nodes are often unable to provide professional data storage services. Employing designated storage nodes in a heterogeneous wireless sensor network is one way to increase the capacity of data storage in a WSN. The data detected



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- Name of the faculty member : DR. N.V. Subbarao
- Designation : Asst. Professor
- Department : EEE
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: power system stability enhancement using
..... Unified power flow controller
- Duration of the Program :
- Associating professional body :
- Financial support particulars (Rs.) :

 - Registration Charges : 1000/-
 - Travelling Allowance :
 - Membership Fee :
 - Others :

Date: 01/Jan/2022

Subbarao

Signature of the faculty member

- Recommendations of the HoD: [Signature]
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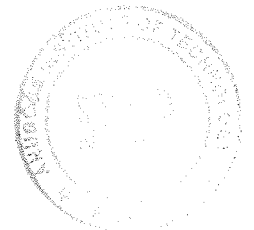
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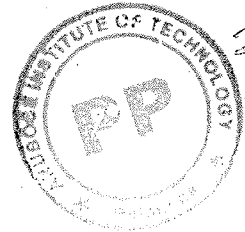
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Analysis modelling & Simulation of grid connected STATCOM (Dr. N.V. Subba Rao)	1000/-	

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Analysis Modelling & Simulation of grid connected STATCOM

Dr.N.V.Subba Rao

Anu Bose Institute of Technology, Paloncha, India

nvsubbarao@rediffmail.com

ABSTRACT

The increasing Industrialization, urbanization of life style has lead to increasing dependency on the electrical energy. This has resulted into rapid growth of power systems. This rapid growth has resulted into few uncertainties. Power disruptions and individual power outages are one of the major problems and affect the economy of any country. These constraints affect the quality of power delivered. However, these constraints can be suppressed by enhancing the power system control. Now a days Flexible AC Transmission Systems (FACTS) devices are very useful for the compensation and control of active power in transmission and distribution power system network. This will improve the stability of system under faulty condition and under varying condition. In these FACTS devices STATCOM is used to control power issues like Power unbalances, harmonics distortion, Faults and short circuit interruptions. STATCOM improve the stability of the power system. In this Paper, Pulse width Modulation technique with PI Controller is being proposed for compensation of Power in transmission lines. Simulation results carried out by MATLAB/SIMULINK to evaluate the performance of the proposed method. Simulation results determine that the proposed model is the most efficient solution for the problem in Power systems.

1.INTRODUCTION

The FACTS is a generic term representing the application of power electronics based solutions to AC power system. These systems can provide compensation in series or shunt or a combination of both series and shunt. The FACTS can attempt the compensation by modifying impedance, voltage or phase angle. Flexible AC transmission systems (FACTS) devices have been proposed and implemented in many power systems. In which, static synchronous compensator (ST A TCOM) plays an

important role to improve the power quality. Such as, ST A TCOM is connected at the point of common coupling (PCC) to maintain stable voltage to improve the power quality by protecting DFIG-based wind farm interconnected to weak grid from going offline during and after the disturbances. A design procedure for ST A TCOM with constantly updates the parameters of PI controller for voltage regulator to enhance the voltage profile of the multi-machine system under dynamic disturbances has been proposed in. STATCOM can combine with Power System Stabilizer (PSS) in multi-machine



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- Name of the faculty member : ... D. V. Subha Rao
- Designation : Assistant professor
- Department : EEE
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details: PA based hysteresis controlled active filter
 utilizing decrease of harmonics
- Duration of the Program :
- Associating professional body :
- Financial support particulars (Rs.) : 1000/-

 - Registration Charges :
 - Travelling Allowance : -
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Date: 04/April/2022

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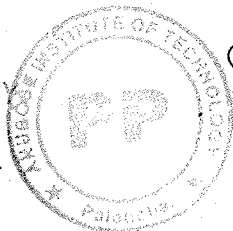
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Subba Rao
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Power System Stability Enhancement using Unified Power Flow Controller

Dr.N.V.Subba Rao

*Professor, Department of EEE, Anu Bose Institute of Technology, Paloncha, India
nvsubbarao@rediffmail.com*

ABSTRACT:

With the growing demand of electricity, at times, it is not possible to erect new lines to face the situation. Flexible AC Transmission System (FACTS) uses the thyristor controlled devices and optimally utilizes the existing power network. FACTS devices play an important role in controlling the reactive and active power flow to the power network and hence both the system voltage fluctuations and transient stability. This paper proposes the Unified Power Flow Controller (UPFC) as a power electronic based device that has capability of controlling the power flow through the line by controlling its series and shunt converters, also combined with Distributed Generation (DG) connected in the DC link to mitigate power quality disturbances. The proposed control will enable increased connections of renewable energy sources in the smart grid. The function of the scheme has been investigated the improvement of transient stability and voltage fluctuations of inter-area power system.

I.INTRODUCTION

The solutions to improve the quality of supply in the electrical networks with distributed generation go through the applications of the developments in semiconductor power devices, that is to say, the utilization of static power converters in electrical energy networks [1]. The technological advances in power semiconductors are permitting the development of devices that react more like an ideal switch, totally controllable, admitting high frequencies of commutation to major levels of tension and power [2]. The concept of distributed generation (DG) is generally associated to the development of the renewable energy sources such as fuel cells, wind energy and solar cells, another factor to keep in mind in the development

and configuration of the electrical system. The DG will need important electronic equipment based on power converters that facilitate the integration of these sources of energy, without damaging over the reception quality of the users connected to the electricity network [3].

The FACTS controllers offer great opportunities to regulate the transmission of alternating current (AC), increasing or diminishing the power flow in specific lines and responding almost instantaneously to the stability problems. The potential of this technology is based on the possibility of controlling the route of the power flow and the ability of connecting networks that are not adequately interconnected, giving the possibility of trading energy between distant agents [4]. One particular concept, called the

Volume XIV, Issue I, January/2022

Page No: 1106

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Details: *Sensor - assisted monte carlo segmentation*
..... *with Robustness boost for MSA and POT*

5. Duration of the Program :

6. Associating professional body :

7. Financial support particulars (Rs.) :

i. Registration Charges : *1000/-*

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Avani

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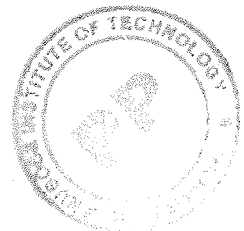
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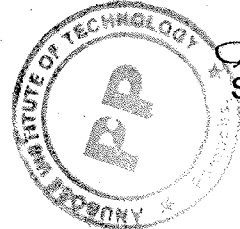
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The Wireless Sensor Network Implementation of a Hybrid Fault-Tolerant Routing Based on the Gaussian Network

Dr. A. Avani

Assistant Professor, Department of CSE, Anu Bose Institute of Technology, Paloncha, India

avanialla@gmail.com

ABSTRACT:


Throughout this publication, we develop a dynamic fault-tolerant routing as a solution to the problem of fault-tolerance in wireless sensor networks (WSNs) that are based on hierarchical topology. Clustering and the labelling of sensor nodes as Gaussian integers are also components of the hierarchical topology. As a result, the area of the network is segmented into smaller square grids, and the cluster head of each grid is denoted by an integer based on the Gaussian distribution. A Gaussian network has been created using these cluster heads by connecting them all together. In this paper, a hybrid fault-tolerant clustering routing protocol for wireless sensor networks based on the Gaussian network is proposed. This protocol makes use of node symmetry, the shortest distance in the Gaussian network, as well as the benefits of multi-path routing (FCGW). The goal of the Fault Tolerance, Increased Data Reliability, and Energy Consumption (FCGW) project is to make wireless sensor networks more resilient to failures, increase the reliability of their data, and The findings of the experiments that were conducted in order to test the suggested scheme demonstrate that the FCGW protocol has a high level of data dependability. Additionally, the FCGW protocol uses approximately 48% of the energy that is available in the network, whereas other protocols use approximately 70% more energy.

I INTRODUCTION:

In wireless sensor networks (WSN), the metrics such as data reliability, optimum power consumption, defined as repeated, and data latency are some of the significant issues in the efficient implementation of the network [1–3]. In particular, in WSN, incorrect connections are constantly caused by the limited number of sensor nodes, as well as by severe communication settings like as rain, wind, snow, and water. This can be seen in [4]. As a result, increasing the failover clustering of the networks will result in an improvement in the quality and services and dependability provided by the WSN. Since that time, resolving concerns with high availability has been an essential necessity for the design of WSNs.

As of right now, there are a few different fault-tolerant strategies that have been suggested as ways to make fault tolerance better. These fault-tolerant strategies will be organised into three primary categories: mechanisms that are based on redundancy, mechanisms that are based on clustering, and mechanisms that are based on deployment, as described in [5]. To be more specific, the following are some of the benefits and drawbacks associated with each mechanism:

- Mechanisms that are based on redundancy: The fault tolerance may be increased by including redundancy components into the system, such as duplicate routing pathways, redundant time, and data duplication [6]. Active replicating and passively replicating both play a role in this mechanism's primary

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6. Associating professional body :
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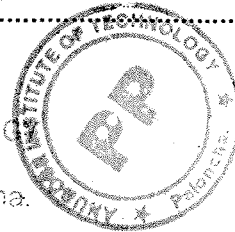
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Compensation of Voltage Sag and Swell Using DVR

Dr.N.V.Subba Rao

Anu Bose Institute of Technology, Paloncha, India

nvsubbarao@rediffmail.com

ABSTRACT:

The most significant disturbances in the Power systems are Voltage sags and swells which may bring great economic losses to the sensitive customers. To solve this problem, installation of FACTS device has become necessary. Dynamic Voltage Restorer (DVR), one of the FACTS device is being installed in the power system at both transmission and distribution level in order to present effective solution to this problem of voltage sags/swells. The Dynamic Voltage Restorer (DVR) has recently been introduced to protect sensitive loads from voltage sags and swells other voltage disturbance. It is a series connected power electronic based device. Its appeal includes smaller size and fast dynamic response to disturbances. In this paper, Sinusoidal pulse width modulation technique with PI Controller is being proposed for compensation of Voltage sag/swell. Simulation results carried out by MATLAB/SIMULINK to evaluate the performance of the proposed method. Simulation results determine that the proposed model is the most efficient solution for the problem of voltage sag/swell.

I. INTRODUCTION

In modern technology, there are two major challenges that power system must deal with: one is voltage fluctuations and the other is short circuit faults. Present days, many power loads have become more sensitive to the disturbances due to voltage fluctuations, flickers that are produced because of wide use of non-linear loads. Because of these disturbances or fluctuations quality of power being distributed or transmitted has become low. Faults in power system can cause voltage sag or swell in the entire system or major part of it. In addition, harmonics, voltage transients, flickers are also one of the voltage quality problems [1].

Consumers need constant sine wave shape, constant frequency and symmetrical voltage

with a constant root mean square (rms) value to continue the production. To satisfy these demands, the disturbances must be eliminated from the system. The typical power quality disturbances are voltage sags, voltage swells, interruptions, phase shifts, harmonics and transients. Voltage sag can be either balanced or unbalanced which mostly depending on type of faults. The main sources of voltage sag are any type of faults in power system. Voltage sags are considered as major threats to the power quality. Similarly voltage swells occurs at any instant of time ranging from 1.1 to 1.8 p.u and that lasts for half a cycle to one minute. But voltage swells are less frequent compared to that of voltage sags which are mainly produced because of sudden switching off of large loads or energization of capacitor banks [3].



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1. Name of the faculty member : Dr. N.V. Subba Rao

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3. Department : EEE

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Details: compensation of voltage sag anal sy

5. Duration of the Program :

6. Associating professional body :

7. Financial support particulars (Rs.) :

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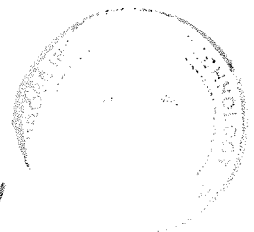
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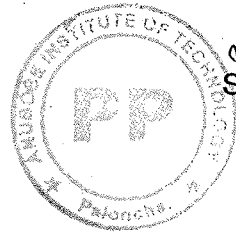
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Modeling and Simulation of Grid connected PV-System with D-STATCOM

Dr.N.V.Subba Rao

*Professor, Department of EEE, Anu Bose Institute of Technology, Paloncha, India
nvsubbarao@rediffmail.com*

ABSTRACT:

Due to increase demand of energy world community looking towards renewable energy and in this solar is best option due to clean energy and there are no environmental concern. Government is also providing subsidy over last decade .But due to weather dependent power generation of solar it requires to connect this with grid .For this we requires FACTS devices which will connect PV-system to grid .This paper presents grid connected PV-system with STATCOM. Now a days FACTS devices are very useful for the compensation and control of active power in transmission and distribution power system network. This will improve the stability of system under faulty condition and under varying condition. By using Matlab simulation I-V and P-V characteristic are shown. The basic control strategy which is used in this paper is dual close loop control. In this there are two control loop one is called outer control loop also called voltage control another is inner control loop also called current control loop .Which are used for active and reactive power compensation. Here P&O algorithm is used for solar MPPT. Which are easier and accurate and it requires less parameter compared to others.

I. INTRODUCTION:

In recently with the growth of nonlinear loads demand increasing day by day and also due to industrial growth requirement of more active and reactive power to the industry and consumer with improved quality of power. But due to environmental and health impact of conventional energy source and much more cost for constructing new generating and

transmission line it is better to generate active and reactive power at the load side by using clean energy source and take small amount of power from the grid .Now renewable energy is the best option which can full fill the demand and world moving very fast towards such type of energy sources which full fill our demand without creating any environmental and health hazard problem. In this solar PV is the best option now a days it is world fastest growing



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- Name of the faculty member : Dr. A. A. Vani
- Designation : Assistant Professor
- Department : DSE

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Details: The wireless sensor network implementation of a hybrid fault

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- Associating professional body :
- Financial support particulars (Rs.) :

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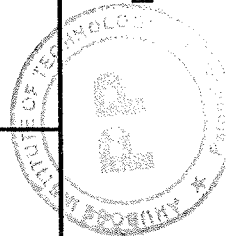
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Authentication upon that Blockchain that Is Completely Anonymous Together with Addition To improvements with Distributed Generation Computation Offloading Network

Dr. A. Avani

Assistant Professor, Department of CSE, Anu Bose Institute of Technology, Paloncha, India

avanialla@gmail.com

ABSTRACT

Conventional virtualized smart grid systems have a number of significant obstacles, two of the most significant of which are obtaining low-latency and offering real-time services. As a result, there has been a growing tendency toward shifting toward edge computing. Existing cryptographic methods often do not enable conditional anonymous or flexible identity management, despite the fact that there have been a number of cryptographic protocols developed specifically to make it easier to maintain encrypted systems in smart grid networks.

In light of this, the purpose of this article is to provide a mutual authentication and key negotiation methodology for edge computing-based smart grid systems that is based on blockchain technology. Specifically, by using blockchain technology, the protocol is able to offer efficient conditional anonymity as well as access control. This is accomplished without the use of any extra complicated encryption algorithms. The security analysis demonstrates that the protocol delivers a fair degree of safety assurance. Furthermore, the comparison summary for both safety and efficacy hints that the suggested technique may have use in the context of a power system implementation.

I. INTRODUCTION

One of the many categories of the Industrial Internet of Things (IIoT), smart grid systems are among those that have the potential to increase the dependability, flexibility, and quality of energy distribution [1]. However, when the system grows in size (for example, as the number of customers grows), it may become more difficult to achieve certain goals, such as reducing latency and enhancing quality of service (QoS) [2]. As a result, there have been attempts made to use edge computing to mitigate these challenges, such as utilising electric vehicle charging stations to act as edge computing devices and facilitate real-time decision making, and as a result, improving provisioned quality of service

and eco-friendliness in latency-sensitive applications [3, 4]. In addition, there have been attempts made to use edge computing to improve provisioned QoS and eco-friendliness in latency-sensitive applications [5, 6].

It is a well-known cliché that there is no such thing as a completely safe system, and this adage holds true for smart grid security as well.

For instance, the characteristics that are inherent to the architecture of edge computing, such as heterogeneity, mobility, geo-distribution, and location-awareness, can be exploited by attackers to carry out their malicious activities. These characteristics include heterogeneity, mobility, geo-distribution, and location-awareness. In light of this, the



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with distributed generationed computation

- Duration of the Program :
- Associating professional body :
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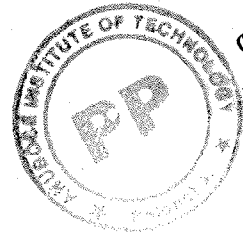
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Sensor-Assisted Monte Carlo Segmentation with Robustness Boost for WSN and IOT

Dr. A. Avani

Assistant Professor, Department of CSE, Anu Bose Institute of Technology, Paloncha, India

avanialla@gmail.com

ABSTRACT:

Knowing where the geo-location sensor data was gathered from is crucial for scattered sensor systems, particularly in instances involving mobile sensors, which are often encountered in wireless sensor networks or the Internet of Things. With the exception of a radio, which is already present on sensor nodes, range-free Monte Carlo localization-based techniques are relatively energy efficient. However, the incorporation of motion sensor data based dead reckoning considerably enhances location estimations' accuracy and provides resilience against erroneous or hostile network actors. In this paper, we offer a robust sensor-assisted Monte Carlo localization method (RESA-MCL). We demonstrate the efficiency of RESA-MCL in terms of general localization accuracy as well as resilience against malicious assaults or malfunctioning nodes. We offer three attack scenarios based on malicious anchor nodes to assess and contrast our strategy against previous approaches. RESA-performance MCL's are assessed using these attack models, and our method outperforms other methods in conditions of both extremely low and high anchor node density, obtaining a localization error of 0.5 with an anchor density of 0.33. Overall, RESA-MCL does a better job than similar techniques at lower anchor density, reducing localization errors by up to 48%, and is much more resistant to attacks while requiring only a little more computing work.

I INTRODUCTION:

In the modern world, a growing number of Internet of Things (IoT) devices with various types of sensors, as well as Wireless Sensor Networks (WSN), are being

deployed to cover a wide range of scenarios, from connected devices [9] to decentralised volunteer measures for evaluating air quality [2], [17], industrial uses [10], and wildlife monitoring [16]. Knowing where the data was recorded is crucial for understanding the



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Financial Support Request Letter

- Name of the faculty member : Dr. N.V. Subba Rao
- Designation : Asst professor
- Department : EEE
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: Stability Enhancement in Transmission line Using
UPQC
- Duration of the Program :
- Associating professional body :
- Financial support particulars (Rs.) : ₹

 - Registration Charges : 1000/-
 - Travelling Allowance : -
 - Membership Fee : -
 - Others : -

Date: 2 / Feb / 2022

Subba Rao

Signature of the faculty member

- Recommendations of the HoD: Asst
- Recommendations of the IQAC: Not
- Recommendations of the Principal: AMS

Sanctioned/Not Sanction

Account Department

Accountant: (Signature)

Date: 01/2/2022

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V.No.....

Date: 17/03/2022

CASH VOUCHER

DEBIT

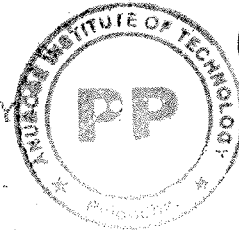
PARTICULARS	AMOUNT	
	Rs.	ps.
PQ based hysteresis controlled Active filter utilizing Decrease of harmonics (Dr.N.v. Subba Rao)	1000/-	

Cashier

Principal

Received Rs. 1000/- (Rupees One thousand
rupees only
being.....

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Subbay
Signature

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Bhadradri Kothagudem Dist.

PQ based Hysteresis Controlled Active Filter utilizing Decrease of Harmonics

Dr.N.V.Subba Rao

*Professor, Department of EEE, Anu Bose Institute of Technology, Paloncha, India
nvsubarao@rediffmail.com*

ABSTRACT:

A three-stage inverter-based Active Filter (AF) constrained by Instantaneous genuine and responsive power hypothesis (PQ hypothesis) based hysteresis and PI regulators are introduced in this paper. An Inverter based AF is utilized to lessen the sounds brought about by non-direct loads in the source voltage and current by infusing the remunerating flows. The hysteresis Controller produces the entryway beats expected for the activity of AF. Quick genuine and receptive power hypothesis (PQ hypothesis) screens the dynamic and responsive powers and creates the reference current as needs be. PI regulator directs the voltage of the DC interface capacitor. The MATLAB Simulink model has been intended for the proposed approach and the THD is decreased altogether.

INTRODUCTION:

Power Quality is characterized as the appearance of issues in current, voltage or deviation of recurrence which causes to disoperation, harm, and breaking down of customer hardware. Variety in recurrence, voltage, and waveform contortion is basic power quality issues. In a new electrical framework, it is occurring extremely normal that there is an unexpected increment of non-direct loads, for example, foothold drives, customizable speed drives, battery chargers, PCs, and variable recurrence drives, and so forth. These heaps are causing music when they draw the non-sinusoidal flows from the inventory mains which misshapes the normal sinusoidal influx of both voltage and flows. The twisting in the sinusoidal wave likewise causes the unfortunate power factor, overheating of hardware, overheating of transformer units, inordinate unbiased flows, and so on. This made a premium to create and explore the gear called 'symphonious channels' to reduce the sounds existing in the power framework. Customarily, power quality issues and sounds are moderated overall and specific separately by uninvolved channels. In any case, the presentation of the inactive channels is

limited because of the issue of the absence of dynamic pay and reverberation. The dynamic power channels are created to vanquish these restrictions. There are many advantages in utilizing dynamic channels contrasted with latent channels like the better reaction than the varieties of music and shifting burdens. Likewise, a more prominent number of music, and alleviation of other power quality issues like hang, swell and gleam, and so forth can be repaid by a solitary dynamic channel. These dynamic channels dispense with the voltage and current music, accordingly counterbalance negative and zero arrangement parts and further developing the power factor. The geography of dynamic power channels (APF) is viewed as series or equal contingent upon the wellsprings of sounds. In this paper, AF is utilized to moderate sounds. In the proposed approach AF infuses the remunerating flows that are inverse and equivalent in stage with the line flows which comprise the sounds. PQ hypothesis is the control system utilized for controlling the AF. Hysteresis Controller produces the door beats for AF by contrasting the genuine line flows and the reference remunerating flows. The PI regulator directs the DC interface capacitor voltage by taking the reference DC voltage



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Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : Dr. N.V. Subba. Rao.....
- Designation : Assistant professor.....
- Department : EEE.....
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: Analysis modelling & simulation of g.p.c.f.....
..... Connected S.A.T.COM.....
- Duration of the Program :
- Associating professional body :
- Financial support particulars (Rs.) :

 - Registration Charges : 1000/-.....
 - Travelling Allowance : -.....
 - Membership Fee : -.....
 - Others : -.....

Date: 2 / Feb / 2022

Signature of the faculty member

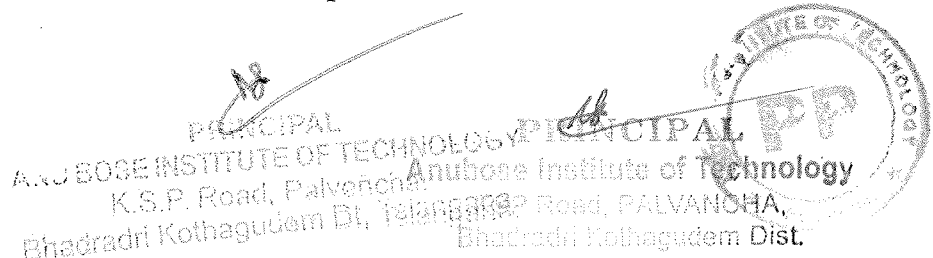
- Recommendations of the HoD: AS
- Recommendations of the IQAC: MS
- Recommendations of the Principal: AB

Sanctioned/Not Sanction

Account Department

Accountant: MP

Date: 2 / 09 / 2022



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V.No.....

Date: 14/01/2022

CASH VOUCHER

DEBIT

PARTICULARS	AMOUNT	
	Rs.	ps.
Stability enhancement in Transmission line using Vpac CDr. N.V. Subba Rao	1000	-

Cashier

Principal

Received Rs. 1000 (Rupees one thousand

Rs. only

being

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Bhadradi Kothagudem Dt, Telangana.



Subba Rao
Signature

Stability Enhancement In Transmission Line Using UPQC

Dr.N.V.Subba Rao

Anu Bose Institute of Technology, Paloncha, India

nvsubarao@rediffmail.com

ABSTRACT

This paper presents the power quality improvement using UPQC. UPQC consists of series inverter, shunt inverter and capacitance. Every inverter connected with pulse generator for switching on. UPQC is especially obtained to resolve different kind of power quality drawback like reactive power compensation, voltage interruption and harmonics. DVR is connected in series to deliver the active and reactive power to distribution network. DC-link capacitors stay high as a result of the DVR needs a minimum amount of DC-link voltage to compensate sag. So, DC-link voltage is connected with PV module to reduce the cost. Design of UPQC device with multi-bus system obtained using MATLAB/SIMULINK and simulation results are mentioned to support the developed conception.

I.INTRODUCTION

The term "power quality" (PQ) has gained significant attention in the past few years. Electrical power generated and distributed to the long distance. Some non-linear loads are connected on the distribution side for effective flow of power to the consumers. It cause voltage flicker, distortion, transient. Hence by using facts devices we'll be able to reduce the power quality issues. Distribution resources (DR) causes voltage increase or decrease on the feeder depending on DR kind, control technique, its delivered power at feeder

parameters and loading. Interruption is critical when compared with voltage sag in latter it may become a number one power quality problem. DVR is connected between supply and critical load feeder. It provides complete security for distribution network.

The advancement in the semiconductor device technology has made it possible to realize most of the power electronics based devices/prototypes at commercial platform. The development of power electronic technology makes it possible to realize many kinds of Flexible Alternating Current Transmission Systems devices to obtain



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Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : Dr. N.V. Subba Rao
- Designation : Asst. Professor
- Department : EEE
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: Voltage sag compensation Using Dynamic
- Duration of the Program :
- Associating professional body :
- Financial support particulars (Rs.) :

 - Registration Charges : 1000/-
 - Travelling Allowance :
 - Membership Fee :
 - Others :

Date: 1 / Jan / 2022

Subba Rao
Signature of the faculty member

- Recommendations of the HoD: Ashish
- Recommendations of the IQAC: Not
- Recommendations of the Principal: MS

Sanctioned/Not Sanction

Account Department

Accountant: mf

Date: 1 / 01 / 2022

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Voltage Sag Compensation Using Dynamic Voltage Restorer

Dr.N.V.Subba Rao

Professor, Department of EEE, Anu Bose Institute of Technology, Paloncha, India
nvsubbarao@rediffmail.com

ABSTRACT:

The increase use of modern sensitive and sophisticated loads connected to distribution system causes various problems to the system. The major problems are voltage sag and swell, there is need to compensate it. Today most of the distribution companies are using power semiconductor based devices for improving power quality. Among the various custom power devices, Dynamic voltage restorer is used for compensation in this paper, which is most popular and widely used method. Here, in phase compensation technique of DVR is used with battery energy storage system. The DVR's life time is improved by replacing battery with super capacitor. Super capacitor is most suitable short duration energy requirement. The control technique employed here is SRF controller. The overall work is carried out in MATLAB Simulink.

Keywords: DVR, voltage sag, voltage swell, In Phase compensation, Battery Energy storage, SRF controller.

1. INTRODUCTION:

The electric power industry comprises electricity generation, transmission, and distribution. The electricity then moves through wiring system of end user until it reaches the load. During this quality of power may change and disturbance or called power quality problem occur. Power quality involves voltage, frequency, and waveform. Good power quality can be defined as steady supply voltage that stays within the prescribed range, steady ac frequency close to rated value and smooth voltage curve waveform. Without proper power electrical device or load may malfunction, fail prematurely or not operate at all. There are many ways in which electric power can be of poor quality and many more causes of such poor quality power. The number of power quality issues including voltage sag, swell, flicker, harmonics, transients etc. has different causes.

The most of the common power disturbance that impact sensitive equipment is voltage sag. The main sources of sag are large increase in current

due to faults and abrupt increase in system impedance. Voltage sag are caused by reduction in the load with poor voltage regulation. Voltage sag means that decrease in normal voltage from 10% to 90% of its RMS value which last for a cycle less than one minute. Voltage swell, in contrast, can be defined as increase in RMS voltage above the nominal value which last for a cycle greater than one minute. Switching off of large load and energization of capacitor bank are considered as causes of voltage swell. In this scenario power quality is directly related to distribution system because of it is situated at the end power system and directly connected to customer. If any disturbance is occur in distribution system, a huge amount of losses may happen, therefore loss of productivity and competitiveness. Hence it is necessary to improve quality of power. In order to improve quality of power, there is number of custom power devices are available which protects the load from voltage sag, swell harmonics etc. DVR is a series connected device installed between source and load. DVR inject the voltage

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V.No.....

Date: 10/01/2022

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DEBIT.....

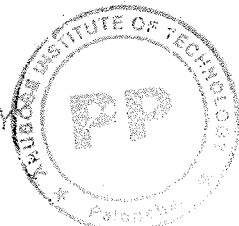
PARTICULARS	AMOUNT	
	Rs.	ps.
Compensation of voltage sag and sv (Dr. N.V. Subba Rao)	1000/-	

Cashier

NS
Principal

Received Rs. 1000 (Rupees One thousand
rupees only.)
being.....

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Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

1. Name of the faculty member : Dr. N.V. Subba Rao.....

2. Designation : Asst. Professor.....

3. Department : EE.....

4. Conference/Publication/ Membership Fee/ Workshop /FDP Certificate

Details: Voltage sag compensation using Dynamic.....

5. Duration of the Program :

6. Associating professional body :

7. Financial support particulars (Rs.) :

i. Registration Charges : 1000/-.....

ii. Travelling Allowance :

iii. Membership Fee :

iv. Others :

Date: 1 / Jan / 2022

Subba Rao
Signature of the faculty member

1. Recommendations of the HoD: Ashish

2. Recommendations of the IQAC: Not

3. Recommendations of the Principal: MS

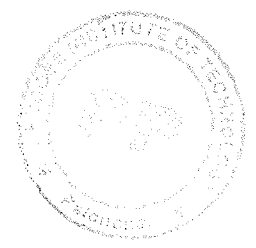
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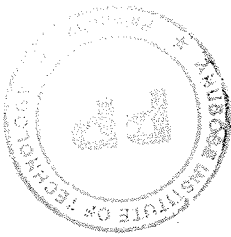
Account Department

Accountant: [Signature]

Date: 1 / 01 / 2022

[Signature]
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Bhadradi Kothagudem Dt, Telangana.





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Signature
Subba

being

rupees only.

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V.No.....

Date: 10/01/2022

CASH VOUCHER

DEBIT

PARTICULARS	AMOUNT	
	Rs.	ps.
Compensation of voltage sag and s (Dr. N.v. Subba Rao)	1000/-	

Cashier

Principal
Subba

Voltage Sag Compensation Using Dynamic Voltage Restorer

Dr.N.V.Subba Rao

*Professor, Department of EEE, Anu Bose Institute of Technology, Paloncha, India
nvsubbarao@rediffmail.com*

ABSTRACT:

The increase use of modern sensitive and sophisticated loads connected to distribution system causes various problems to the system. The major problems are voltage sag and swell, there is need to compensate it .Today most of the distribution companies are using power semiconductor based devices for improving power quality. Among the various custom power devices, Dynamic voltage restorer is used for compensation in this paper, which is most popular and widely used method. Here, in phase compensation technique of DVR is used with battery energy storage system. The DVR's life time is improved by replacing battery with super capacitor. Super capacitor is most suitable short duration energy requirement. The control technique employed here is SRF controller. The overall work is carried out in MATLAB Simulink.

Keywords: DVR, voltage sag, voltage swell, In Phase compensation, Battery Energy storage, SRF controller.

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The most of the common power disturbance that impact sensitive equipment is voltage sag .The main sources of sag are large increase in current

due to faults and abrupt increase in system impedance. Voltage sag are caused by reduction in the load with poor voltage regulation. Voltage sag means that decrease in normal voltage from 10% to 90% of its RMS value which last for a cycle less than one minute .Voltage swell, in contrast, can be defined as increase in RMS voltage above the nominal value which last for a cycle greater than one minute. Switching off of large load and energization of capacitor bank are considered as causes of voltage swell. In this scenario power quality is directly related to distribution system because of it is situated at the end power system and directly connected to customer. If any disturbance is occur in distribution system, a huge amount of losses may happen, therefore loss of productivity and competitiveness. Hence it is necessary to improve quality of power. In order to improve quality of power, there is number of custom power devices are available which protects the load from voltage sag, swell harmonics etc. DVR is a series connected device installed between source and load. DVR inject the voltage





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CODE	College	Counselling
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Financial Support Request Letter

- Name of the faculty member : *N. Soikanth*
- Designation : *Assistant professor*
- Department : *MINING*
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: *Accelerating... clean... Green... and... sustainable... Innovation*
..... *in India*
- Duration of the Program : *7th may - 14th June, 2023*
- Associating professional body : *Dr. D. Y. Pati Institute of Technology*
- Financial support particulars (Rs.) :
i. Registration Charges : *1000/-*
ii. Travelling Allowance : *—*
iii. Membership Fee : *—*
iv. Others : *—*

Date: _____ Signature of the faculty member *Soikanth*

1. Recommendations of the HoD: _____ *Pench*

2. Recommendations of the IQAC: _____ *Mani*

3. Recommendations of the Principal: _____ *X*

Sanctioned/Not Sanctioned

Accountant: *(Signature)*

Date: *06/05/2023*

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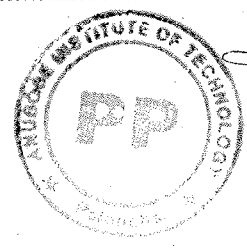
DEBIT.....	PARTICULARS	AMOUNT	
		Rs.	ps.
	Accelerating clean, green and sustainable innovation in India	1000/-	
	N. Srikanth		

Cashier

Principal

Received Rs. 1000 (Rupees One Thousand) rupees only being.....

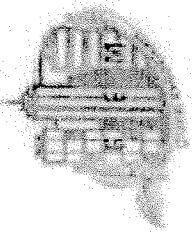
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Bhadradri Kothagudem Dist.



Srikanth
Signature

DPU

Dr. D. Y. Patil Institute of Technology, Pimpri, Pune
&
ASSOCHAM GEM Green Building Council, New Delhi



Faculty Development Program on
“Accelerating Clean, Green and Sustainable Innovation in India”
Certificate of Participation

N. Srikanth

This certificate is awarded to
of

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in recognition to attending the FDP on “Accelerating Clean, Green and Sustainable Innovation In India” organized by
Department of Electrical Engineering and Department of Instrumentation Engineering of Dr. D. Y. Patil Institute of
Technology, Pimpri, Pune in collaboration with ASSOCHAM GEM Green Building Council, New Delhi, during 7th

May 2023 to 14th June 2023

FDP0505180610

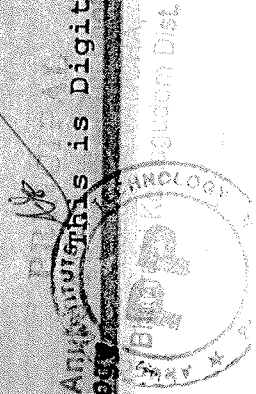
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CODE	College	Counselling
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Financial Support Request Letter

- Name of the faculty member : *B. charan*
- Designation : *Assistant professor*
- Department : *CIVIL*
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details: *Short term training programme through I.C.T mode*
 *on modelling, Analysis & Design of structure using*
 *latest version of STAAD*
- Duration of the Program : *27/09/2023 to 31/07/2023*
- Associating professional body : *National Institute of Technical Teacher Training*
- Financial support particulars (Rs.) :
 i. Registration Charges : *1000/-*
 ii. Travelling Allowance : *-*
 iii. Membership Fee : *-*
 iv. Others : *-*

Date: _____ Signature of the faculty member *Charan*

- Recommendations of the HoD: _____ *Peru*
- Recommendations of the IQAC: _____ *Abhishek*
- Recommendations of the Principal: _____ *PP*

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Account Department

Accountant: *(Signature)*
Date: *24/09/2023*

(Signature)
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 Bhadradi, Kothagudem Dist.,
 KSP Road, PALVANCHA,
 Bhadradi, Kothagudem Dist.



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K.S.P.Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

Date: 18/07/2023

V.No.....

CASH VOUCHER

DEBIT.....

PARTICULARS	AMOUNT	
	Rs.	ps.
short term training programme through ICT mode on modelling, analysis & design of structures latest version of STAAD B.Charan	1000/-	

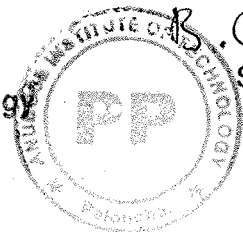
Cashier

Principal

Received Rs. 1000 (Rupees One Thousand rupees only)

being.....

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KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist



B. Charan
Signature

PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA

Certificate

Awarded to
B. charan

for having successfully completed the
Short Term Training Programme through ICT Mode on

Modelling, Analysis and Design of Structures using latest version of STAAD

organised by this institute

from 25/07/2023 to 31/07/2023 [duration: 1 Week]

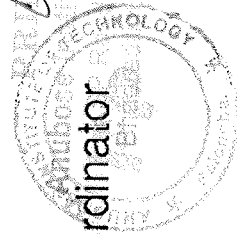
Edlang

Programme Coordinator (s)

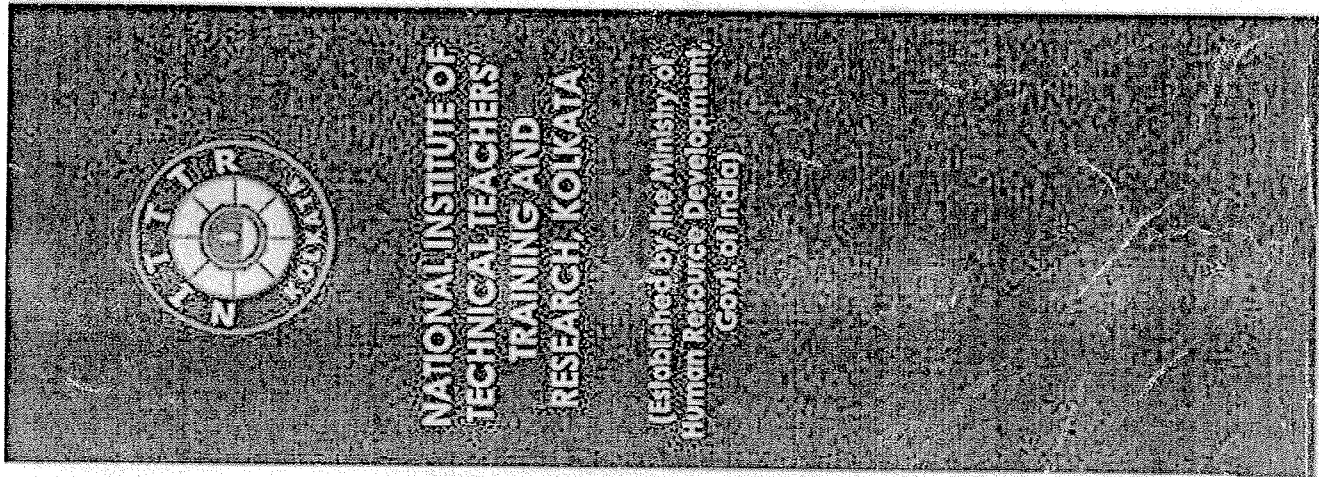
PR Academic Coordinator

Andhra Pradesh Institute of Technology
KSP Road, PALVANCHA,
Bhadraeri Kothagudem Dist.

Prashant



Director





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Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : B. Ramesh
- Designation : Assistant professor
- Department : F.E.E.
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: Short term training programme through I.C.T
..... made on modelling: Analysis & Design of structures
..... using latest version of STAAD
- Duration of the Program : 25/07/23 to 31/07/2023
- Associating professional body : National Institute of Technical Teacher Training
- Financial support particulars (Rs.) :
i. Registration Charges : 1000/-
ii. Travelling Allowance : -
iii. Membership Fee : -
iv. Others : -

Date: _____ Signature of the faculty member Ramishy

1. Recommendations of the HoD: Agha

2. Recommendations of the IQAC: Alwaf

3. Recommendations of the Principal: RS

Sanctioned/Not Sanction

Account Department

Accountant: (Signature)

Date: 24/07/2023

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Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi, Kothagudem Dist.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradri Kothagudem Dist.

Date: 18/07/2023

V.No.....

CASH VOUCHER

DEBIT.....

PARTICULARS	AMOUNT	
	Rs.	ps.
Short term training program through ICT mode on modelling, analysis and design of structures using latest version of STAAD B. Ramesh	1000/-	

Cashier

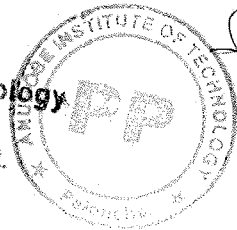
~~PP~~ PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA

~~PP~~ Principal

Received Rs. 1000 (Rupees One Thousand rupees only)

being.....

~~PP~~ PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.



Ramesh
Signature

Certificate

Awarded to
B. Ramesh

for having successfully completed the
Short Term Training Programme through ICT Mode on

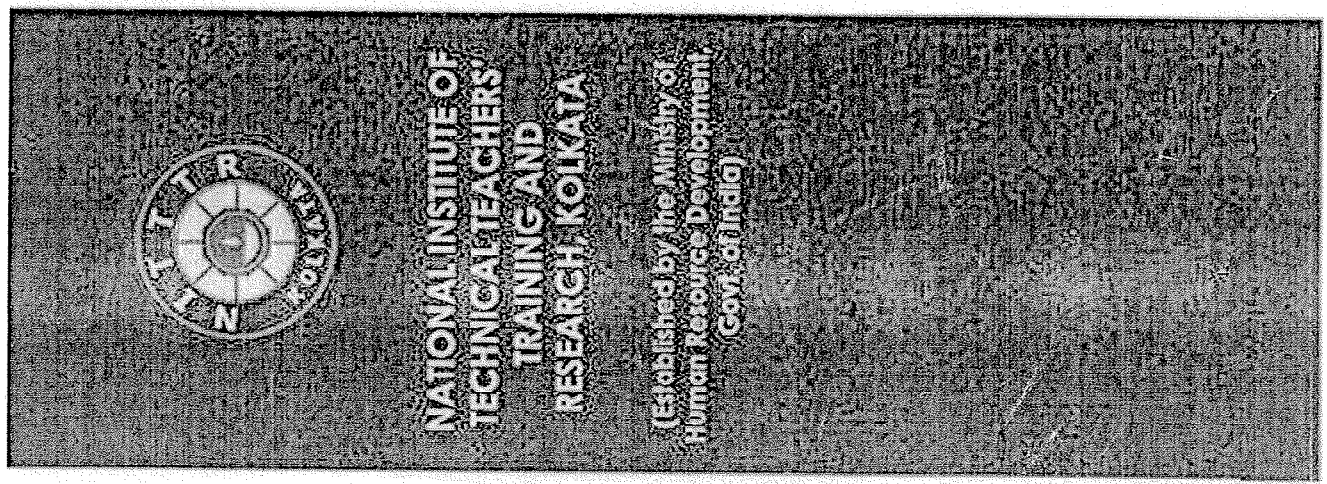
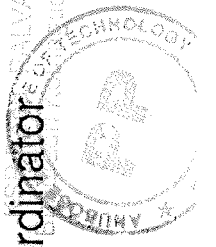
Modelling, Analysis and Design of Structures using latest version of STAAD

organised by this institute

from 25/07/2023 to 31/07/2023 [duration: 1 Week]

Edhang

Prin
Principal
Anubose Institute of Technology
Anubose, Palvancha, Bhaeradri, Kothagudem Dist.
Programme Coordinator (s) **PRIN Academic Coordinator**
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhaeradri Kothagudem Dist.





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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : *M. Nagaraju*
- Designation : *Assistant professor*
- Department : *S. & IT*
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: *Accelerating... Clean... Green... and... Sustainable... Innovation...
in India*
- Duration of the Program : *7th may - 14th June 2023*
- Associating professional body : *Dr. D.J. Patil Institute of Technology*
- Financial support particulars (Rs.) :
 - Registration Charges : *1000/-*
 - Travelling Allowance : *—*
 - Membership Fee : *—*
 - Others : *—*

Date: _____ Signature of the faculty member *Nagaraju*

- Recommendations of the HoD: *Vankh*
- Recommendations of the IQAC: *Nagaraju*
- Recommendations of the Principal: *[Signature]*

Sanctioned/Not Sanction

Account Department

Accountant: *[Signature]*

Date: *05/05/2023*

[Signature]
PRINCIPAL
 Anubose Institute of Technology
 KSP Road, PALVANCHA,
 Bhadradi Kothagudem Dist.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

V.No.....

Date: 20/04/2023

CASH VOUCHER

DEBIT.....

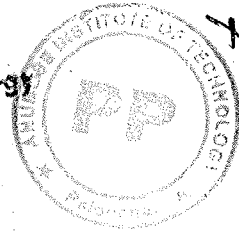
PARTICULARS	AMOUNT	
	Rs.	ps.
Accelerating clean, green and sustainable innovation in India M. Nagaraju	1000/-	

Cashier

Principal

Received Rs. 1000 (Rupees One Thousand) rupees only being.....

PRINCIPAL
Anubase Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.

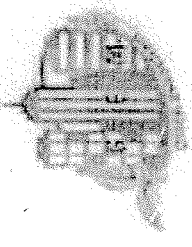


Nagaraju
Signature

PRINCIPAL
Anubase Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.

DPU

Dr. D. Y. Patil Institute of Technology, Pimpri, Pune
&
ASSOCHAM GEM Green Building Council, New Delhi



Faculty Development Program on
“Accelerating Clean, Green and Sustainable Innovation in India”
Certificate of Participation

M. Nagaraju

This certificate is awarded to
of

Anubose Institute of Technology

in recognition to attending the FDP on “Accelerating Clean, Green and Sustainable Innovation In India” organized by
Department of Electrical Engineering and Department of Instrumentation Engineering of Dr. D. Y. Patil Institute of
Technology, Pimpri, Pune in collaboration with ASSOCHAM GEM Green Building Council, New Delhi, during 7th

May 2023 to 14th June 2023

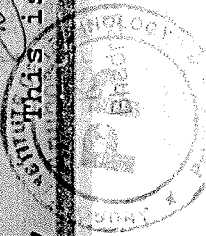
PRINCIPAL

Anubose Institute of Technology

FDP0505180610

Bhaeradri Kothagudem Dist.

This is Digital Certificate and does not require signature





ANUBOSE INSTITUTE OF TECHNOLOGY

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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : *Vr. Ashok*
- Designation : *Assistant professor*
- Department : *FEE*
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: *one week faculty development programme on*
..... *Electric vehicle*
- Duration of the Program : *11th to 16th may, 2023*
- Associating professional body : *Thulelal Institute of Technology*
- Financial support particulars (Rs.) :

 - Registration Charges : *1000/-*
 - Travelling Allowance : *-*
 - Membership Fee : *-*
 - Others : *-*

Date: _____ Signature of the faculty member *Ashok*

1. Recommendations of the HoD: *Ashok*

2. Recommendations of the IQAC: *Naresh*

3. Recommendations of the Principal: *[Signature]*

Sanctioned/Not Sanction

Accountant: *[Signature]*

Date: *10/05/2023*

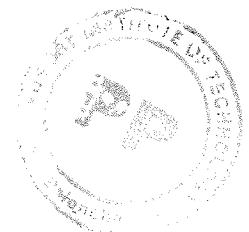
Account Department

[Signature]

PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.

[Signature]

PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradri Kothagudem Dist.

Date: 04/05/2023

V.No.....

CASH VOUCHER

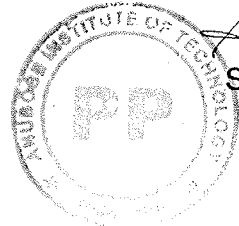
DEBIT.....	PARTICULARS	AMOUNT	
		Rs.	ps.
	One week faculty development program on electric vehicle	1000/-	
	W. Ashok		

W
Principal

Cashier

Received Rs. 1000..... (Rupees One Thousand)
rupees only.....
being.....

W
PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.



Ashok
Signature

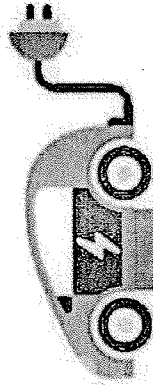
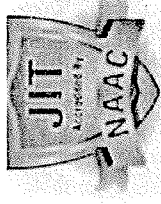
W
PRINCIPAL
Anubose Institute of Technology;
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.



Jhulelal Institute Of Technology

Approved by AICTE, Recognized by DTE and Affiliated to RTM Nagpur University

Department of Electrical Engineering



CERTIFICATE OF PARTICIPATION

This certificate is presented to

V. Ashok

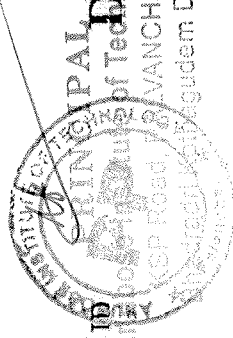
for attending One Week Faculty Development Programme on "Electric Vehicle" organised by Department of Electrical Engineering, Jhulelal Institute of Technology, Nagpur-441111 in association with CADD Center Training Services and Vishvakariya Automotive Pvt. Ltd. Nagpur, held on

11th - 16th May 2023

[Signature]

PRINCIPAL

Anubhose Institute of Technology
KSP Road, PALVANCHA
Bhadraji Kothagudem Dist.



[Signature]

Prof. Rohan M. Ingle

Dr. Narendra N. Bawane

KSP Road, PALVANCHA,
Bhadraji Kothagudem Dist.



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : *T. Rambabu*
- Designation : *Assistant professor*
- Department : *S & H*
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: *Accelerating..... clean..... green..... and..... sustainable.....*
..... *Innovation in India*
- Duration of the Program : *15th May - 15th June, 2023*
- Associating professional body : *Dr. D. J. Patil Institute of Technology*
- Financial support particulars (Rs.) :
 - Registration Charges : *1000/-*
 - Travelling Allowance : *-*
 - Membership Fee : *-*
 - Others : *-*

Date: _____ Signature of the faculty member *Rambabu*

- Recommendations of the HoD: *[Signature]*
- Recommendations of the IQAC: *[Signature]*
- Recommendations of the Principal: *[Signature]*

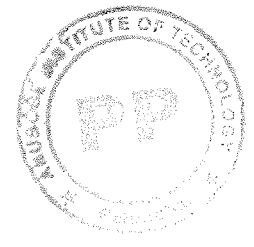
Sanctioned/Not Sanction

Account Department

Accountant: *[Signature]*

Date: 05/05/2023

[Signature]
PRINCIPAL
 Anubose Institute of Technology
 KSP Road, PALVANCHA,
 Bhadradri, Kothagudem Dist.
 Anubose Institute of Technology
 KSP Road, PALVANCHA,
 Bhadradri, Kothagudem Dist.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradri Kothagudem Dist.

V.No.....

Date: 30/04/2023

CASH VOUCHER

DEBIT.....

PARTICULARS	AMOUNT	
	Rs.	ps.
Accelerating clean, green and sustainability innovation in India T. Rambabu	1000/-	

Cashier

PRINCIPAL

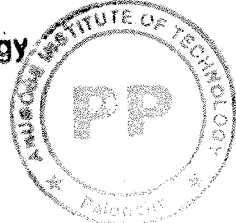
Anubose Institute of Technology

KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.

Principal

Received Rs. 1000 (Rupees One Thousand Rupees only) being.....

PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist



Rambabu
Signature

DPU

Dr. D. Y. Patil Institute of Technology, Pimpri, Pune
&
ASSOCHAM GEM Green Building Council, New Delhi

Faculty Development Program on
“Accelerating Clean, Green and Sustainable Innovation in India”
Certificate of Participation

T. Rambabu

This certificate is awarded to
of

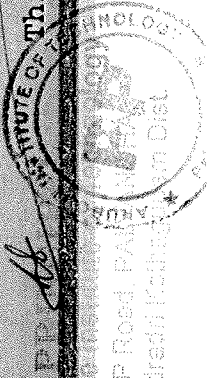
Anubose Institute of Technology

in recognition to attending the FDP on “Accelerating Clean, Green and Sustainable Innovation In India” organized by
Department of Electrical Engineering and Department of Instrumentation Engineering of Dr. D. Y. Patil Institute of
Technology, Pimpri, Pune in collaboration with ASSOCHAM GEM Green Building Council, New Delhi, during 7th

May 2023 to 14th June 2023

FDP0505180610

This is ~~anubose institute of technology~~ certificate and does not require signature
Principal
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadraeri Kothagudem Dist.



KSP Road, PALVANCHA,
Bhadraeri Kothagudem Dist.



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : SK: Mybelli.....
- Designation : Assistant professor.....
- Department : E.E.E.....
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: One week Faculty Development programme.....
..... on Electric vehicle.....
- Duration of the Program : 11th to 16th May, 2023.....
- Associating professional body : J. K. Institute of Technology.....
- Financial support particulars (Rs.) :
i. Registration Charges : 1000/-.....
ii. Travelling Allowance : —.....
iii. Membership Fee : —.....
iv. Others : —.....

Date: _____ Signature of the faculty member

- Recommendations of the HoD: _____
- Recommendations of the IQAC: _____
- Recommendations of the Principal: _____

Sanctioned/Not Sanctioned

Account Department

Accountant: _____

Date: 10/05/2022

PRINCIPAL
Anubose Institute of Technology
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PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradri Kothagudem Dist.

V.No.....

Date: 04/05/2023

CASH VOUCHER

DEBIT.....

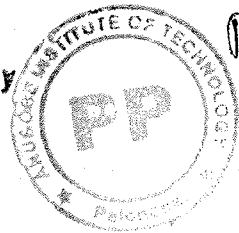
PARTICULARS	AMOUNT	
	Rs.	ps.
One week faculty development program on electric vehicle	1000/-	
Sk. Mybelli		

Cashier

Principal

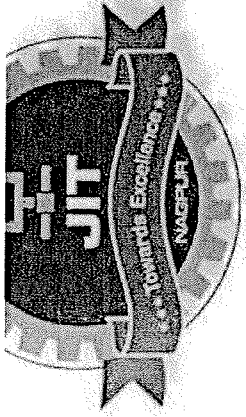
Received Rs. 1000 (Rupees One Thousand) Rupees only being

PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.



Mybelli
Signature

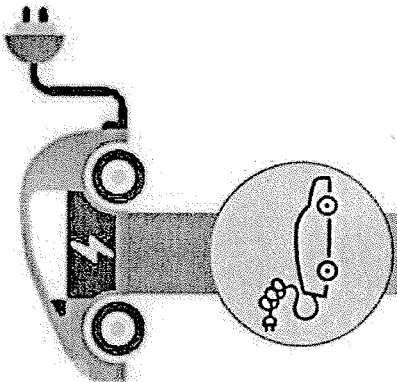
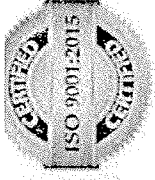
PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.



Jhulelal Institute Of Technology

Approved by AICTE-Recognized by DTE and Affiliated to JNM Nagpur University

Department of Electrical Engineering



CERTIFICATE OF PARTICIPATION

This certificate is presented to

Sk. Mybelli

for attending One Week Faculty Development Programme on "Electric Vehicle" organised by Department of Electrical Engineering, Jhulelal Institute of Technology, Nagpur-441111 in association with CADD Center Training Services and Vishvakariya Automotive Pvt. Ltd. Nagpur, held on 11th - 16th May 2023

Sejal

PRINCIPAL

Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhaeradri Kothagudem

Prof. Rohan M. Ingle

Dr. Narendra N. Bawane



Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhaeradri Kothagudem



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : *G. S. Radhulatha*
- Designation : *Assistant professor*
- Department : *CIVIL*
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: *Short term training programme through I.T mode*
..... *on modelling, Analysis and Design of structure using*
..... *latest version of STAAD*
- Duration of the Program : *25/07/2023 to 31/07/2023*
- Associating professional body : *National Institute of Technical Teachers*
..... *Training*
- Financial support particulars (Rs.) :
i. Registration Charges : *1000/-*
- ii. Travelling Allowance : *-*
- iii. Membership Fee : *-*
- iv. Others : *-*

Date: _____ Signature of the faculty member *Radhulatha*

- Recommendations of the HoD: *Radhulatha*
- Recommendations of the IQAC: *Radhulatha*
- Recommendations of the Principal: *Radhulatha*

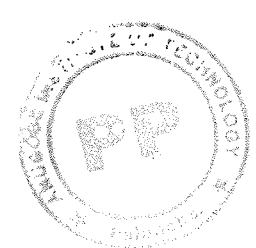
Sanctioned/Not Sanctioned

Account Department

Accountant: *Radhulatha*

Date: *23/07/2023*

Radhulatha
PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

V.No.....

Date: 18/07/2023

CASH VOUCHER

DEBIT.....

PARTICULARS	AMOUNT Rs. ps.
Short-term training program through ICT mode on modelling, analysis & design of structures using latest version of STAAD G. Madhulatha	1000/-

Cashier

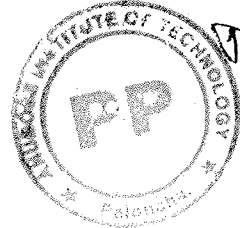
Principal

Principal

Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.

Received Rs. 1000 (Rupees One Thousand rupees only) being.....

PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.



Signature

Madhulatha

Certificate

Awarded to
G. Madhulatha

for having successfully completed the
Short Term Training Programme through ICT Mode on

Modelling, Analysis and Design of Structures using latest version of STAAD

organised by this institute

from 25/07/2023 to 31/07/2023 [duration: 1 Week]

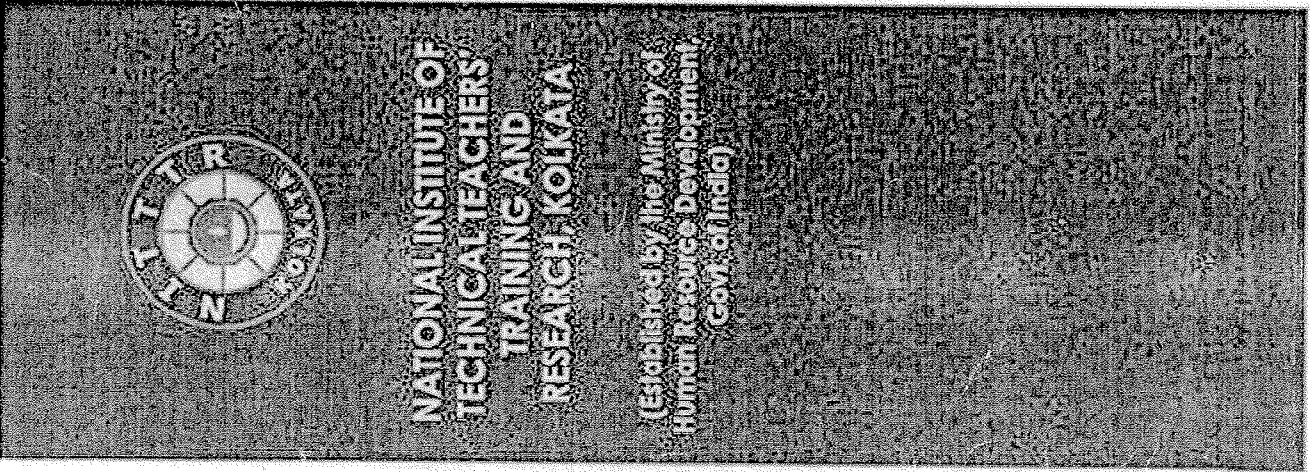
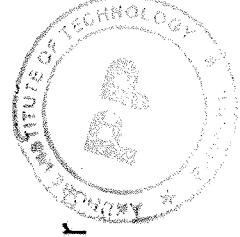
S. Srinivas

PRINCIPAL
Anubose Institute of Technology
PALVANCHA, KOTHAGUDEM DISTRICT

Programme Coordinator (s) *Academic Coordinator*
Anubose Institute of Technology
KSP Road, PALVANCHA,
Kothagudem Dist.

Ajshmr

Director





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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,
Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : M.D. Gausiya Begum
- Designation : Assistant professor
- Department : F.E.E
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: ... one week Faculty Development programme on
..... U Electric vehicle
- Duration of the Program : 11th - 16th May 2023
- Associating professional body : Arsam Science & Technology University
- Financial support particulars (Rs.) :
i. Registration Charges : 1000/-
ii. Travelling Allowance : -
iii. Membership Fee : -
iv. Others : -

Date: _____ Signature of the faculty member *Gausiya*

- Recommendations of the HoD: *Ashub*
- Recommendations of the IQAC: *Nand*
- Recommendations of the Principal: *[Signature]*

Sanctioned/Not Sanctioned

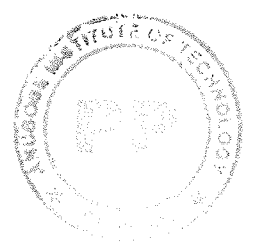
Accountant: *[Signature]*

Date: 09/05/2023

Account Department

[Signature] PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.

[Signature] PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

Date: 04/05/2023

V.No.....

CASH VOUCHER

DEBIT.....

PARTICULARS	AMOUNT	
	Rs.	ps.
One week faculty development program on electric vehicle	1000/-	
MD. Gousiya Begum		

Cashier

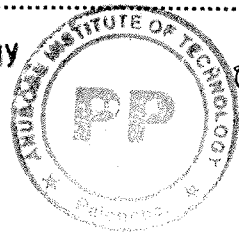
Principal

Received Rs. 1000 (Rupees. One Thousand

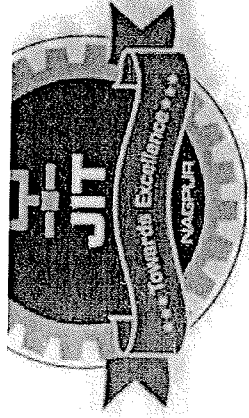
rupees only

being.....

PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist



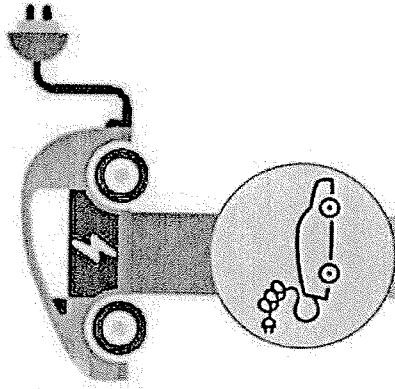
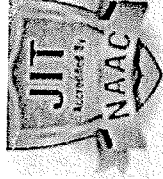
Gousiya
Signature



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Approved by AICTE, Recognized by DTE and Affiliated to RTM Nagpur University

Department of Electrical Engineering

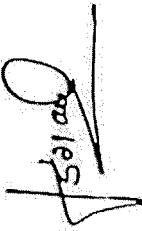



CERTIFICATE OF PARTICIPATION

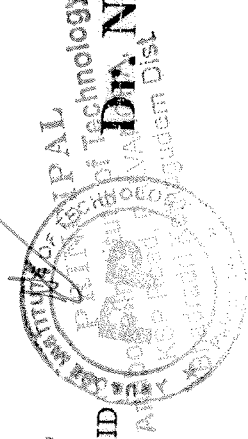
This certificate is presented to

MD. Gousiya Begum

for attending One Week Faculty Development Programme on "Electric Vehicle" organised by Department of Electrical Engineering, Jhulelal Institute of Technology, Nagpur-441111 in association with CADD Center Training Services and Vishvakariya Automotive Pvt. Ltd. Nagpur, held on 11th - 16th May 2023


Prof. Rohan M. Ingle


PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
#hadraadi Kothagudem, Certificate ID



Dr. Narendra N. Bawane

Dr. Narendra N. Bawane



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : R. Sekhes. Babu
- Designation : Assistant professor
- Department : EEE
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: One week Faculty Development programme on
..... Electric vehicle
- Duration of the Program : 11th - 16th may, 2023
- Associating professional body : Jhulelal Institute of Technology
- Financial support particulars (Rs.) :
i. Registration Charges : 1000/-
ii. Travelling Allowance :
iii. Membership Fee :
iv. Others :

Date: _____ Signature of the faculty member

- Recommendations of the HoD: Aphok
- Recommendations of the IQAC: Neel
- Recommendations of the Principal: [Signature]

Sanctioned/Not Sanctioned

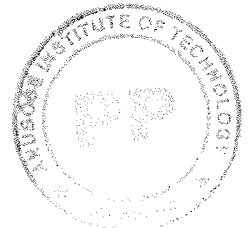
Account Department

Accountant: [Signature]

Date: 10/05/2023

PRINCIPAL PRINCIPAL

Anubose Institute of Technology Anubose Institute of Technology
KSP Road, PALVANCHA KSP Road, PALVANCHA;
Bhadradri Kothagudem Dist. Bhadradri Kothagudem Dist.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P. Road, NEW PALONCHA-507 115,
Bhadradri Kothagudem Dist.

V.No.....

Date: 04/05/2023

CASH VOUCHER

DEBIT.....

PARTICULARS	AMOUNT	
	Rs.	ps.
One week faculty development program on electric vehicle R- sekhar babu	1000/-	

Cashier

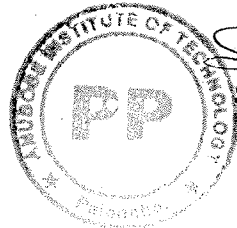
Principal

Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.

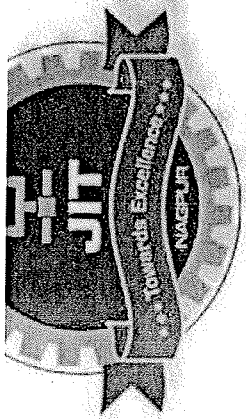
Principal

Received Rs. 1000 (Rupees One Thousand rupees only) being.....

Principal
Anubose Institute of Technology
KSP Road, PALVANCHA,
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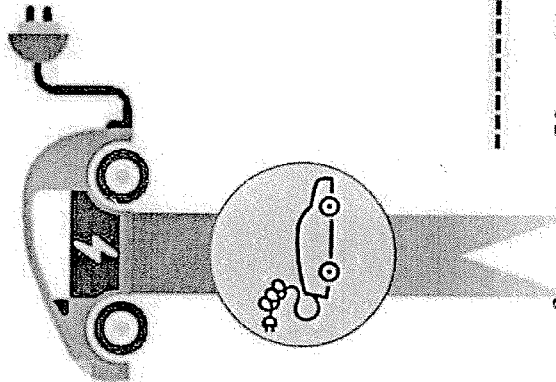
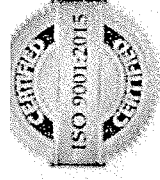
Sekhar
Signature



Jhulelal Institute Of Technology

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Department of Electrical Engineering



CERTIFICATE OF PARTICIPATION

This certificate is presented to

R. Sekhar Babu

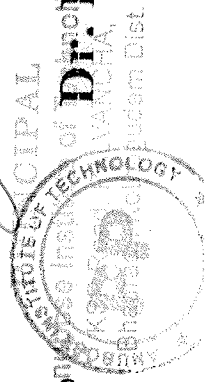
for attending One Week Faculty Development Programme on "Electric Vehicle" organised by Department of Electrical Engineering, Jhulelal Institute of Technology, Nagpur-441111 in association with CADD Center Training Services and Vishvakariya Automotive Pvt. Ltd. Nagpur, held on

11th - 16th May 2023

[Signature]

Prof. Rohan M. Ingle

[Signature]
PRINCIPAL
Anubese Institute of Technology
KSP Road, PALVANCHA,
Bhadraari, Kothagudem Dist.



Dr. Narendra N. Bawane

[Signature]



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Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : ... *N. Venkata. Narasimha. Prabath*
- Designation : *Assistant professor*
- Department : *Civil*
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details: ... *Short term training programme through I.C.T. mode on modelling, Analysis & Design of structures using latest version of STAAD*
- Duration of the Program : ... *25/07/2023 to 31/07/2023*
- Associating professional body : ... *North Eastern Regional Institute of Science & Technology*
- Financial support particulars (Rs.) :
 - Registration Charges : *1000/-*
 - Travelling Allowance : *-*
 - Membership Fee : *-*
 - Others : *-*

Date: _____ Signature of the faculty member *[Signature]*

1. Recommendations of the HoD: *[Signature]*

2. Recommendations of the IQAC: *[Signature]*

3. Recommendations of the Principal: *[Signature]*

Sanctioned/Not Sanctioned

Account Department

Accountant: *[Signature]*

Date: *24/07/2023*

[Signature]
PRINCIPAL
 Anubose Institute of Technology
 KSP Road, PALVANCHA,
 Bhadradi Kothagudem Dist



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P.Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

Date: 18/07/2023

V.No.....

CASH VOUCHER

DEBIT.....

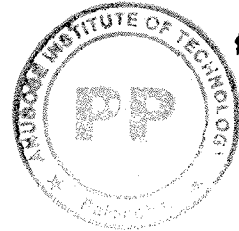
PARTICULARS	AMOUNT	
	Rs.	ps.
short term training program through ICT mode on modelling, analysis & design of structures using latest version of STAAD N. Venkata Narasimha Prabhath	1000/-	

Cashier

Principal

Received Rs. 1000 (Rupees One Thousand rupees only) being.....

PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist



N.V.N. Prabhath
Signature

ANUBOSE INSTITUTE OF TECHNOLOGY
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.

Certificate

Awarded to
N. Venkata Naxasimha Prabath

for having successfully completed the
Short Term Training Programme through ICT Mode on

Modelling, Analysis and Design of Structures using latest version of STAAD

organised by this institute
from 25/07/2023 to 31/07/2023 [duration: 1 Week]

Edhang

Programme Coordinator (s)

Academis Coordinator

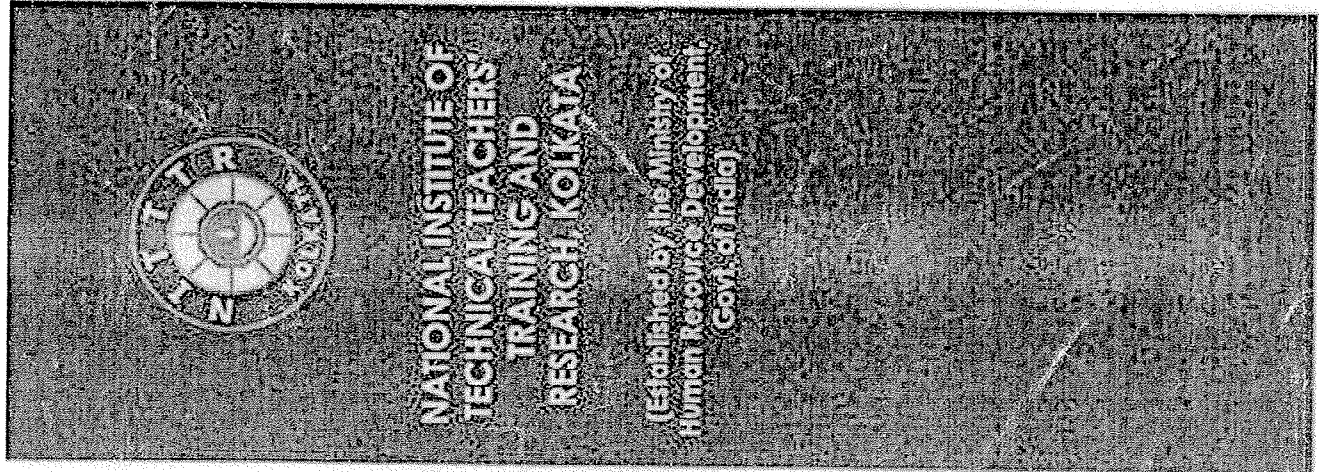
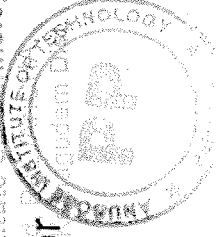
Anubase Institute of Technology
KSP Road, PALVANCHA,
Bhadradri Kothagudem Dist.

[Signature]

PRINCIPAL

Anubase Institute of Technology

Director





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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : B. Laxman... stark
- Designation : Assistant... professor
- Department : P.E.E.
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
 Details: Short... term... training... programme... through... D.C.T.....
... mode... on... modelling... Analysis... &... Design... of... structures... using...
..... latest... version... of... S.T.A.A.D.....
- Duration of the Program : 25/07/2023... to... 31/07/2023.....
- Associating professional body : National... Institute... of... Technical... Teachers... &... Training
- Financial support particulars (Rs.) :
 i. Registration Charges : 1000/-
- ii. Travelling Allowance : —
- iii. Membership Fee : —
- iv. Others : —

Date: _____ Signature of the faculty member chey

- Recommendations of the HoD: Ashok
- Recommendations of the IQAC: Naidu
- Recommendations of the Principal: [Signature]

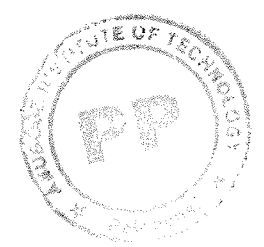
Sanctioned/Not Sanctioned

Account Department

Accountant: [Signature]

Date: 24/07/2023

[Signature]
 PRINCIPAL
 Anubose Institute of Technology
 ANUBOSE INSTITUTE OF TECHNOLOGY
 KSP Road, Paloncha, Bhadradi, Kothagudem Dist.,
 Bhadradi Kothagudem Dt, Telangana.



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Bhadradri Kothagudem Dist.

Date: 18/07/2023

V.No.....

CASH VOUCHER

DEBIT.....

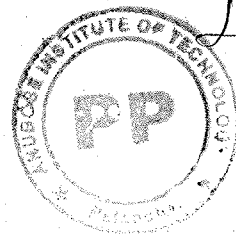
PARTICULARS	AMOUNT	
	Rs.	ps.
Short term training program through ICT mode on modelling, analysis and design of structures using latest version of STAAD B. Laxman Naik	1000/-	

Cashier

Principal

Received Rs. 1000 (Rupees One Thousand) rupees only being.....

ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadradri Kothagudem Dt, Telangana.



Laxman Naik
Signature

Certificate

Awarded to
B. Karman Naik

for having successfully completed the
Short Term Training Programme through ICT Mode on

Modelling, Analysis and Design of Structures using latest version of STAAD

organised by this institute

from 25/07/2023 to 31/07/2023 [duration: 1 Week]

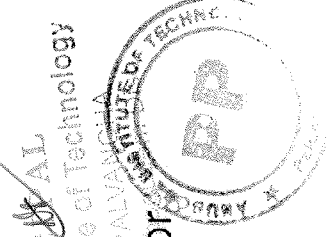
Edla...

Programme Coordinator (s)

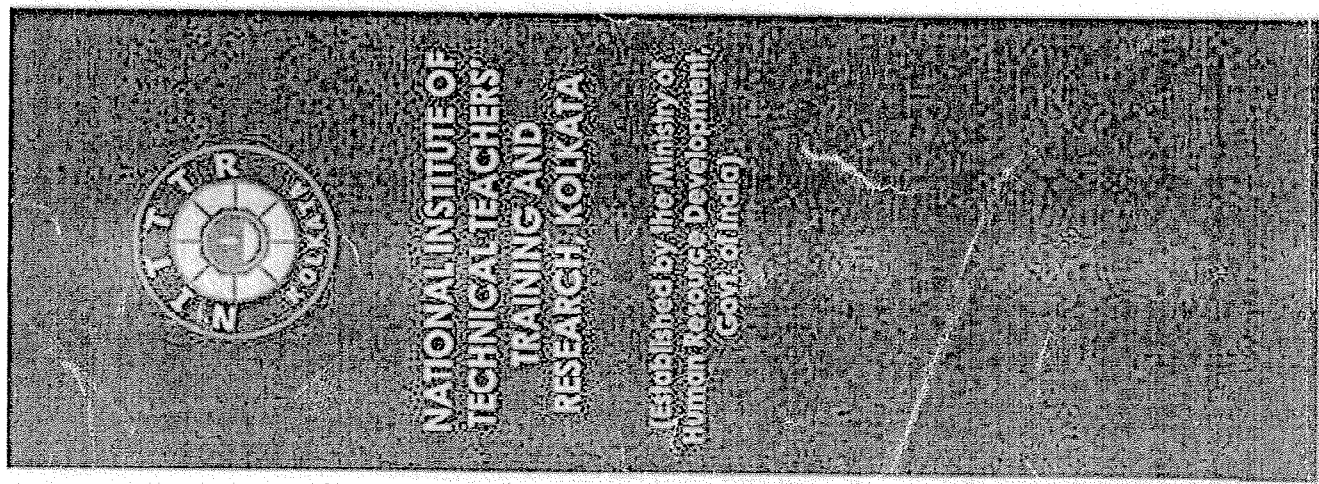
... Academic Coordinator

Director

...



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : *L. Prashanth Kumar*
- Designation : *Assistant Prof. Festos*
- Department : *M.I.N.E*
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: *Accelerating... Clean... Green... and... Sustainable... Innovation...
in... India*
- Duration of the Program : *4th May - 14th June, 2023*
- Associating professional body : *Dr. D. V. Patil Institute of Technology*
- Financial support particulars (Rs.) :
 - Registration Charges : *1000/-*
 - Travelling Allowance : *-*
 - Membership Fee : *-*
 - Others : *-*

Date: _____ Signature of the faculty member *Ranjit*

- Recommendations of the HoD: _____ *Ranjit*
- Recommendations of the IQAC: _____ *Ranjit*
- Recommendations of the Principal: _____ *Ranjit*

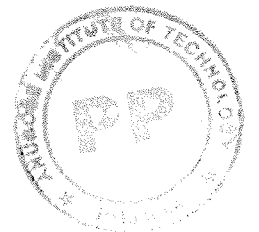
Sanctioned/Not Sanctioned

Account Department

Accountant: _____ *(Signature)*

Date: *06/05/2023*

(Signature) PRINCIPAL
 Anubose Institute of Technology
 KSP Road, PALONCHA INSTITUTE OF TECHNOLOGY
 Bhadradri Kothagudem Dist, P. Road, Paloncha,
 Bhadradri Kothagudem Dt, Telangana.



ANU BOSE INSTITUTE OF TECHNOLOGY

K.S.P. Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

Date: 30/04/2023

V.No.....

CASH VOUCHER

DEBIT.....	PARTICULARS	AMOUNT	
		Rs.	ps.
	Accelerating clean, green & sustainable innovation in India	1000/-	
	L. Prashanth kumar		

Cashier

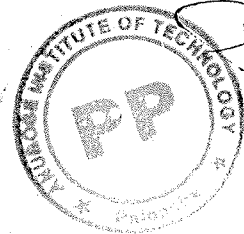
Principal

Received Rs. 1000 (Rupees One Thousand)

rupees only

being.....

PRINCIPAL
ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Paloncha,
Bhadradi Kothagudem Dt, Telangana



Signature

Prashanth

ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road - PALONCHA,
Bhadradi Kothagudem Dist.
PRINCIPAL

DPU

Dr. D. Y. Patil Institute of Technology, Pimpri, Pune

&

ASSOCHAM GEM Green Building Council, New Delhi

**Faculty Development Program on
“Accelerating Clean, Green and Sustainable Innovation in India”**

Certificate of Participation

h. Prashanth Kumar

This certificate is awarded to

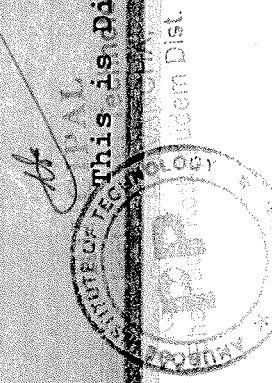
of

Anubose Institute of Technology

in recognition to attending the FDP on “*Accelerating Clean, Green and Sustainable Innovation In India*” organized by Department of Electrical Engineering and Department of Instrumentation Engineering of Dr. D. Y. Patil Institute of Technology, Pimpri, Pune in collaboration with ASSOCHAM GEM Green Building Council, New Delhi, during 7th

May 2023 to 14th June 2023

FDP0505180610



This is Digital Certificate and does not require signature

ANUBOSE INSTITUTE OF TECHNOLOGY
PRINCIPAL
AMBAKSI, Pimpri Road, Pimpri, Pune
Shadrathi Kothari



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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : P. Rama Anivasa Rao
- Designation : Assistant professor
- Department : S.H.
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: Accelerating Clean, Green and Sustainable Innovation
..... In India
- Duration of the Program : 7th May - 14th June, 2023
- Associating professional body : Dr. D.Y. Patil Institute of Technology
- Financial support particulars (Rs.) :
 - Registration Charges : 1000/-
 - Travelling Allowance : -
 - Membership Fee : -
 - Others : -

Date: _____ Signature of the faculty member

- Recommendations of the HoD:
- Recommendations of the IQAC:
- Recommendations of the Principal:

Sanctioned/Not Sanctioned

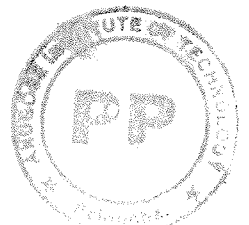
Account Department

Accountant:

Date: 06/05/2023

PRINCIPAL PRINCIPAL

ANUBOSE INSTITUTE OF TECHNOLOGY
KSP Road, PALONCHA, Bhadradi, Kothagudem Dt, Telangana.



ANU BOSE INSTITUTE OF TECHNOLOGY

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Bhadradi Kothagudem Dist.

V.No.....

Date: 30/04/2023

CASH VOUCHER

DEBIT

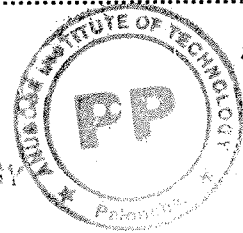
PARTICULARS	AMOUNT	
	Rs.	ps.
Accelerating clean, green and sustainable innovation in India	1000/-	
P. Rama Srinivasa Rao		

Cashier

Principal

Received Rs. 1000 (Rupees One Thousand rupees only) being.....

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ANU BOSE INSTITUTE OF TECHNOLOGY
K.S.P. Road, Palvoncha,
Bhadradi Kothagudem Dt, Telangana.

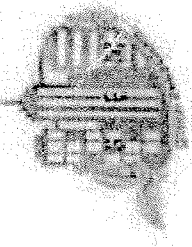


R&R
Signature

PRINCIPAL
Anu Bose Institute of Technology
K.S.P. Road, PALONCHA,
Bhadradi Kothagudem Dist.

DPU

Dr. D. Y. Patil Institute of Technology, Pimpri, Pune
&
ASSOCHAM GEM Green Building Council, New Delhi



Faculty Development Program on
“Accelerating Clean, Green and Sustainable Innovation in India”

Certificate of Participation

P. Ramasrinivasa Rao

This certificate is awarded to
of

Anubose Institute of Technology

in recognition to attending the FDP on “*Accelerating Clean, Green and Sustainable Innovation In India*” organized by Department of Electrical Engineering and Department of Instrumentation Engineering of Dr. D. Y. Patil Institute of Technology, Pimpri, Pune in collaboration with ASSOCHAM GEM Green Building Council, New Delhi, during 7th

May 2023 to 14th June 2023

FDP0505180610

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Telangana State. www.abit.ac.in, Email: abitjntu@gmail.com,

Cell: +91-9246907407

CODE	College	Counselling
	PP	BOSE

Financial Support Request Letter

- Name of the faculty member : *G. Deepthi*
- Designation : *Assistant Professor*
- Department : *ECE*
- Conference/Publication/ Membership Fee/ Workshop /FDP Certificate
Details: *cross talk... analysis... of... dielectric... inserted... side... coated... multilay...
... er... graphene... consist... inter... connect... for... ternary... logic...
... System... using... unconditionality... stable... FDP model*
- Duration of the Program :
- Associating professional body :
- Financial support particulars (Rs.) :

 - Registration Charges : *1000/-*
 - Travelling Allowance :
 - Membership Fee :
 - Others :

Date:

Deepthi
Signature of the faculty member

- Recommendations of the HoD: *[Signature]*
- Recommendations of the IQAC: *[Signature]*
- Recommendations of the Principal: *[Signature]*

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Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist.

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Account Department

Accountant: *[Signature]*

Date: *01/01/2023*

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K.S.P.Road, NEW PALONCHA-507 115,
Bhadradi Kothagudem Dist.

Date: 01/01/2023

V.No.....

CASH VOUCHER

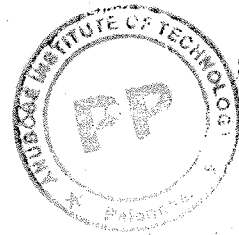
DEBIT.....	PARTICULARS	AMOUNT	
		Rs.	ps.
	Crosstalk analysis of dielectric inserted side contact multilayer graphene nanoribbon interconnect for ternary logic system using unconditional stability FDTD model Gurujala Deepthi	1000/-	

Cashier

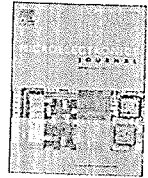
Principal

Received Rs. 1000 (Rupees One Thousand rupees only) being.....

PRINCIPAL
Anubose Institute of Technology
KSP Road, PALVANCHA,
Bhadradi Kothagudem Dist



Deepthi
Signature



Crosstalk analysis of dielectric inserted side contact multilayer graphene nanoribbon interconnects for ternary logic system using unconditionally stable FDTD model

Gurijala Deepthi^{*}, Madhavi Tatineni

Department of Electronics and Communication Engineering, GITAM Deemed to be University, Visakhapatnam, 530045, India

ABSTRACT

In the present work, for examining the performance of dielectric inserted side contact multilayer graphene nano ribbon (DSMLGNR), a novel Unconditionally Stable Finite-Difference Time-Domain (USFDTD) technique is proposed. This technique overcomes the constraint of Courant stability condition prevailing in FDTD technique. The proposed mathematical model is highly time efficient and accurately analyzes the crosstalk effects induced among the coupled interconnect lines. The robustness of the USFDTD technique is verified by determining various performance metrics and justifying it with FDTD technique and HSPICE simulations. The obtained results are in accordance with HSPICE and the average percentage error is less than 1%. For performing the transient analysis, USFDTD model consumes 25% lesser time compared to FDTD technique. Furthermore, accuracy of the proposed model is validated by using Feature Selective Validation (FSV) tool.

1. Introduction

The continuous scaling of technology has the advantage of reduced interconnect length by optimizing the density of components on the chip, however liability of confining the amount of information that has to be transmitted is very less. Over recent years' researchers have implemented binary logic for the design of digital circuitry where it is associated with two logic levels [1]. But by using the binary logic more space is occupied on the chip, only less amount of information can be transmitted and also intricacy arises in wire routing [2,3]. Therefore, at high-speed applications, interconnects not only have the impact of crosstalk effect but also faces problem in attaining high throughput. Hence, research is directed towards the ternary logic where plenty of data can be sent through a wire thereby reducing the number of interconnections and complexity among the ICs. Despite the performance of the ternary logic systems is effected by the significant reduction in the noise margin and the switching threshold voltage when compared to binary logic in counterpart [3]. Due to this the impact of crosstalk effects turned out to be more on ternary logic systems. An extensive research has been performed on ternary based logic interconnects [4–10].

As the chip size is decreasing, probability of failure in IC is also increasing to a greater extent. Electromigration is an insidious form of failure in ICs, which is emerging as a leading problem and need to be optimized in parallel with current density and timing. This can be accomplished by implementing various shielding techniques and by

carrying out the mathematical modelling. Shielding technique is the most effective and common technique to reduce crosstalk noise as well as delay uncertainty to keep the signal integrity efficiently. In Ref. [11], competency of passive shielding has been studied in bringing down the crosstalk effects among CNTFET based ternary circuits. Further the research has been focused towards the implementation of active shielding over ternary based MWCNT interconnect bundle to nullify the crosstalk induced failures [12,13]. In Ref. [14] a comparative analysis has been done by implementing both active and passive shielding techniques in mitigating crosstalk effects among MWCNT and MLGNR interconnects. Subsequently the work has been extended towards DS-MLGNR and Di-VGNR interconnects. And the performance of these interconnects are analyzed by implementing active shielding technique for ternary logic system [15,16]. Hence, it is observed that by incorporating active shielding technique, crosstalk effects can be mitigated to a greater extent compared to passive shielding.

In hunt of finding an alternate solution to silicon technology has propelled a great deal of research into non silicon materials. Among all the devices carbon nanotubes (CNTs) and graphene nanoribbons (GNRs) appear destined to open up a host of new revolution in nanometer industry. These exhibits greater mechanical strength, possess unique electrical properties and are efficient conductors of heat. The intrinsic properties of these materials becomes useful in solving the problems that are plaguing the metallic interconnects and proves to be an appropriate interconnect material for nano electronic devices [17–20]. However,

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